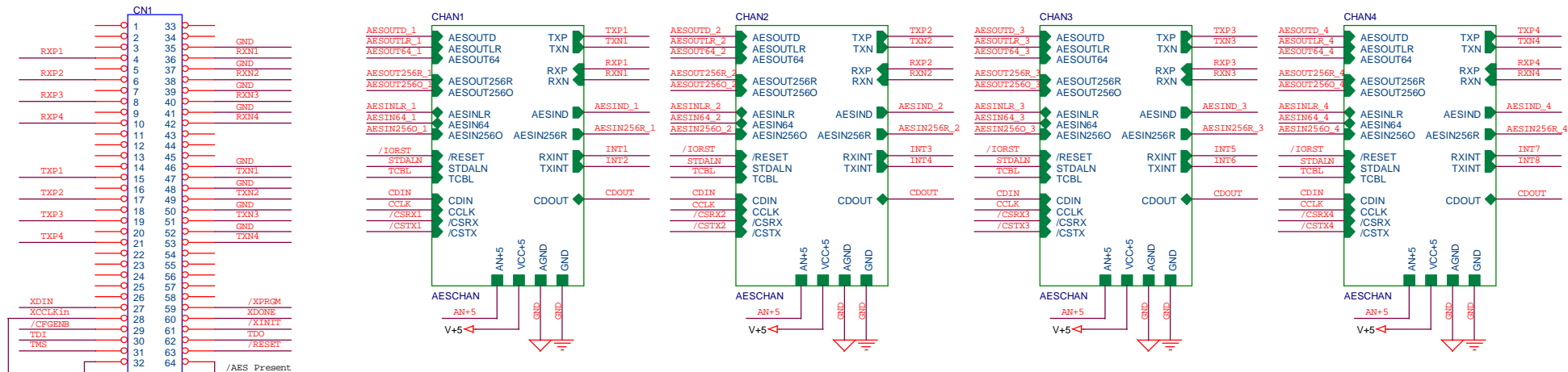
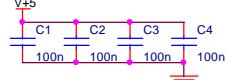
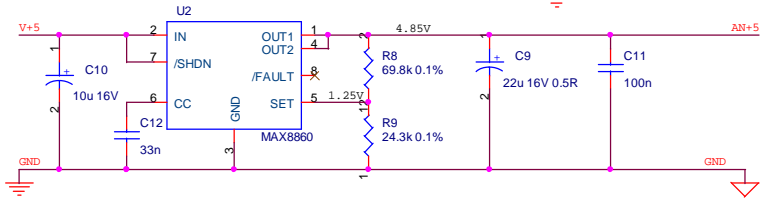
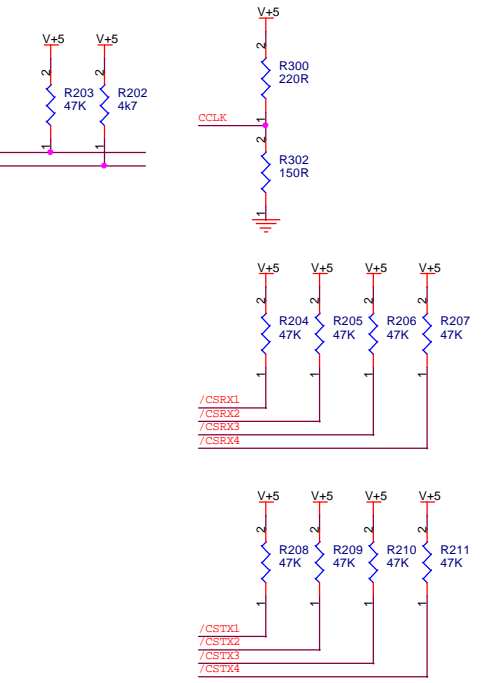
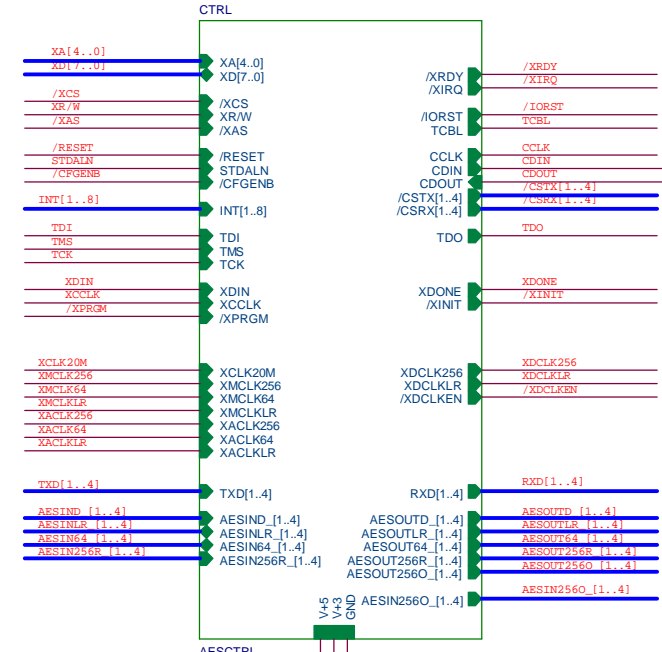
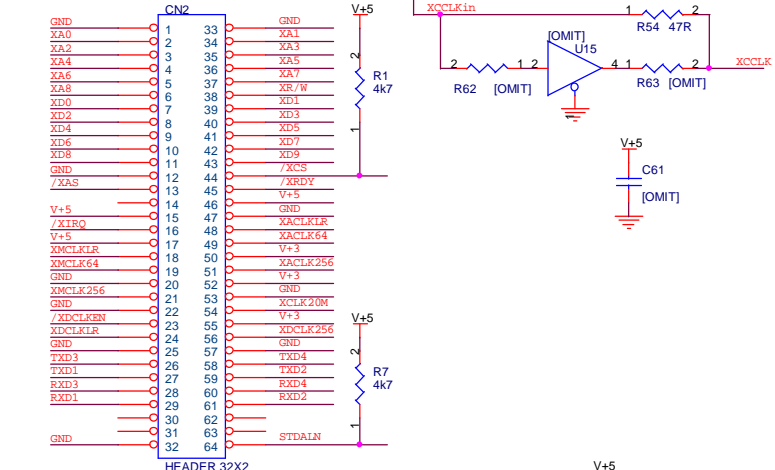
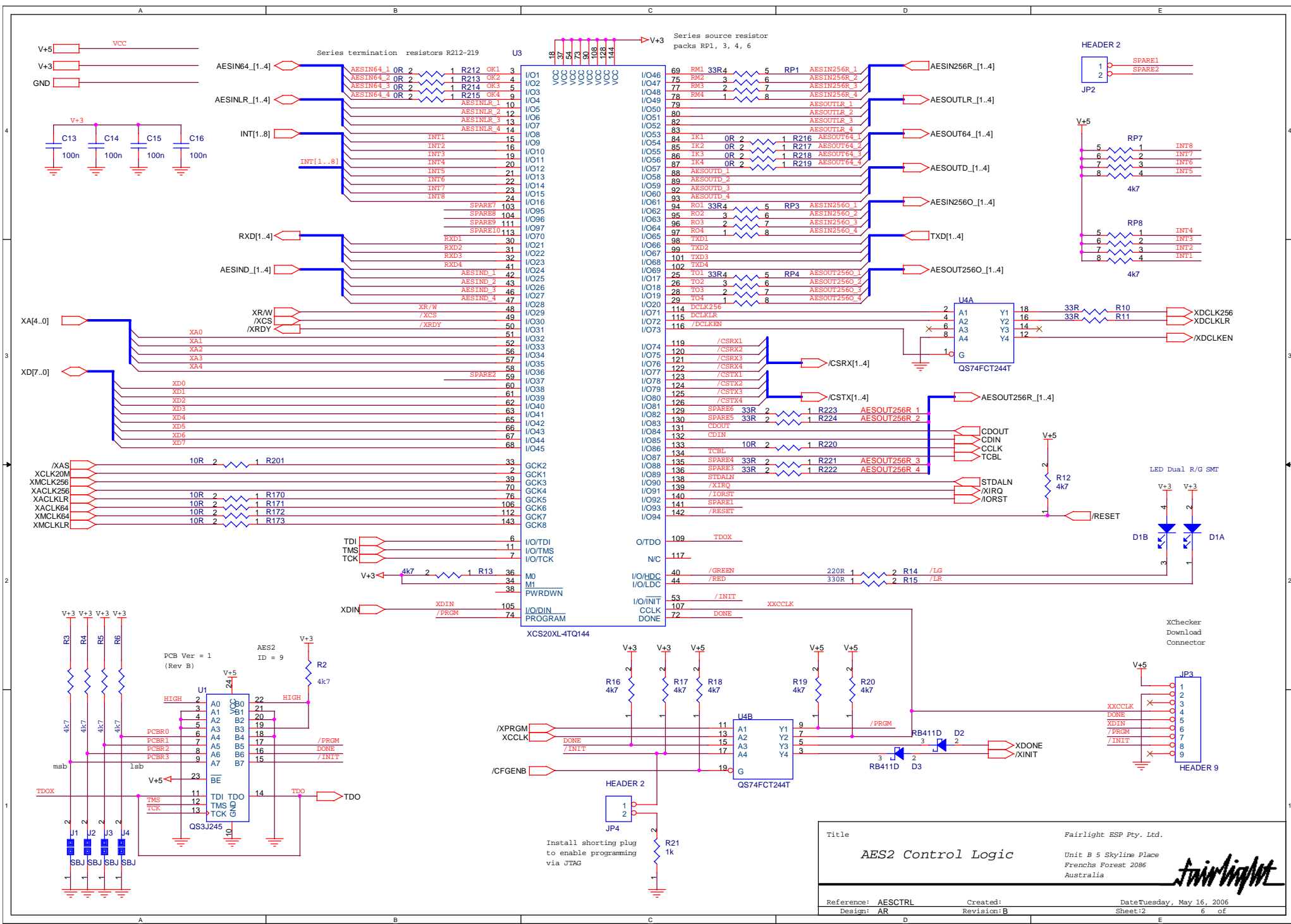


External I/O Connector



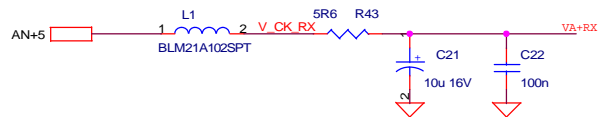
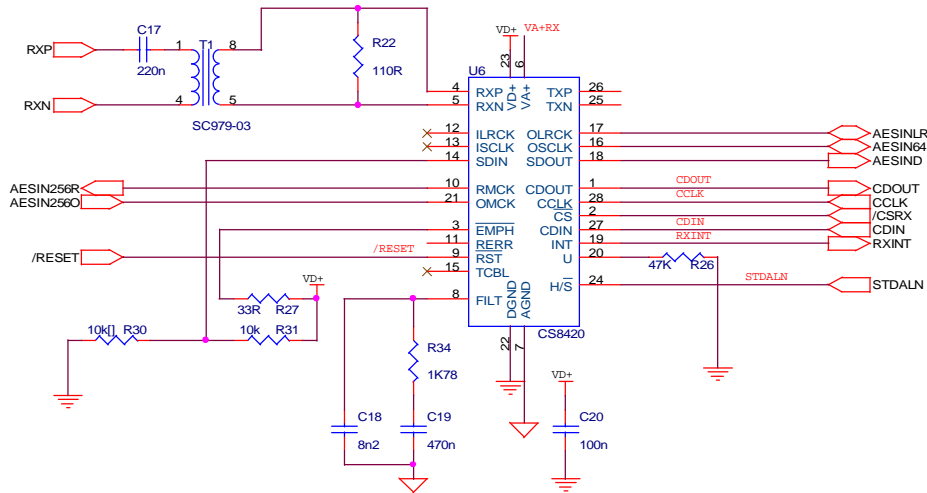
DSP I/O Connector



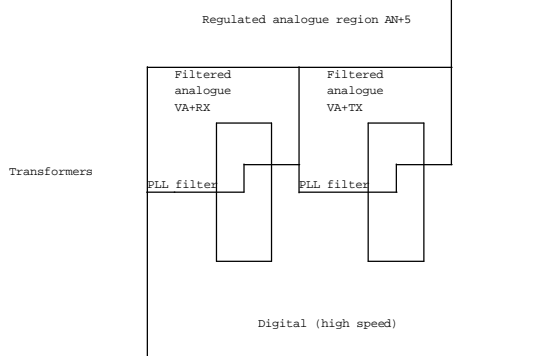


| | |
|---------------------------|--|
| Title | Fairlight ESP Pty. Ltd. |
| AES2 Control Logic | Unit B 5 Skyline Place Frenchs Forest 2086 Australia |
| Reference: AESCTRL | Created: Tuesday, May 16, 2006 |
| Design: AR | Revision: B |
| | Sheet: 2 of 6 |

AES Rx

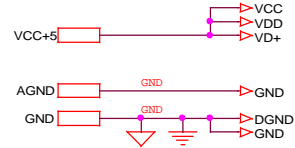


Layout of power/ground planes

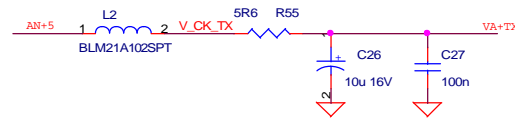
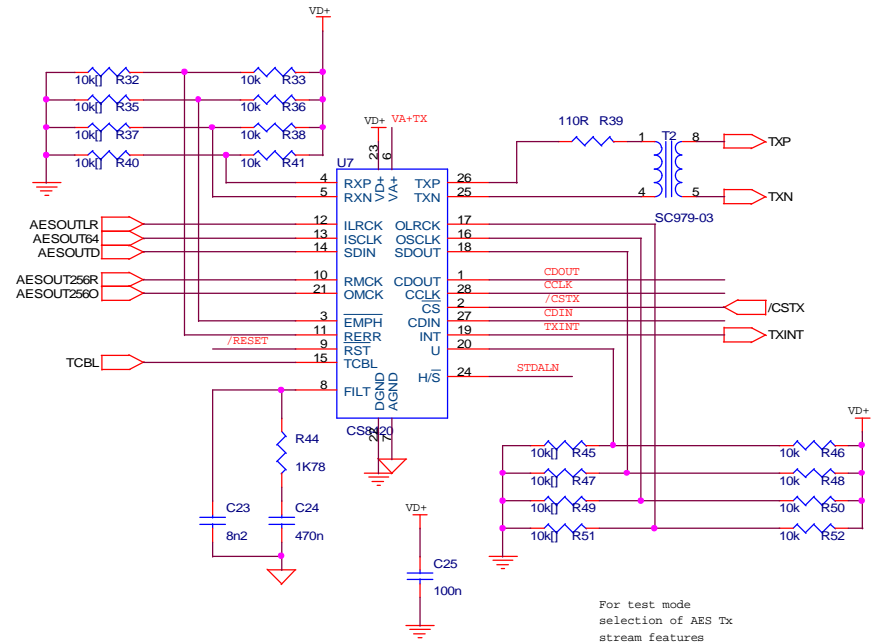


PCB Layers

- TOP
- GND/SIGNAL/3.3V
- GND
- INTERNAL SIGNAL
- INTERNAL SIGNAL
- GND
- 5V
- BOTTOM



AES Tx



For test mode
selection of AES Tx
stream features

Title

AES2 Stereo I/O Channel 1-2

Fairlight ESP Pty. Ltd.

Unit B 5 Skyline Place
Frenchs Forest 2086
Australia

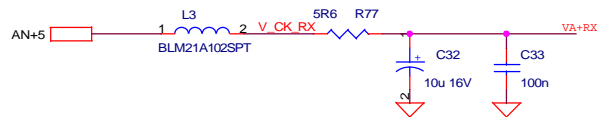
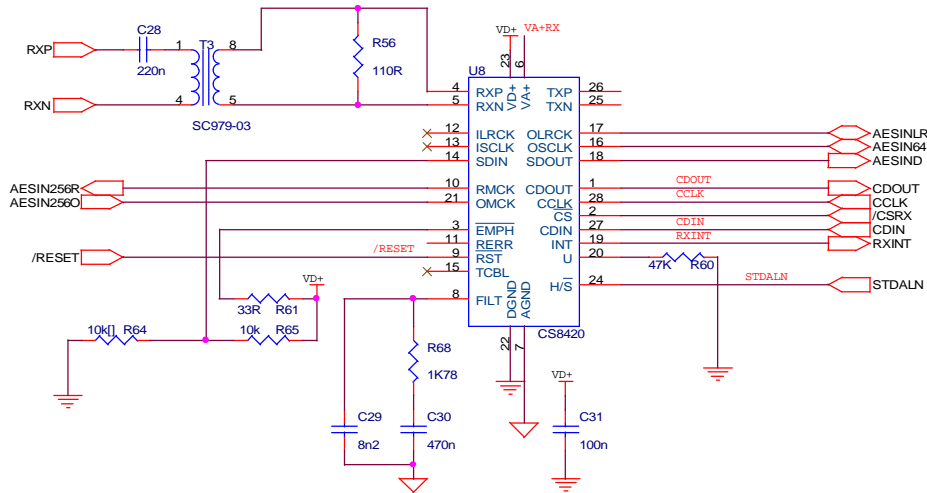


Reference: -
Design: AR

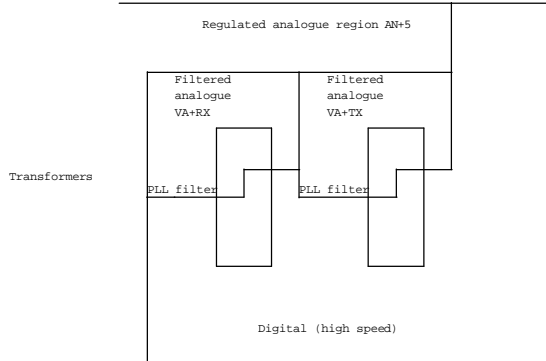
Created: Wednesday, February 18, 1998
Revision: B

Date: Monday, June 04, 2001
Sheet: 3 of 6

AES Rx

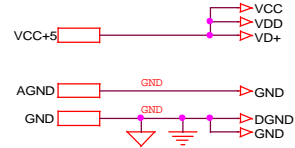


Layout of power/ground planes

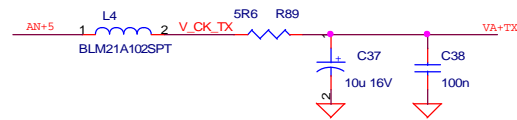
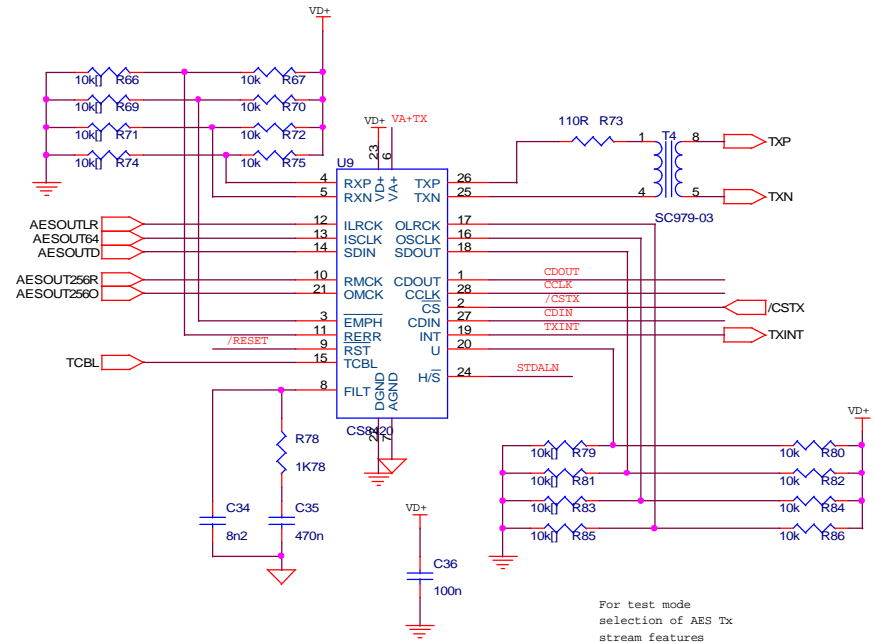


PCB Layers

- TOP
- GND/SIGNAL/3.3V
- GND
- INTERNAL SIGNAL
- INTERNAL SIGNAL
- GND
- 5V
- BOTTOM



AES Tx



For test mode
selection of AES Tx
stream features

Title

AES2 Stereo I/O Channel 3-4

Fairlight ESP Pty. Ltd.

Unit B 5 Skyline Place
Frenchs Forest 2086
Australia

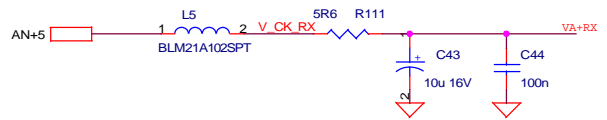
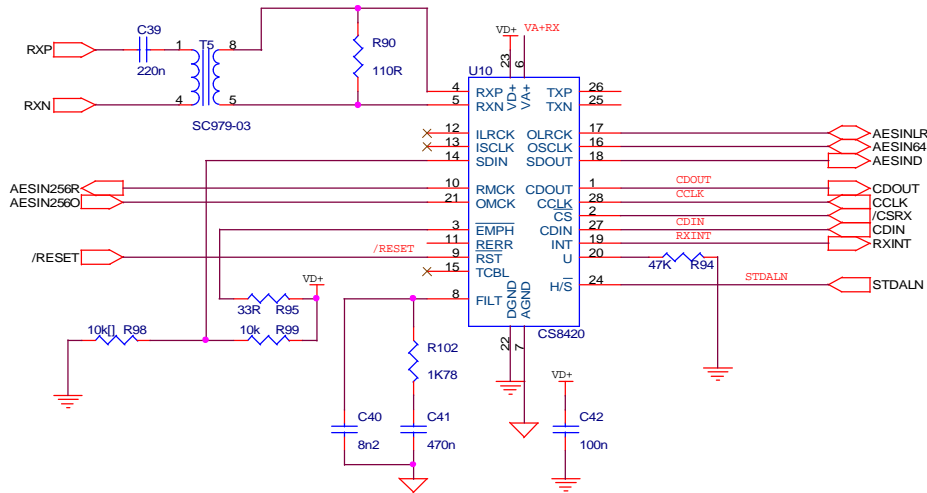


Reference: -
Design: AR

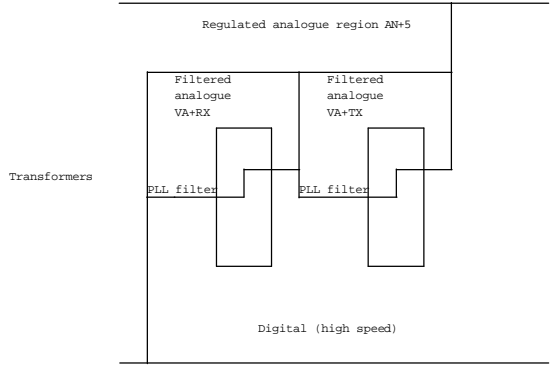
Created: Wednesday, February 18, 1998
Revision: B

Date: Monday, June 04, 2001
Sheet: 4 of 6

AES Rx

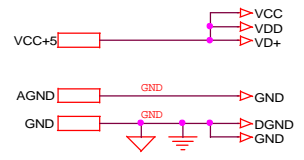


Layout of power/ground planes

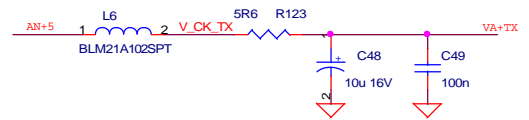
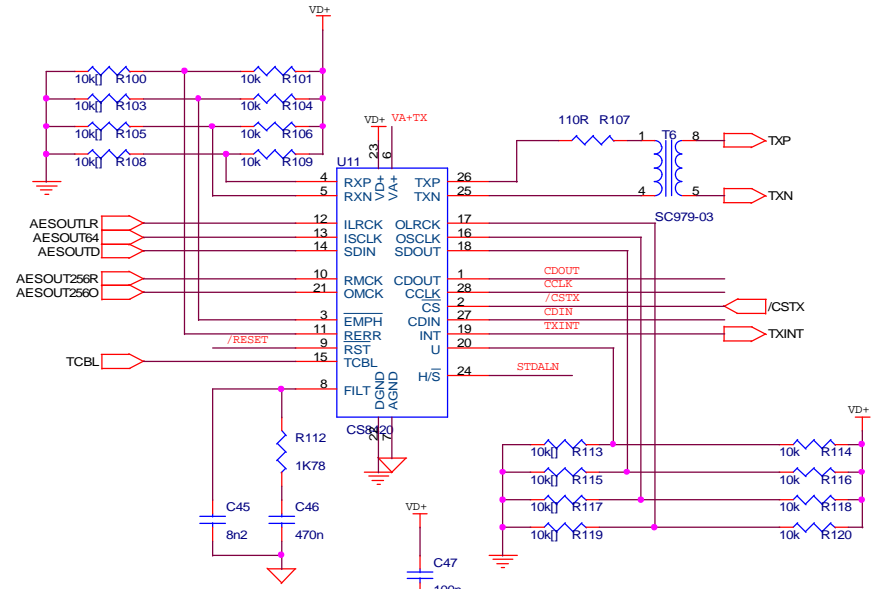


PCB Layers

- TOP
- GND/SIGNAL/3.3V
- GND
- INTERNAL SIGNAL
- INTERNAL SIGNAL
- GND
- 5V
- BOTTOM



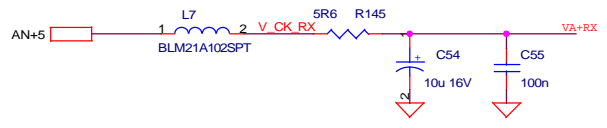
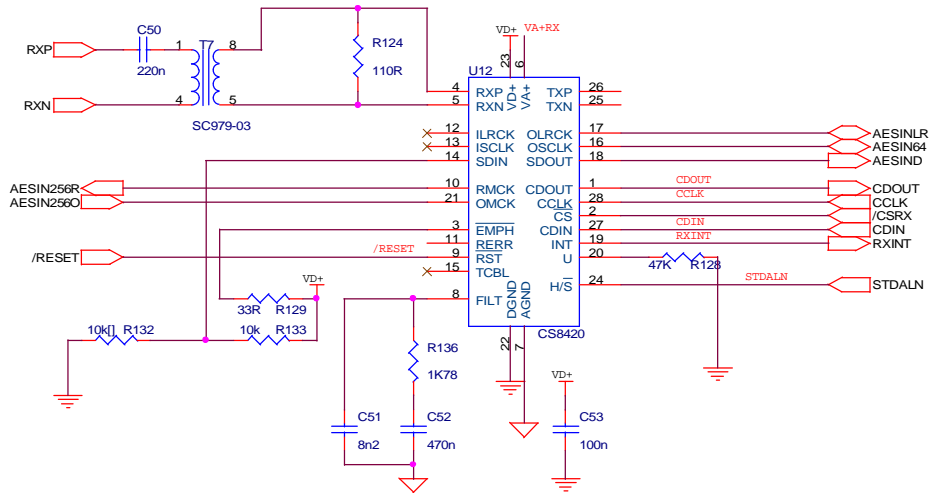
AES Tx



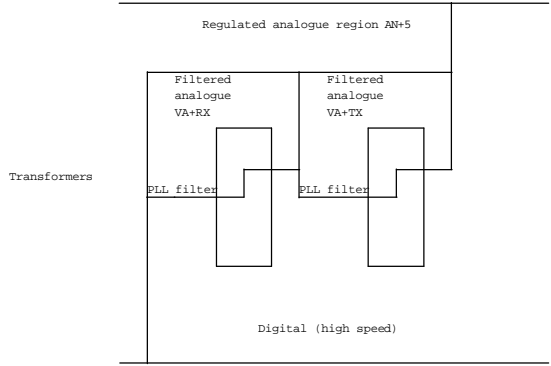
For test mode selection of AES Tx stream features

| | |
|--|---|
| <p>Title</p> <p style="text-align: center;">AES2 Stereo I/O Channel 5-6</p> <p>Reference: -</p> <p>Design: AR</p> | <p style="text-align: right;">Fairlight ESP Pty. Ltd.</p> <p style="text-align: right;">Unit B 5 Skyline Place Frenchs Forest 2086 Australia</p> <p style="text-align: right;"><i>Fairlight</i></p> <p style="text-align: right;">Created: Wednesday, February 18, 1998 Date: Monday, June 04, 2001</p> <p style="text-align: right;">Revision: B Sheet: 5 of 6</p> |
|--|---|

AES Rx

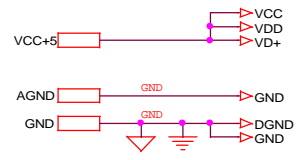


Layout of power/ground planes

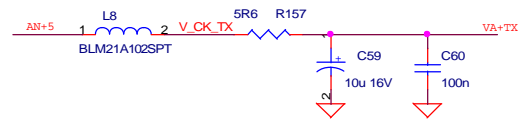
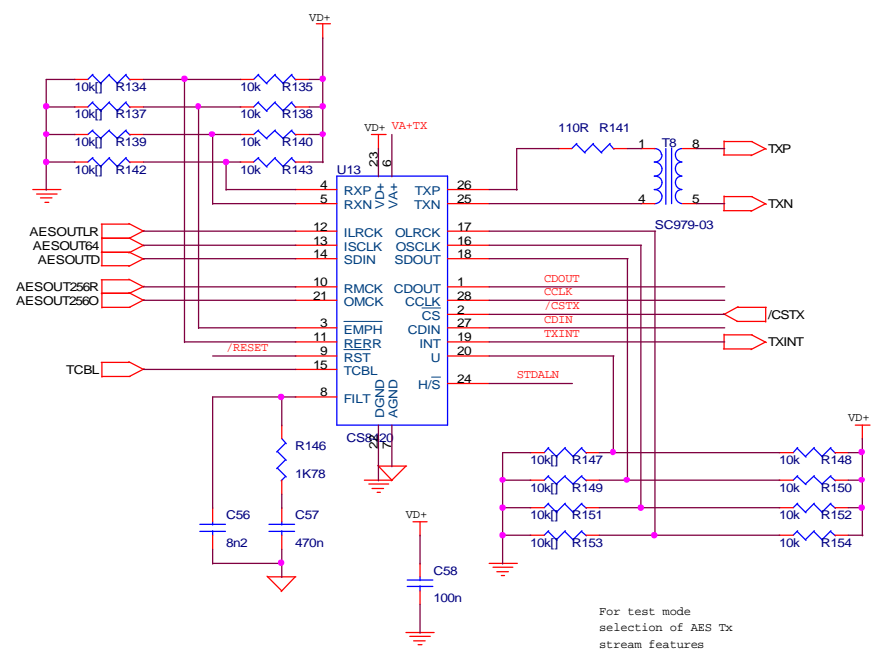


PCB Layers

- TOP
- GND/SIGNAL/3.3V
- GND
- INTERNAL SIGNAL
- INTERNAL SIGNAL
- GND
- 5V
- BOTTOM



AES Tx

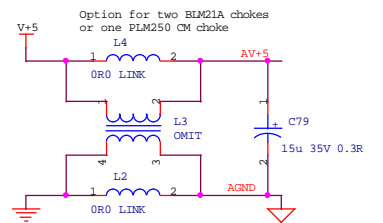
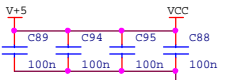
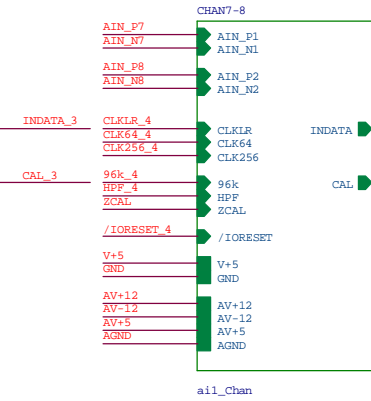
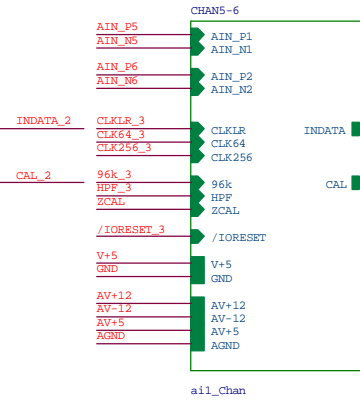
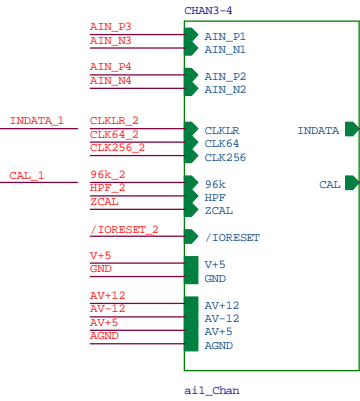
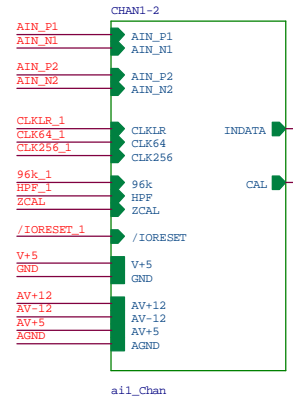
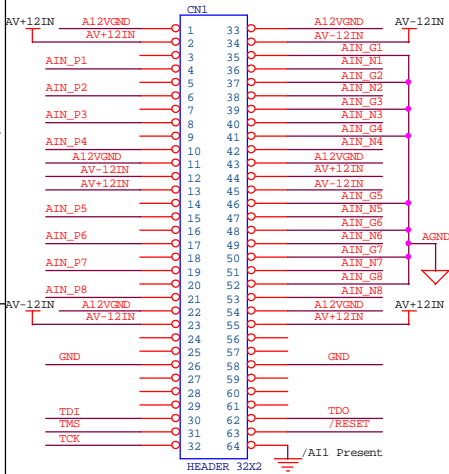


For test mode selection of AES Tx stream features

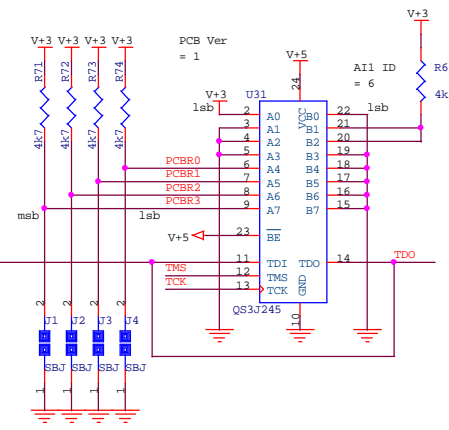
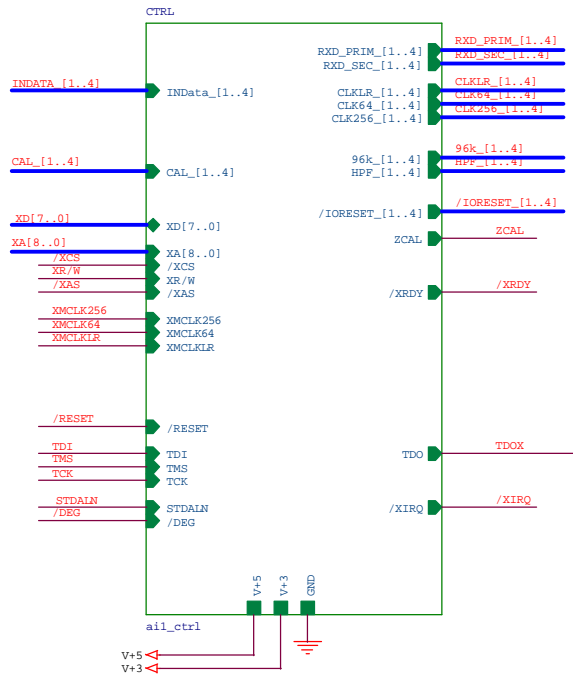
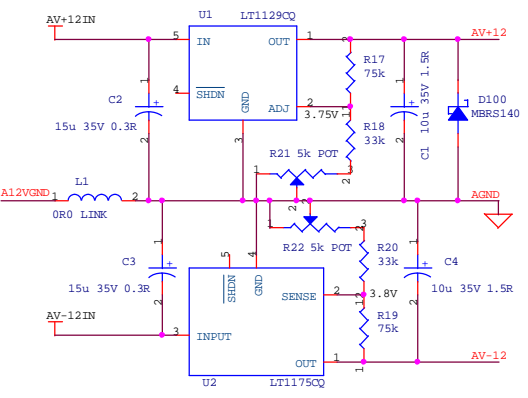
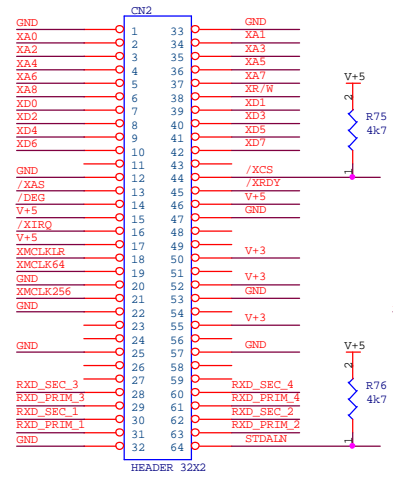
| | |
|------------------------------------|--|
| Title | Fairlight ESP Pty. Ltd. |
| AES2 Stereo I/O Channel 7-8 | Unit B 5 Skyline Place Frenchs Forest 2086 Australia |
| Reference: - | Created: Wednesday, February 18, 1998 |
| Design: AR | Revision: B |
| | Date: Monday, June 04, 2001 |
| | Sheet: 6 of 6 |



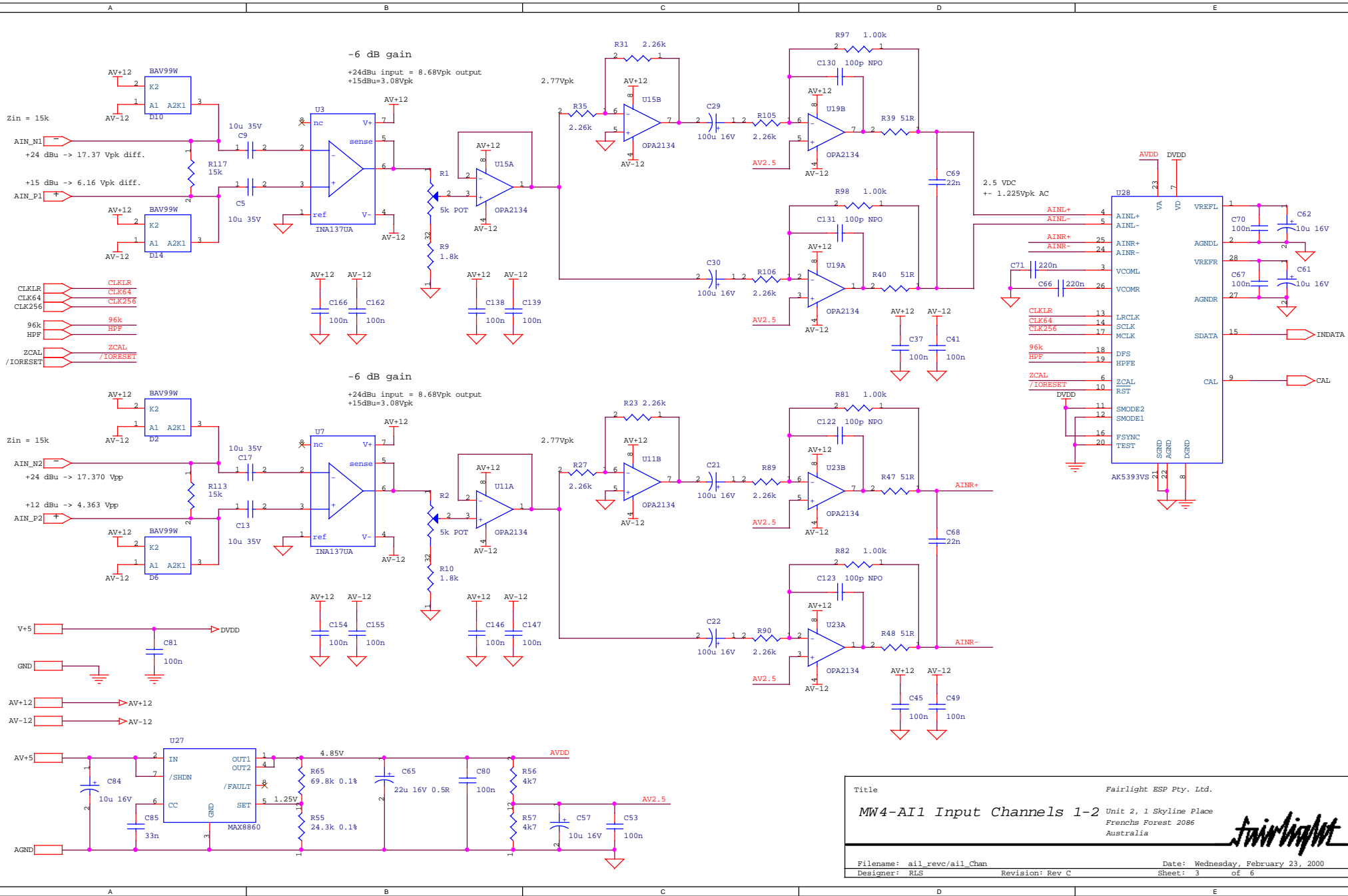
External I/O Connector



DSP I/O Connector

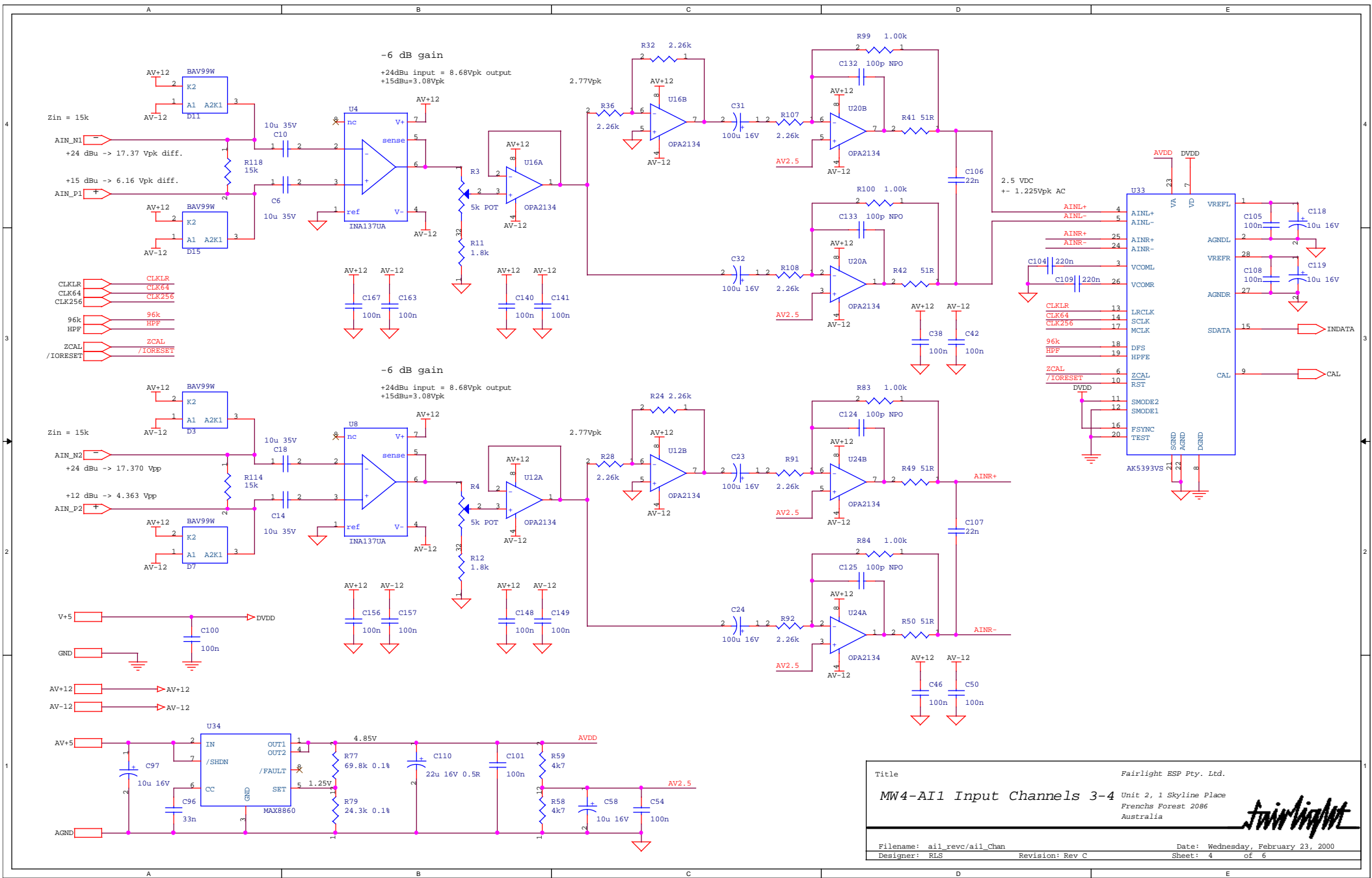


| | | | |
|------------------------|-----------------|---|------|
| Title | | Fairlight ESP Pty. Ltd. | |
| MW4-AI1 | | Unit 2, 1 Skyline Place Frenchs Forest 2086 Australia | |
| Filename: ai1 revc/ai1 | | Date: Thursday, May 18, 2006 | |
| Designer: RLS | Revision: Rev C | Sheet: 1 | of 6 |




| | | |
|-----------------------------|-----------------|---|
| Title | | Fairlight ESP Pty. Ltd. |
| MW4-AI1 Input Channels 1-2 | | Unit 2, 1 Skyline Place Frenchs Forest 2086 Australia |
| Filename: ail_revC/ail_chan | | Date: Wednesday, February 23, 2000 |
| Designer: RLS | Revision: Rev C | Sheet: 3 of 6 |



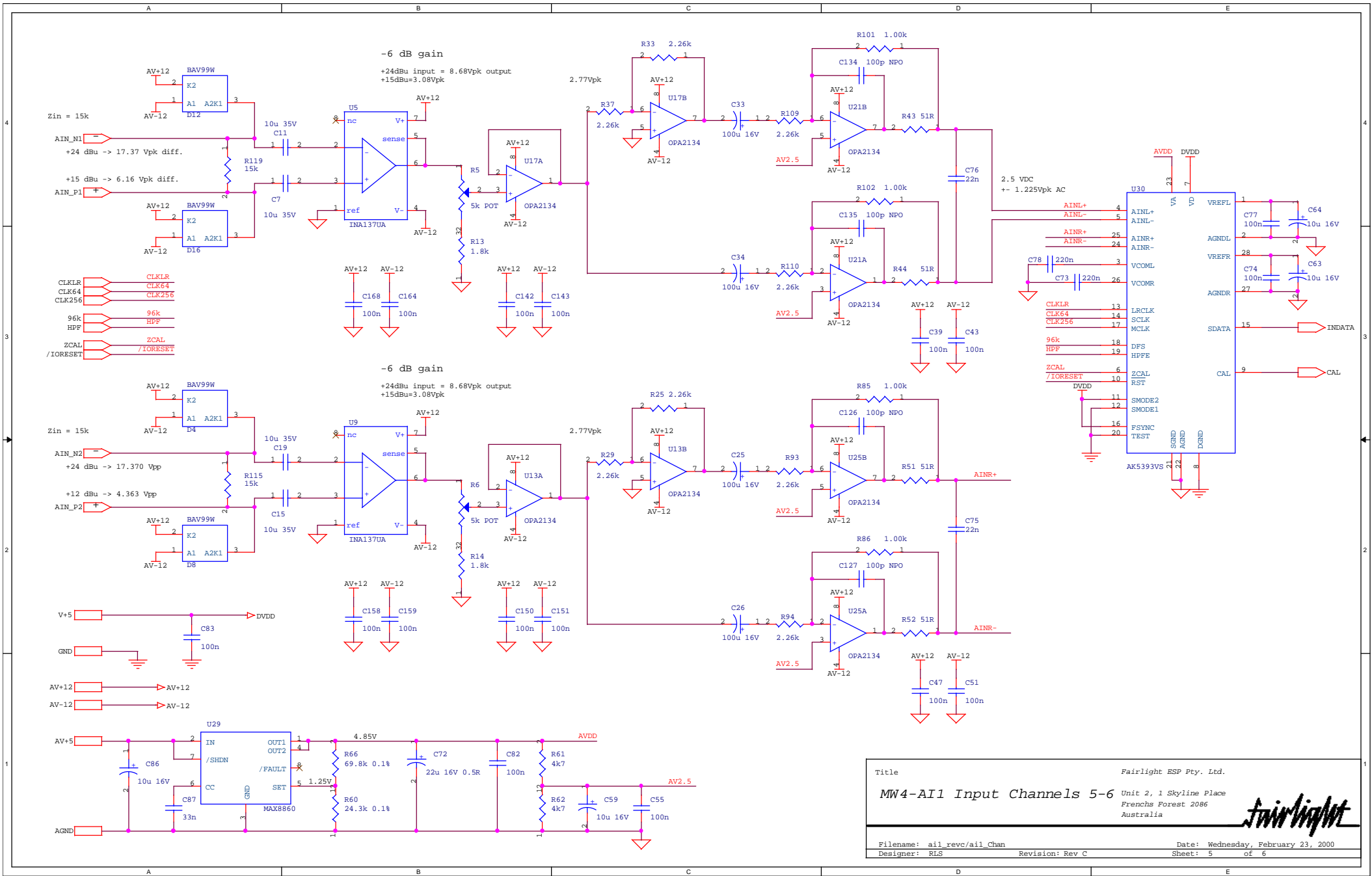


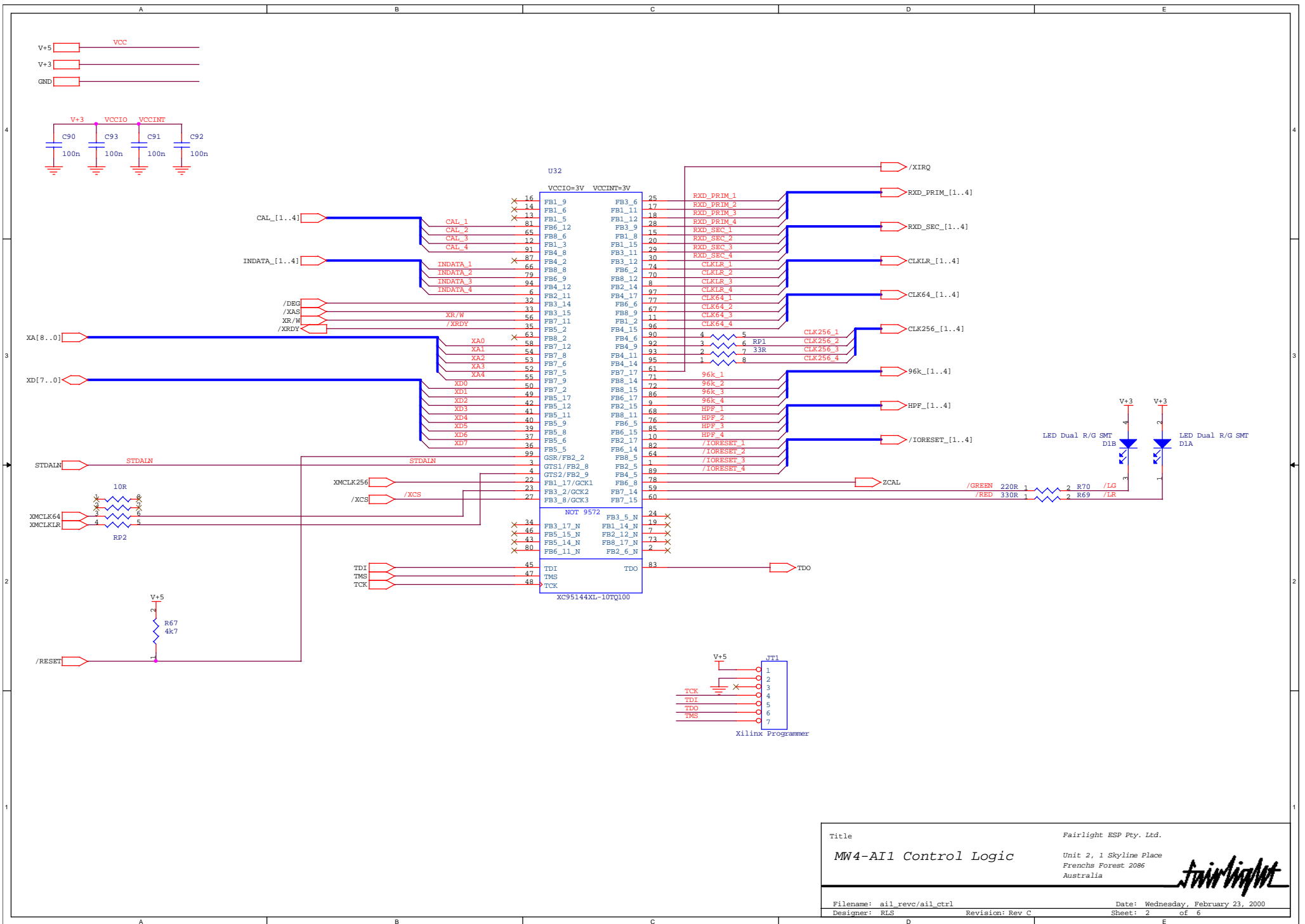
Title: Fairlight ESP Pty. Ltd.

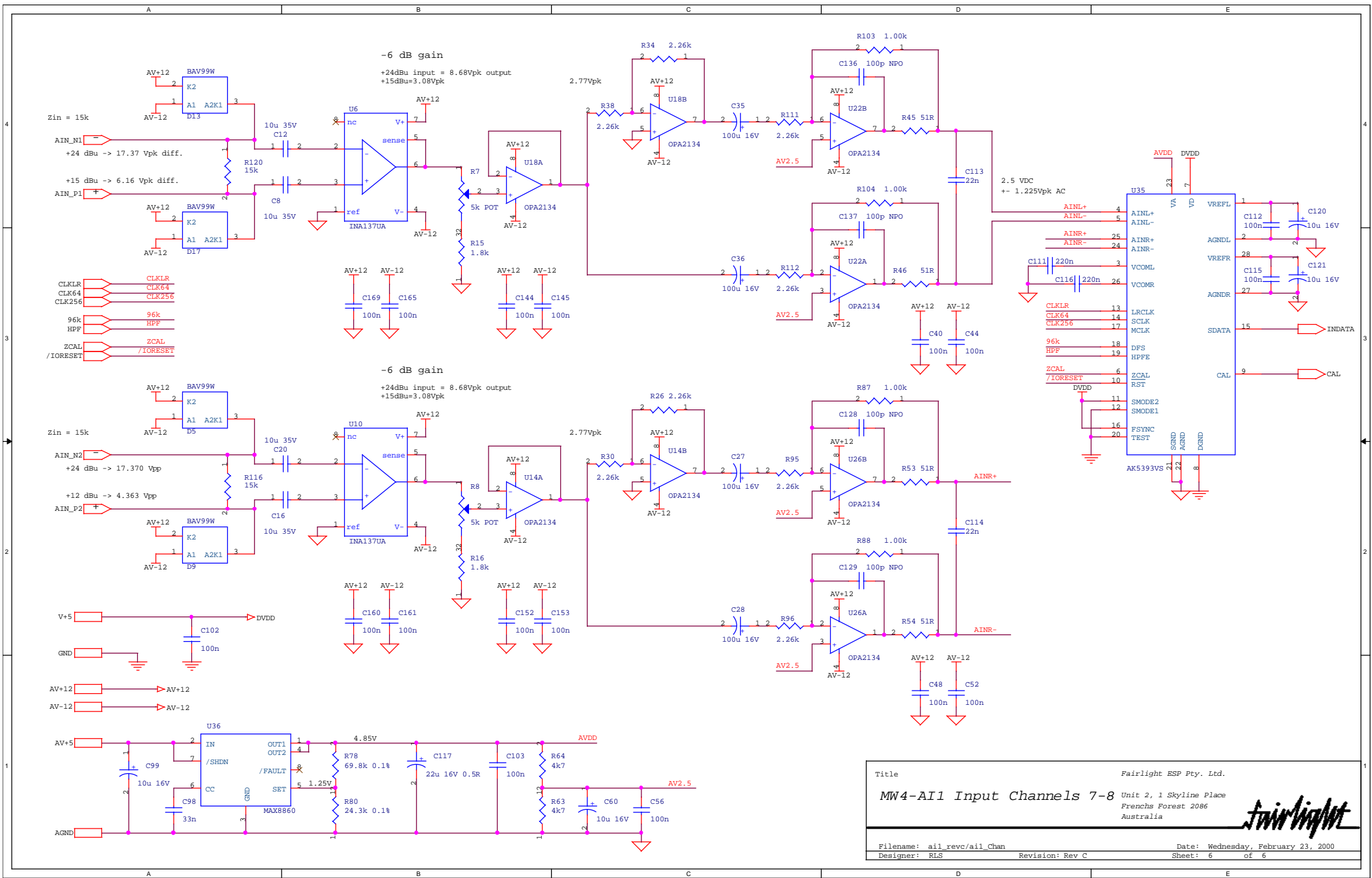
MW4-AI1 Input Channels 3-4 Unit 2, 1 Skyline Place
 Frenchs Forest 2086
 Australia



Filename: ail_rev3/ail_chan Date: Wednesday, February 23, 2000
 Designer: RLS Revision: Rev C Sheet: 4 of 6

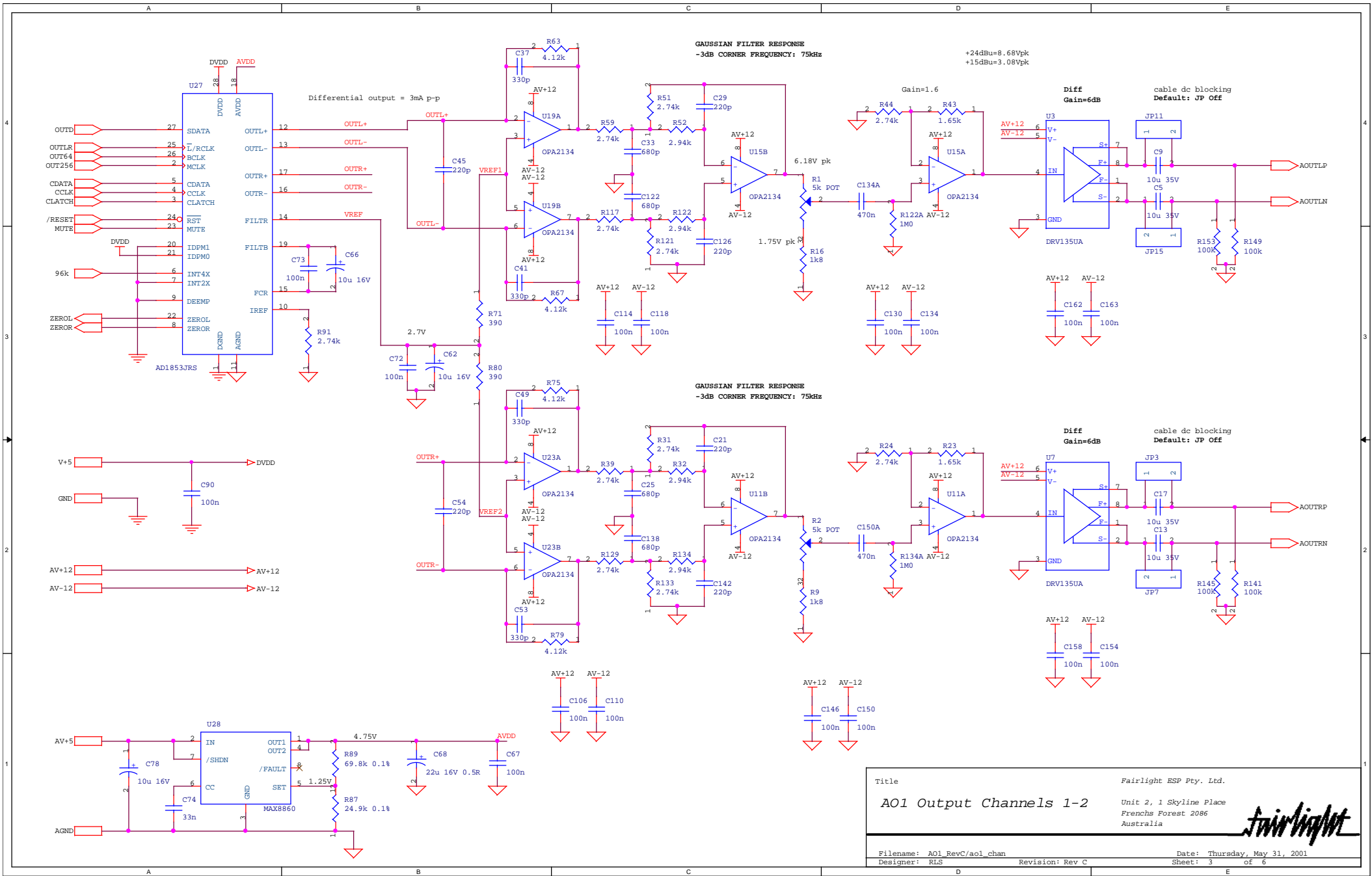


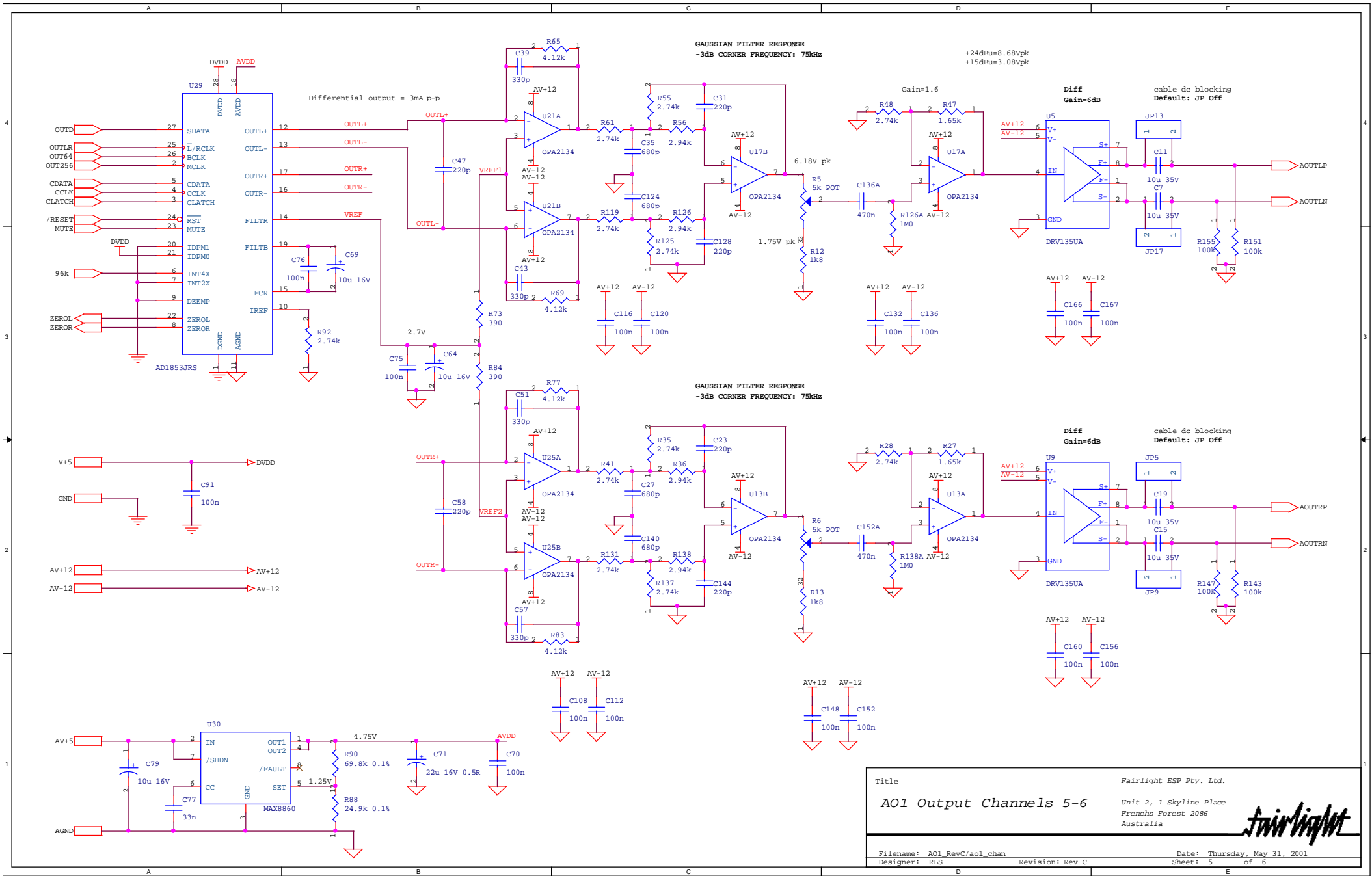




Title: Fairlight ESP Pty. Ltd.
 MW4-AI1 Input Channels 7-8 Unit 2, 1 Skyline Place
 Frenchs Forest 2086
 Australia *Fairlight*

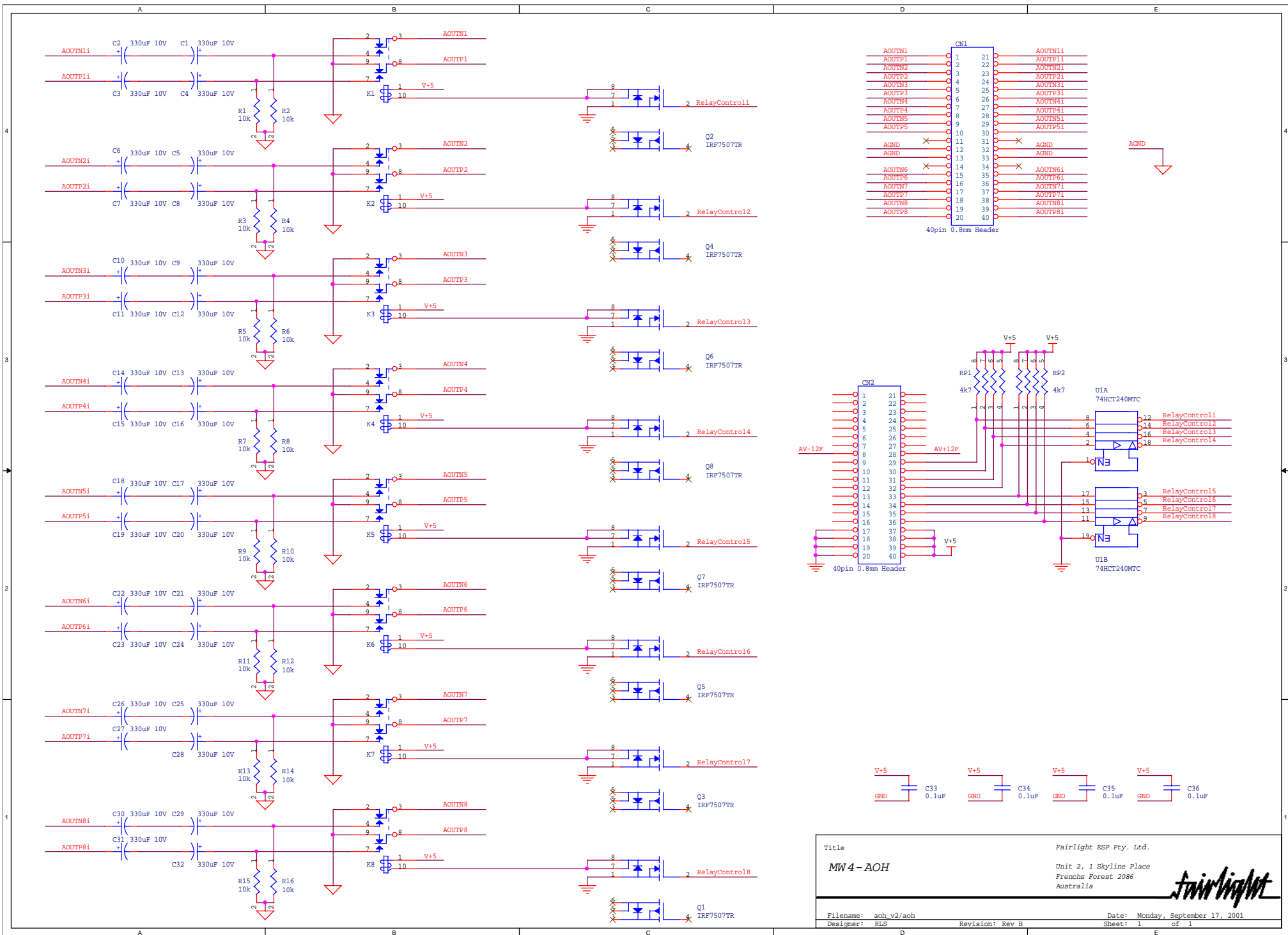
Filename: ail_revC/ail_chan Date: Wednesday, February 23, 2000
 Designer: RLS Revision: Rev C Sheet: 6 of 6





| | |
|--------------------------------|---|
| Title | Fairlight ESP Pty. Ltd. |
| AO1 Output Channels 5-6 | Unit 2, 1 Skyline Place Frenchs Forest 2086 Australia |
| Filename: AO1_RevC/ao1_chan | Date: Thursday, May 31, 2001 |
| Designer: RLS | Revision: Rev C |
| | Sheet: 5 of 6 |

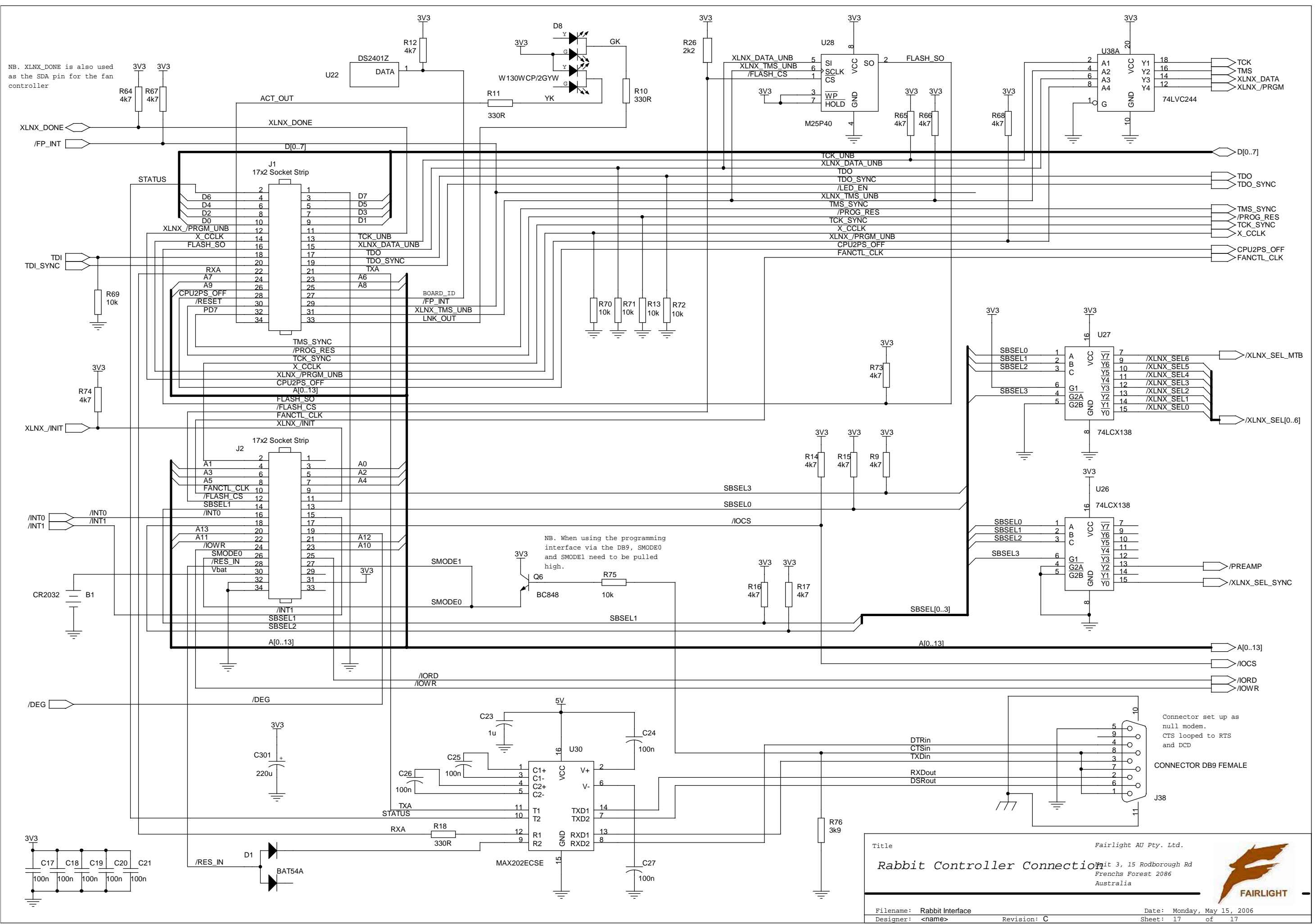




| | |
|----------------------|---|
| Title | Fairlight ESP Pty. Ltd. |
| MW4-AOH | Unit 2, 1 Skyline Place Frenchs Forest 2086 Australia |
| Filename: aoh_v2/aoh | Date: Monday, September 17, 2001 |
| Designer: RLS | Revision: Rev B |
| | Sheet: 1 of 1 |



NB. XLNX_DONE is also used as the SDA pin for the fan controller

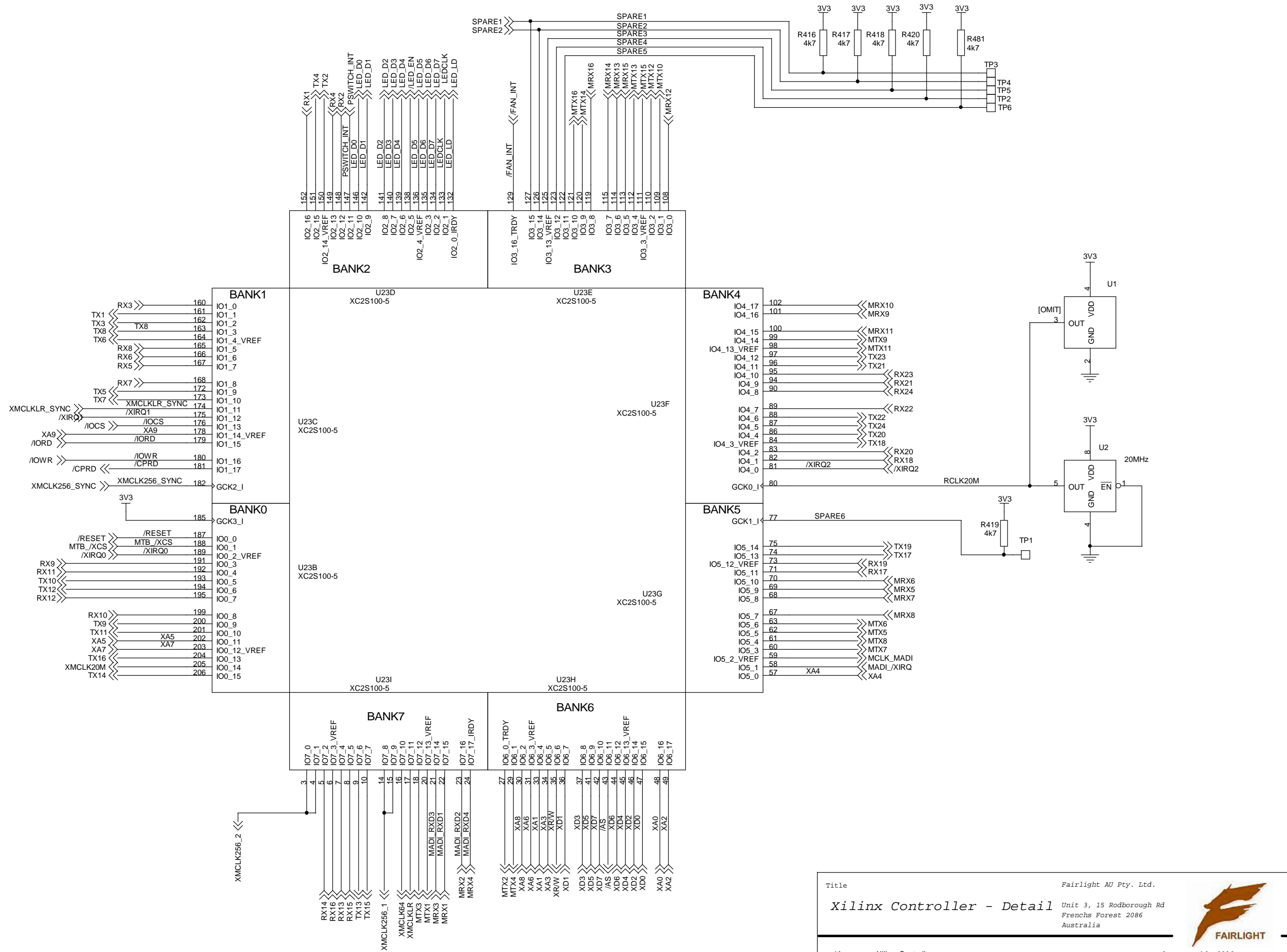


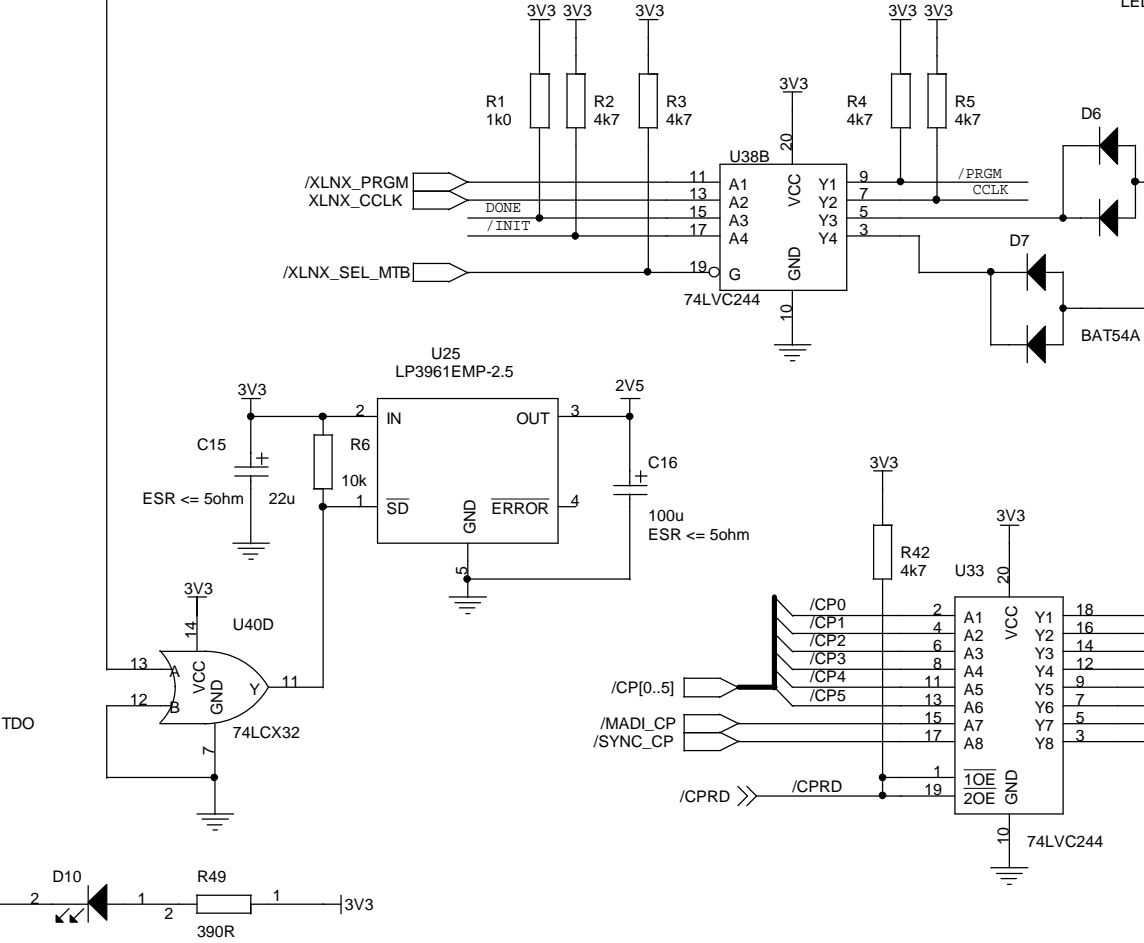
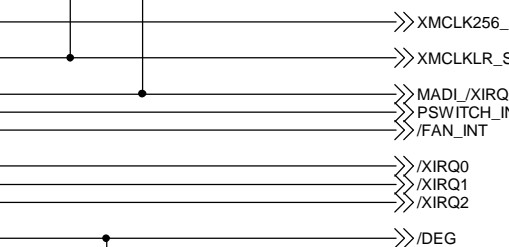
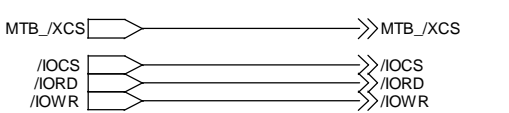
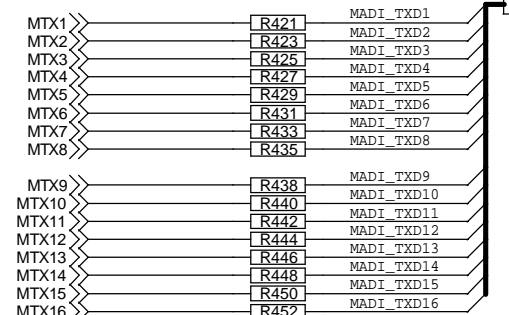
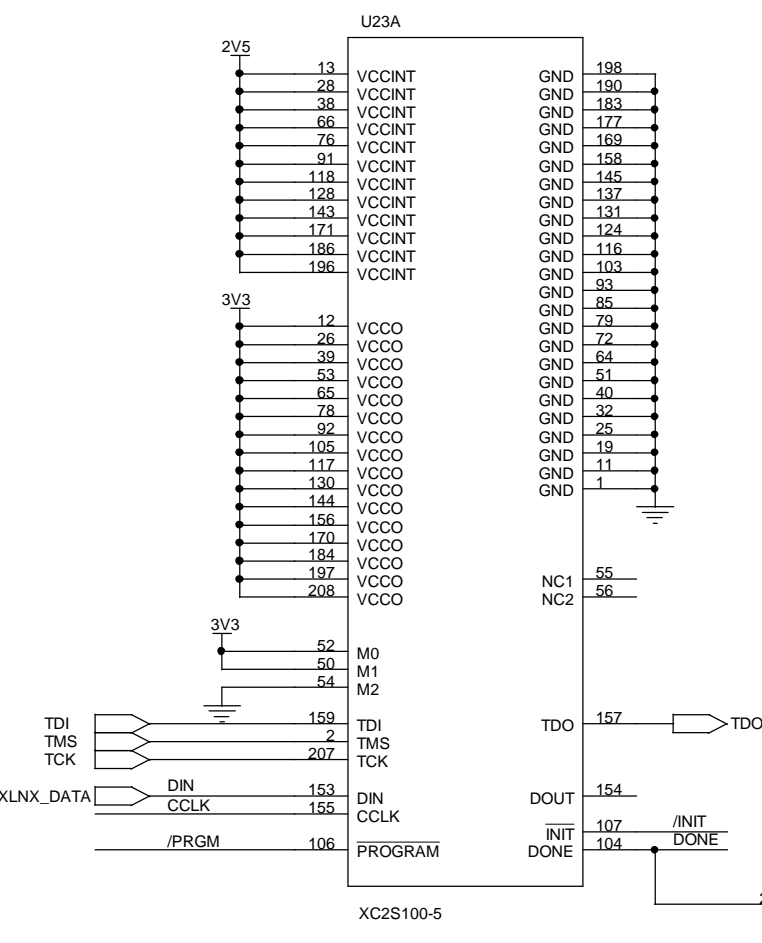
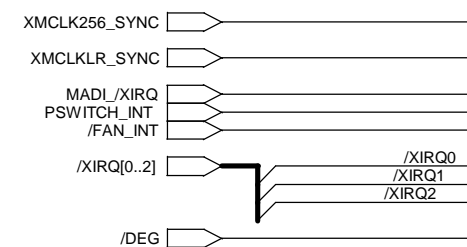
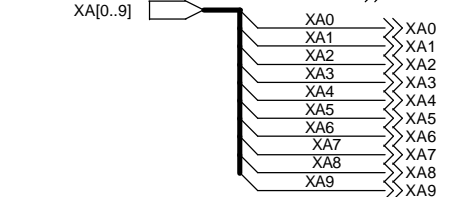
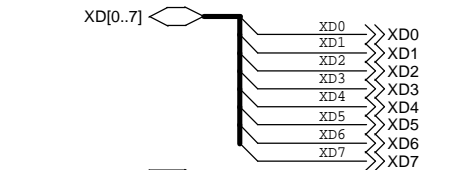
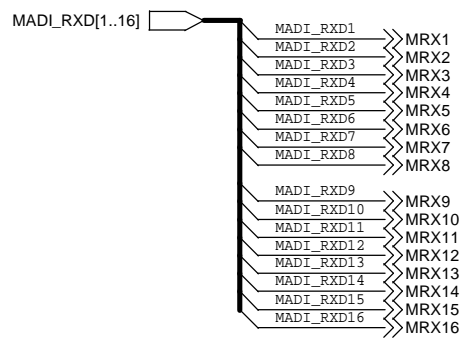
NB. When using the programming interface via the DB9, SMODE0 and SMODE1 need to be pulled high.

Connector set up as null modem. CTS looped to RTS and DCD

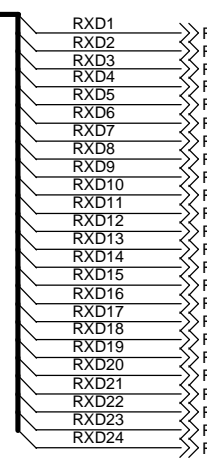
| | | | |
|------------------------------|-------------|--|-----------------|
| Title | | Fairlight AU Pty. Ltd. | |
| Rabbit Controller Connection | | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia | |
| Filename: Rabbit Interface | Revision: C | Date: Monday, May 15, 2006 | Sheet: 17 of 17 |



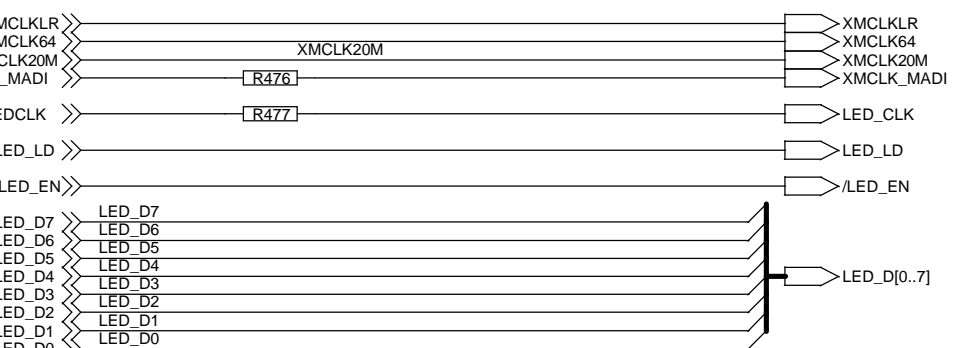
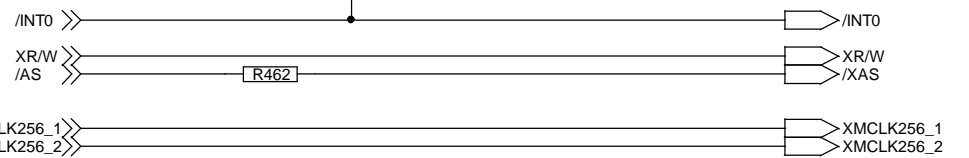
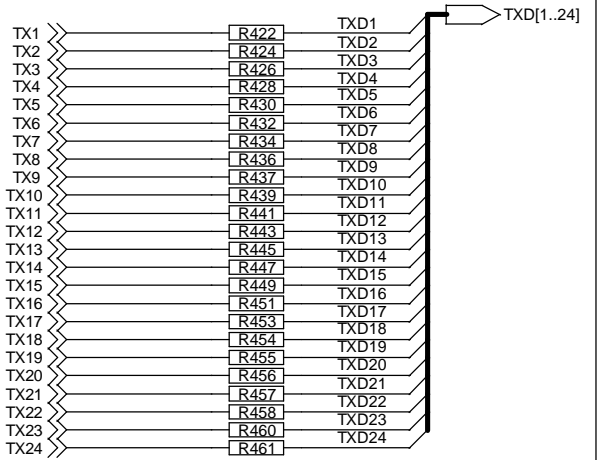




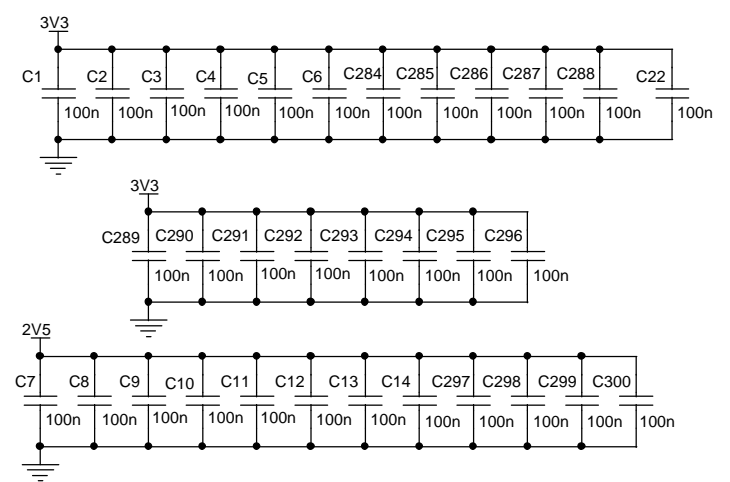
NB. Resistors without marked values are 33R.



NB. Resistors without marked values are 33R.



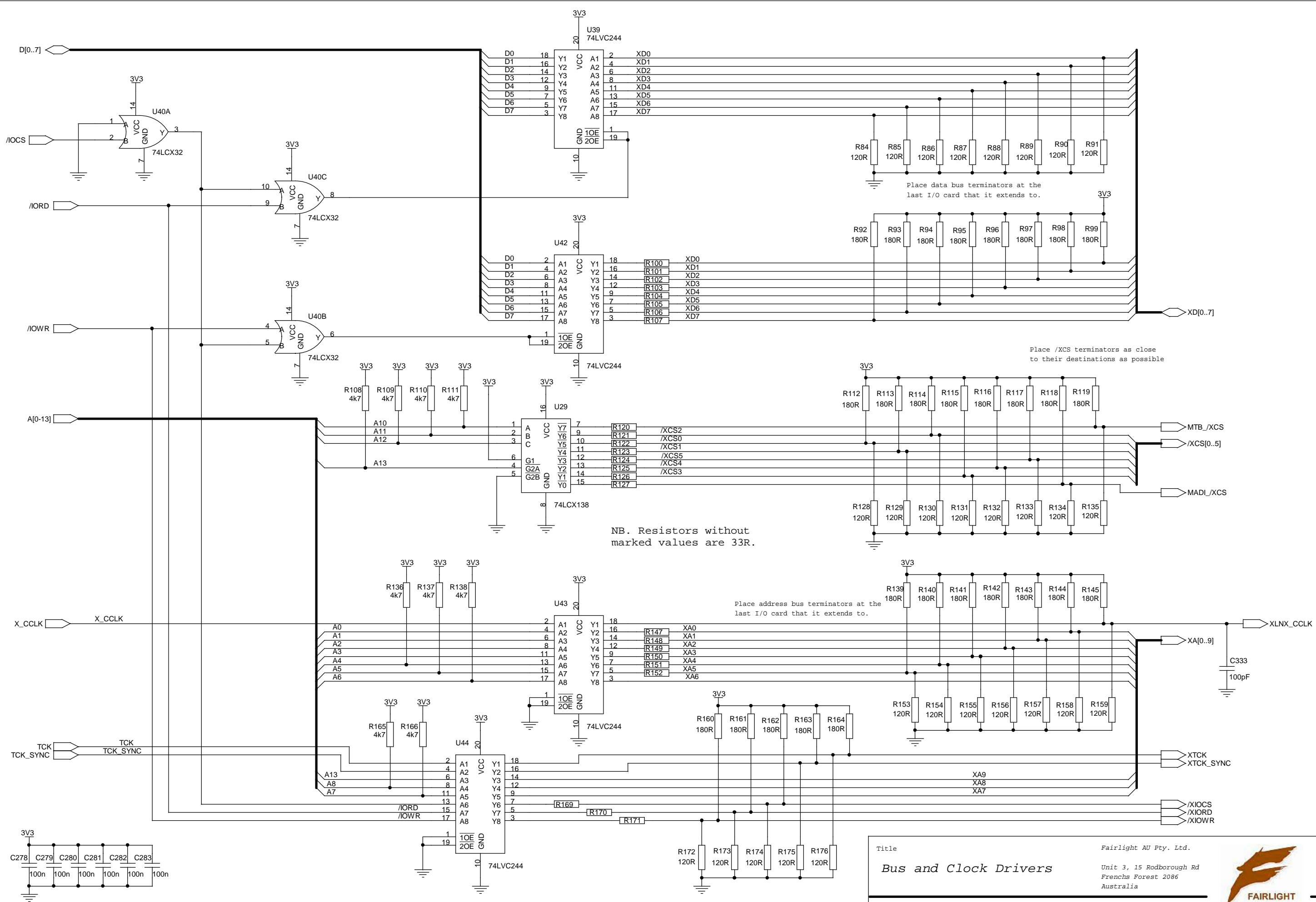
NB. Resistors without marked values are 33R.



Title: Xilinx Controller - Common
Fairlight AU Pty. Ltd.
Unit 3, 15 Rodborough Rd
Frenchs Forest 2086
Australia

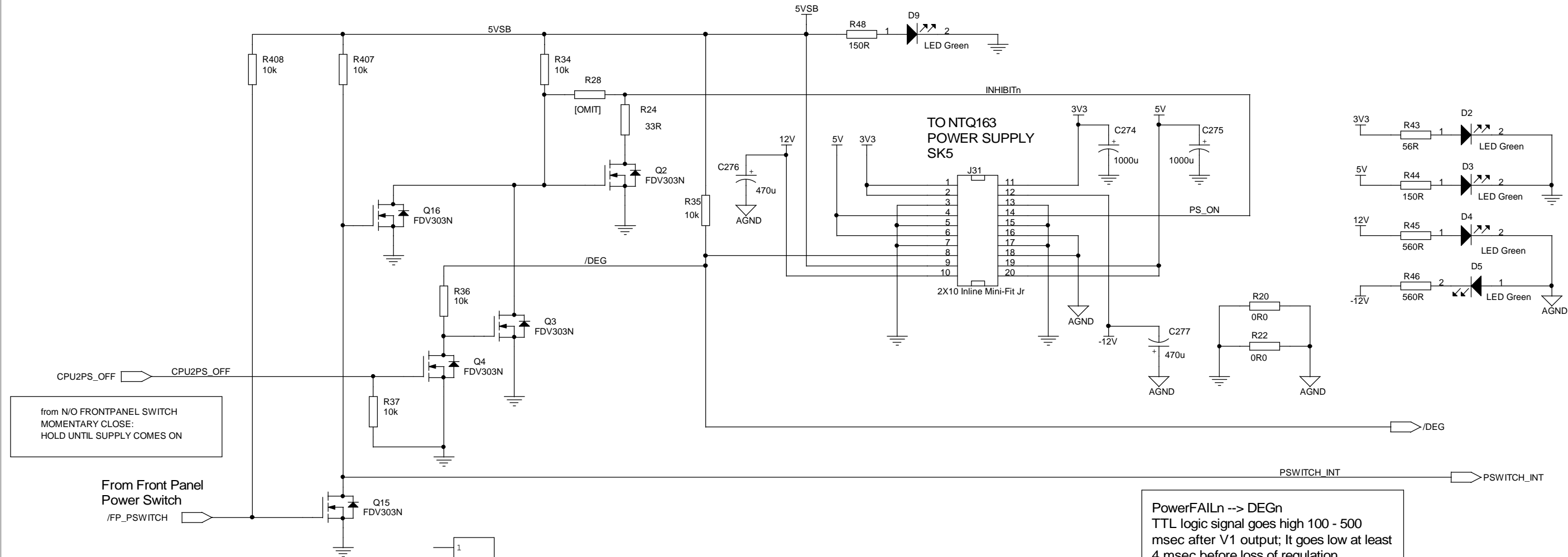
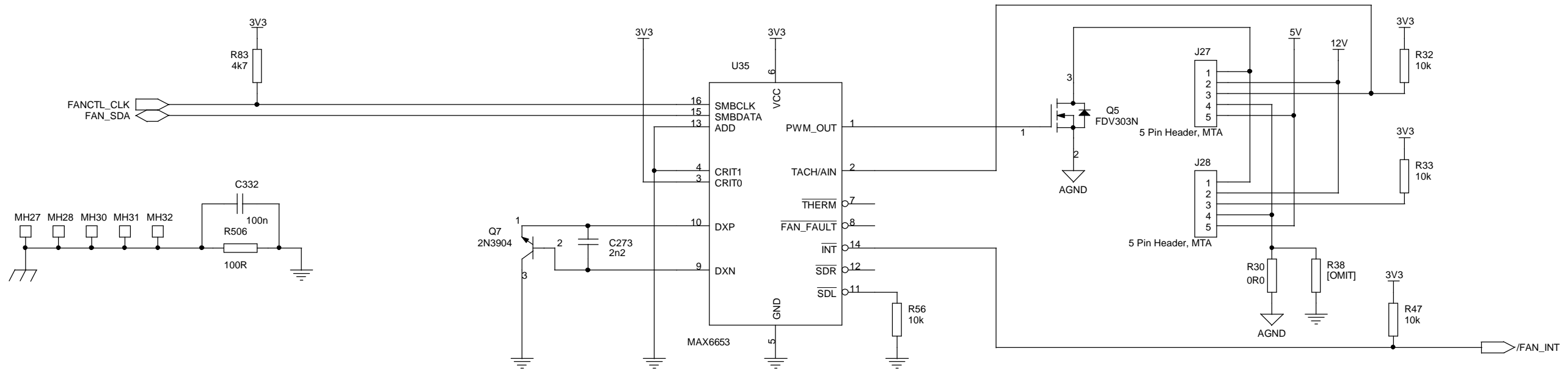
Filename: Xilinx_Controller
Designer: David le Comte
Revision: C

Date: Tuesday, May 16, 2006
Sheet: 13 of 17



NB. Resistors without marked values are 33R.



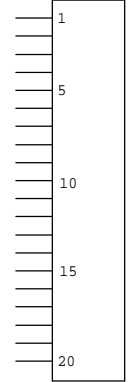


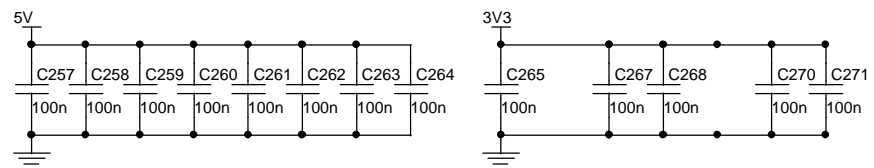
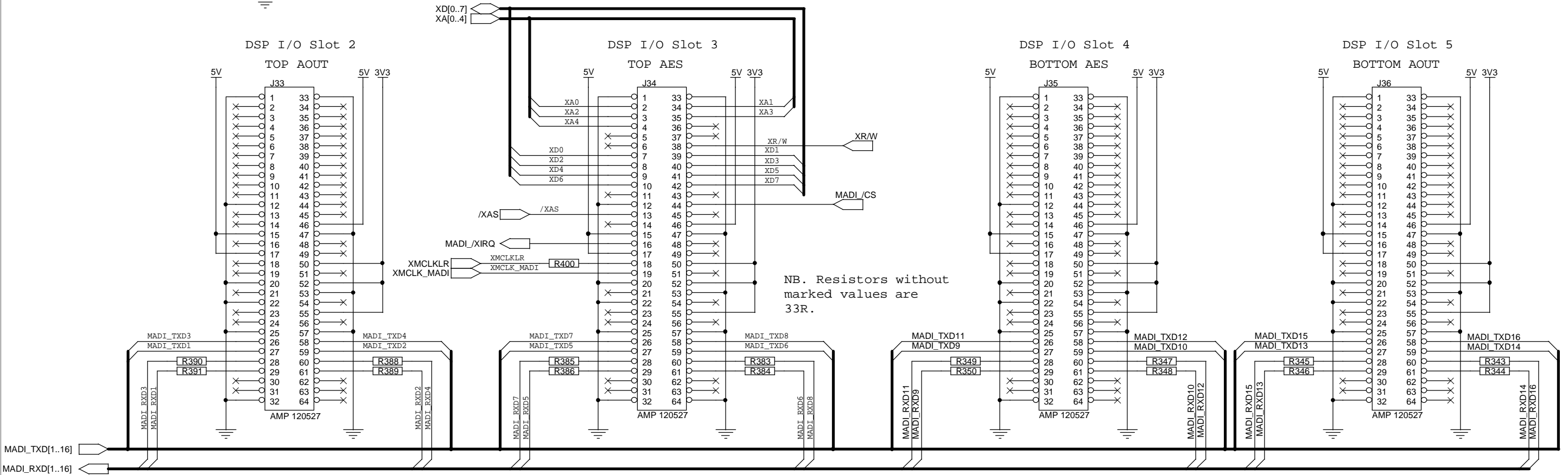
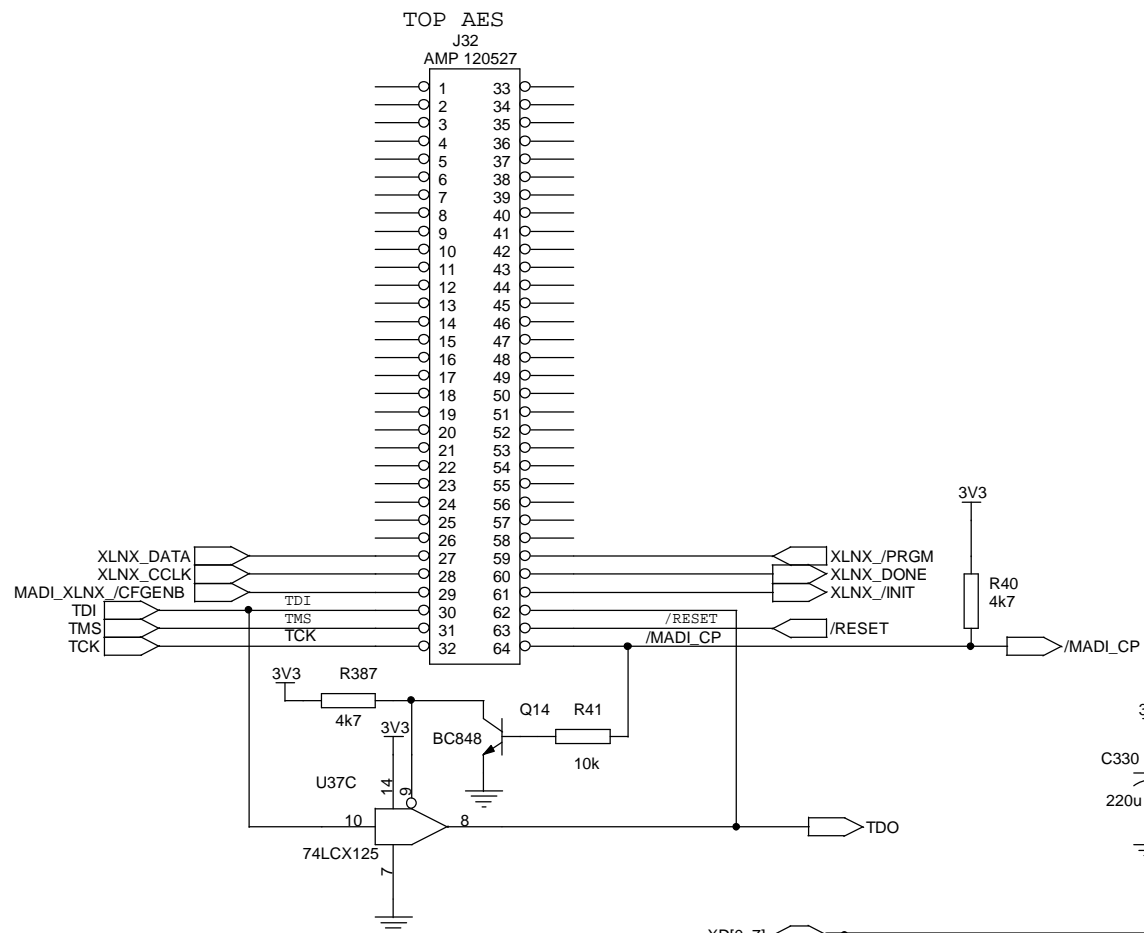
from N/O FRONTPANEL SWITCH
MOMENTARY CLOSE:
HOLD UNTIL SUPPLY COMES ON

From Front Panel
Power Switch
/FP_PSWITCH


PowerFAILn --> DEGn
TTL logic signal goes high 100 - 500
msec after V1 output; It goes low at least
4 msec before loss of regulation

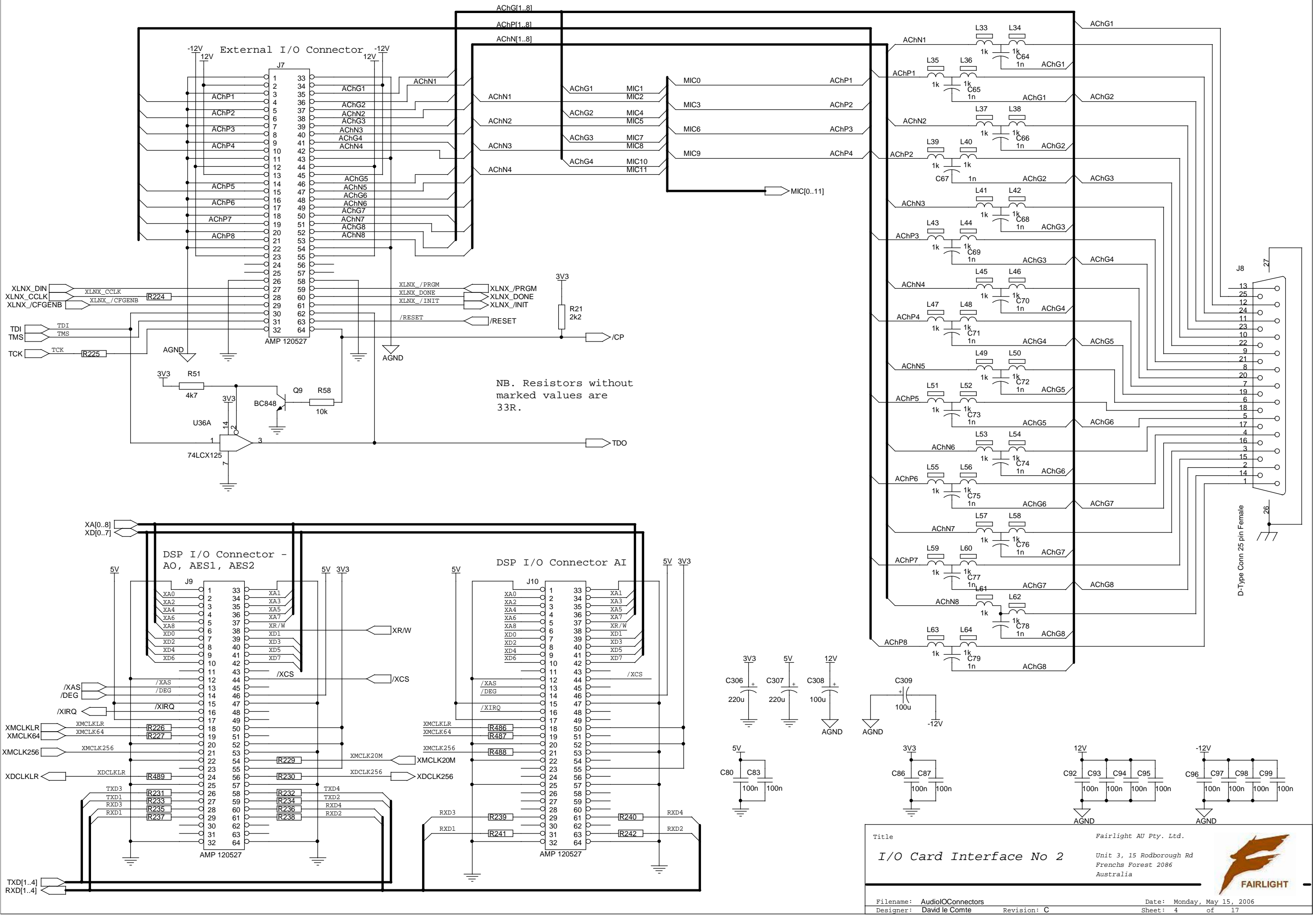
Molex 39-01-2200 Housing
Molex 39-00-0039 Crimp Pins





| | |
|--------------------------|--|
| Title | Fairlight AU Pty. Ltd. |
| MADI Interface | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia |
| Filename: MADI Interface | Date: Monday, May 15, 2006 |
| Designer: David le Comte | Revision: C |
| | Sheet: 10 of 17 |






NB. Resistors without marked values are 33R.

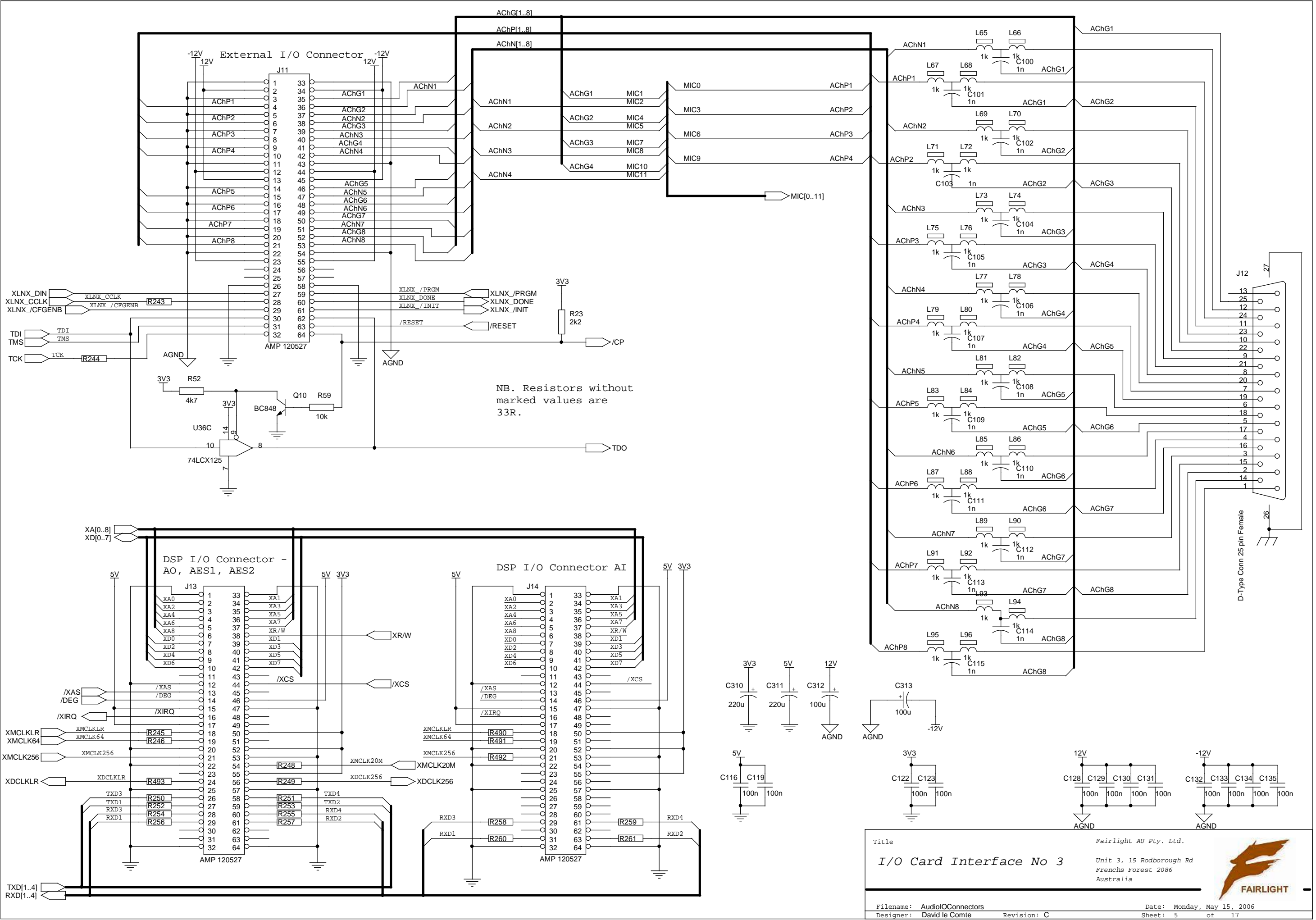
Title: I/O Card Interface No 2

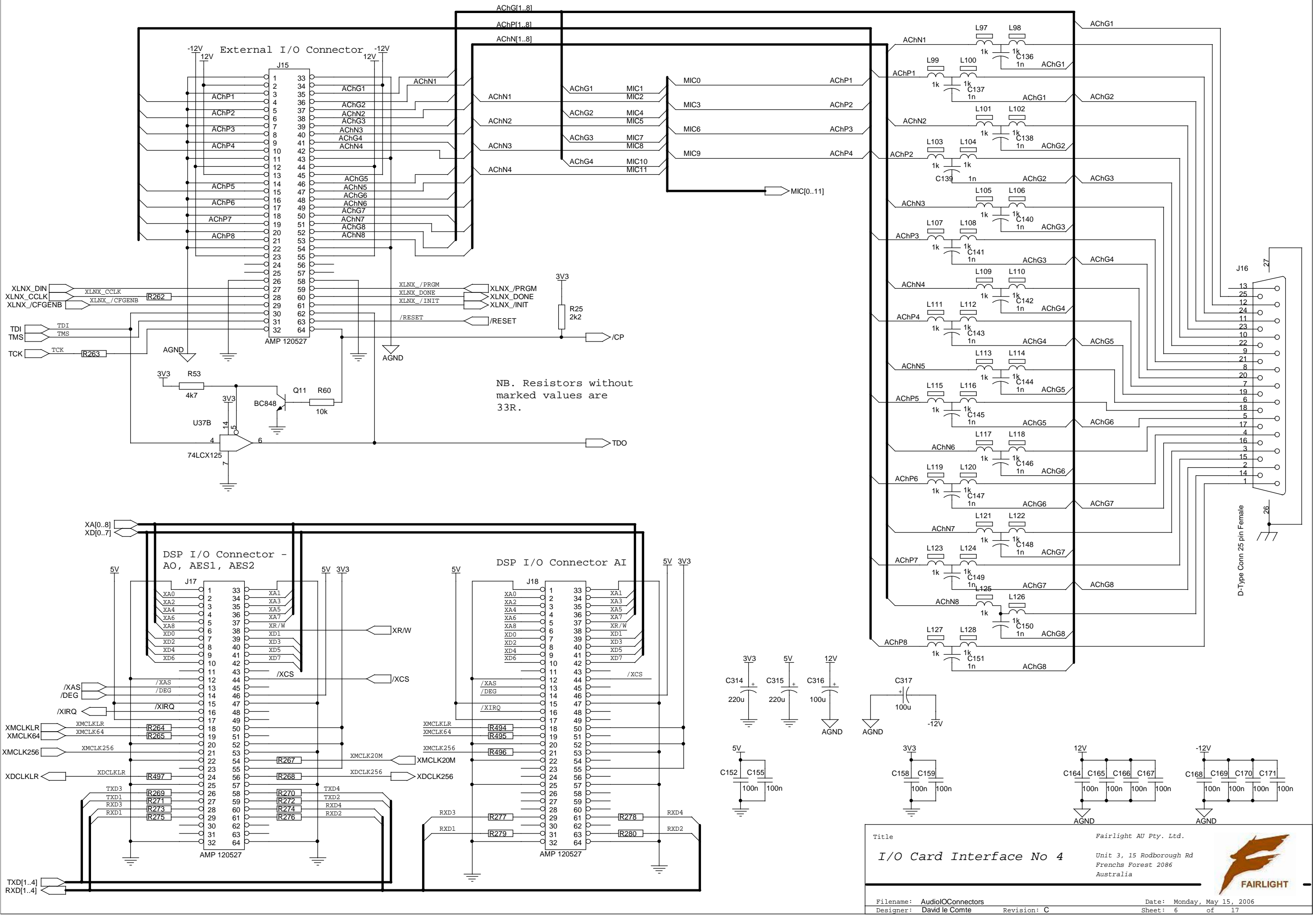
Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia

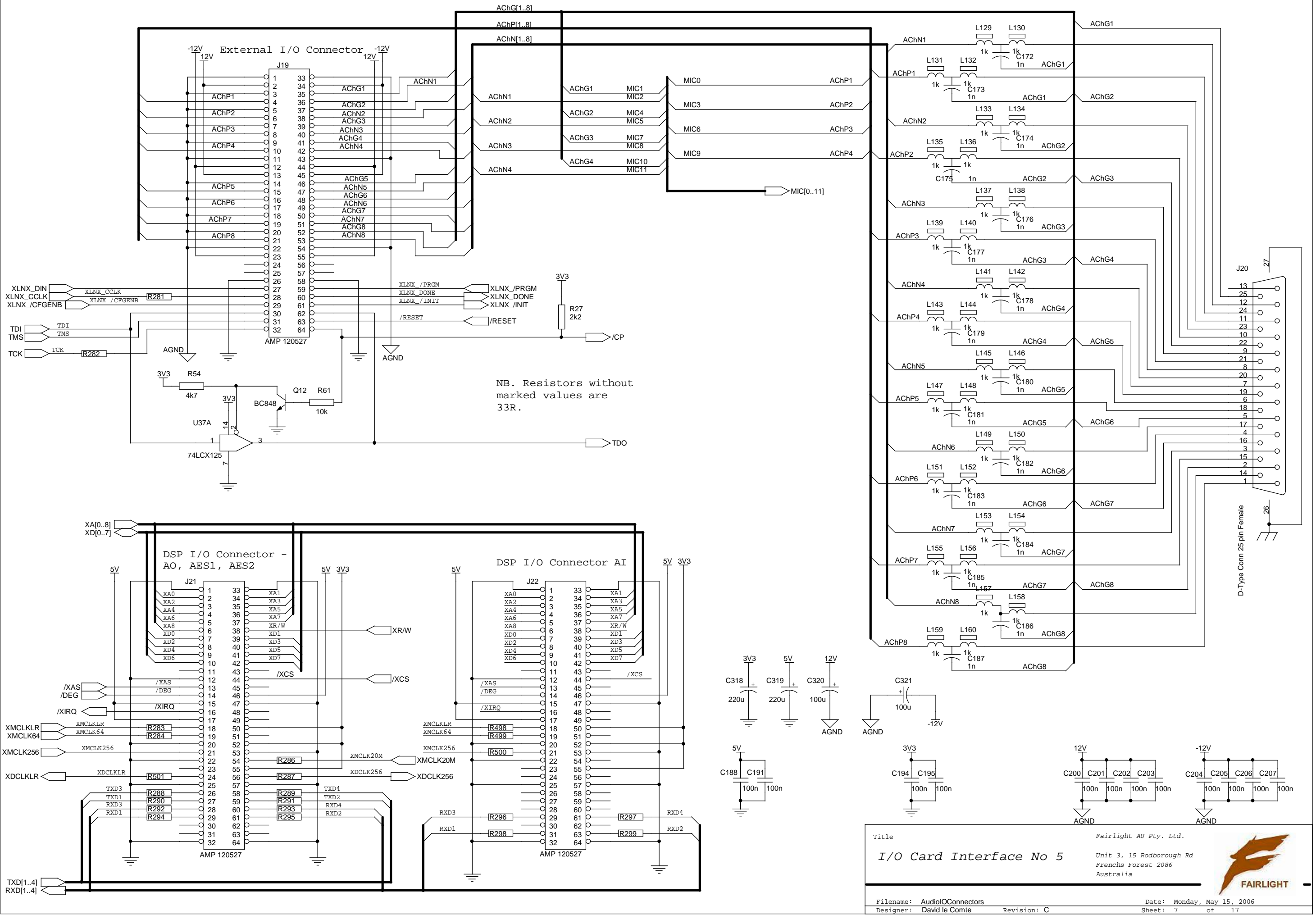


Filename: AudioIOConnectors
 Designer: David le Comte

Date: Monday, May 15, 2006
 Revision: C
 Sheet: 4 of 17








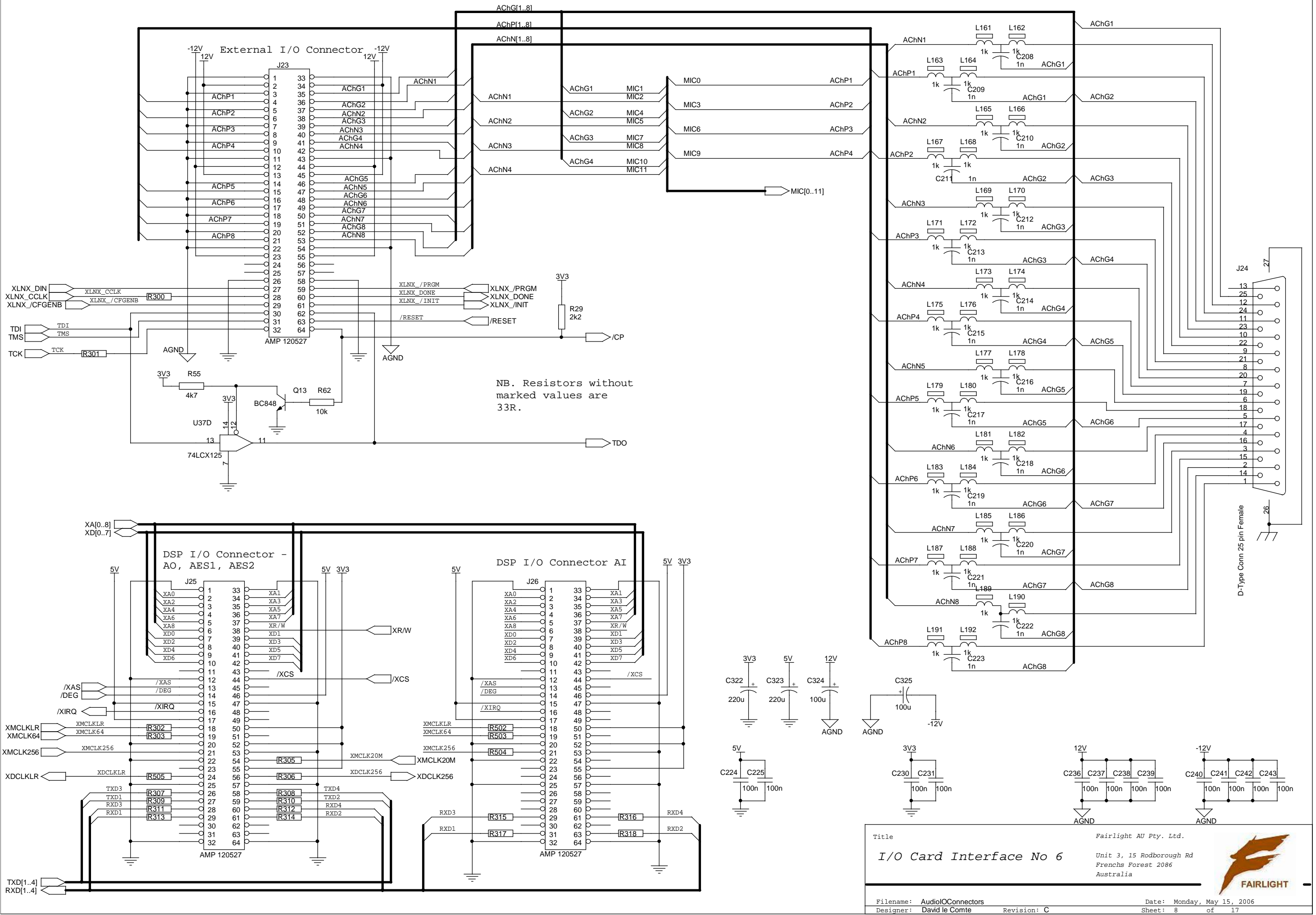
NB. Resistors without marked values are 33R.

Title: *I/O Card Interface No 5*

Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia




Filename: AudioIOConnectors
 Designer: David le Comte
 Date: Monday, May 15, 2006
 Revision: C
 Sheet: 7 of 17



NB. Resistors without marked values are 33R.

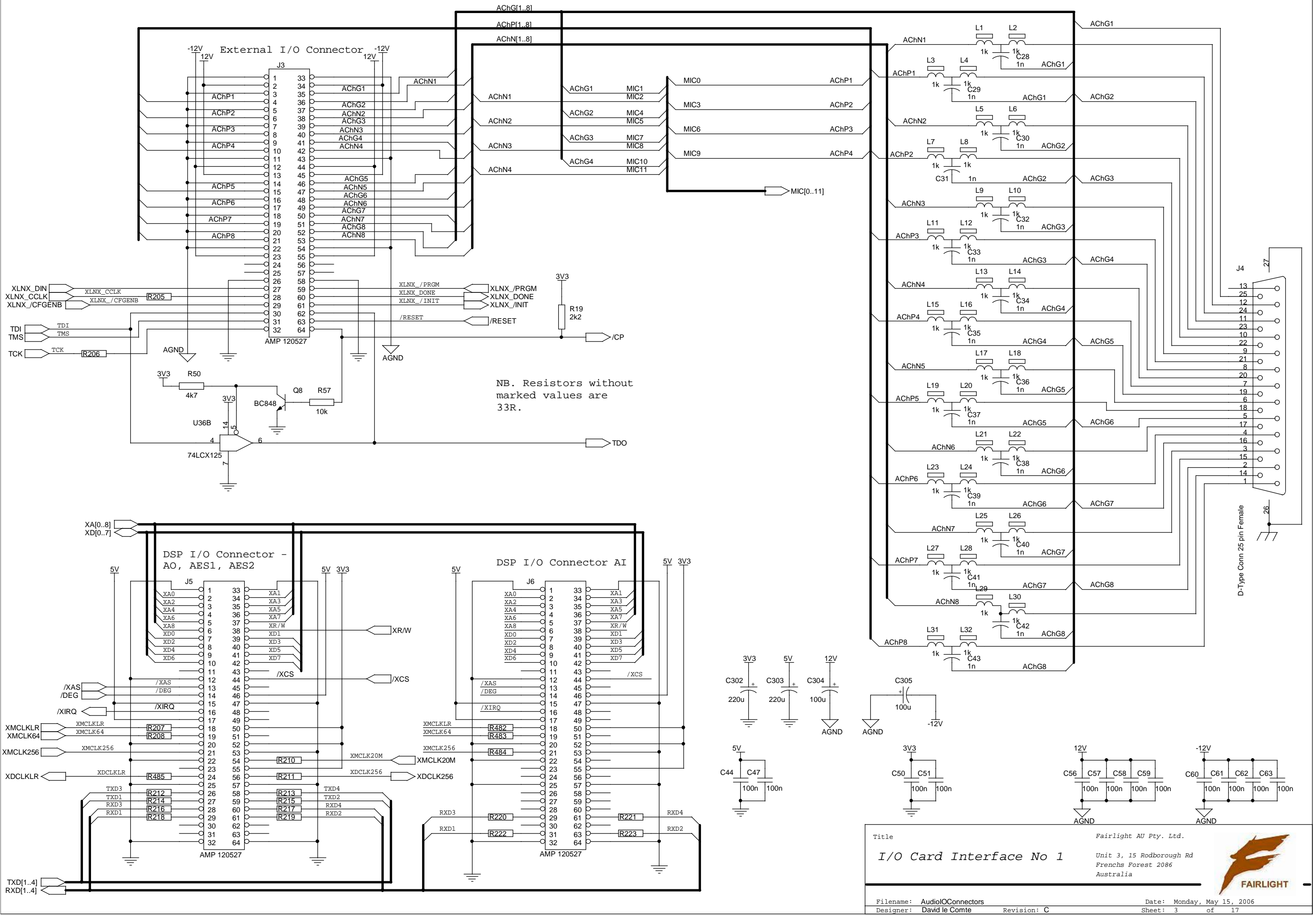
Title: *I/O Card Interface No 6*

Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia



Filename: AudioIOConnectors
 Designer: David le Comte


Date: Monday, May 15, 2006
 Revision: C
 Sheet: 8 of 17



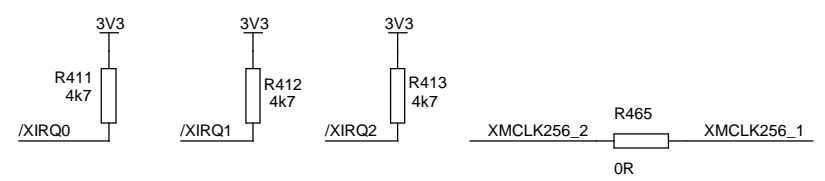
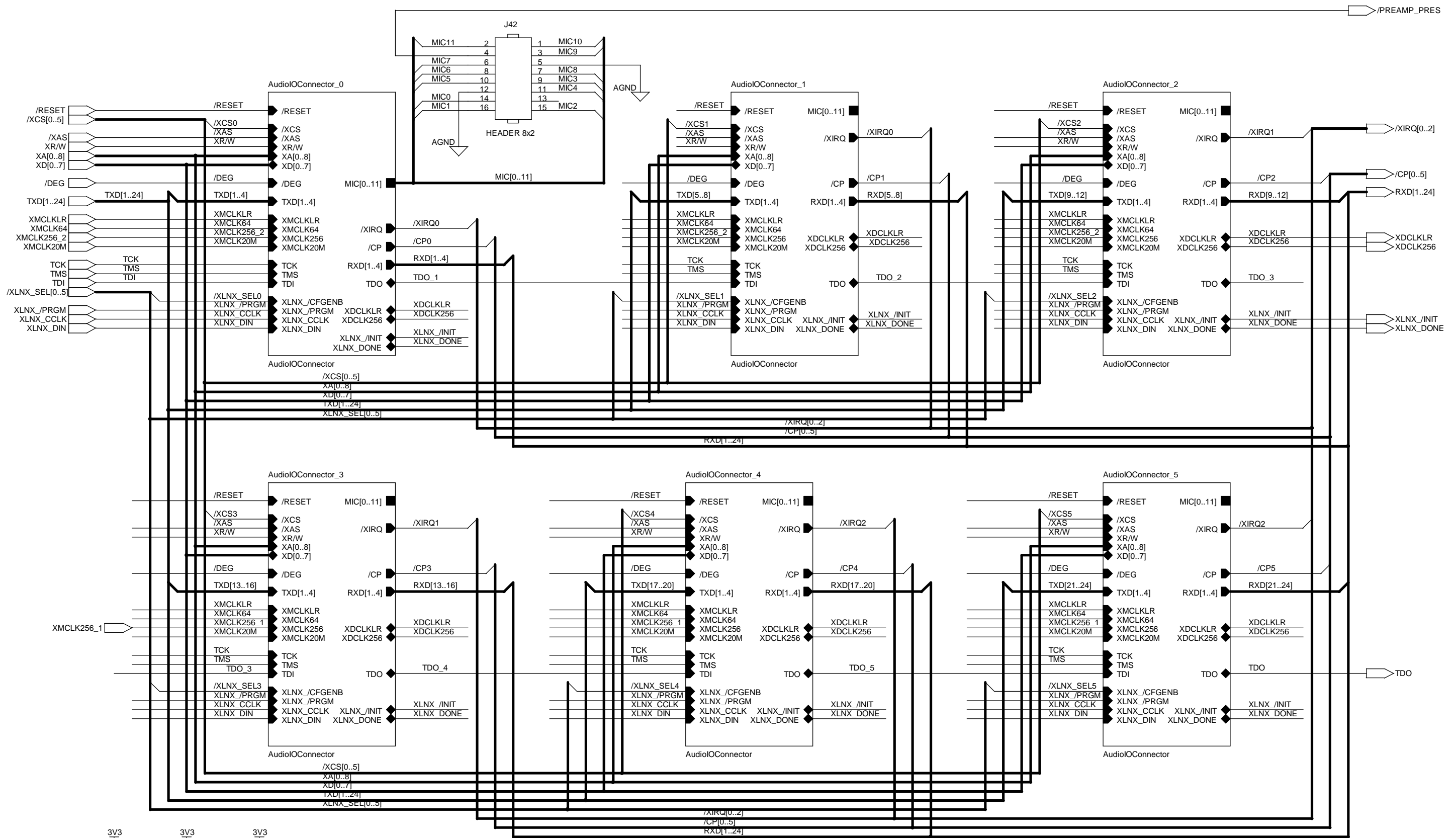
NB. Resistors without marked values are 33R.

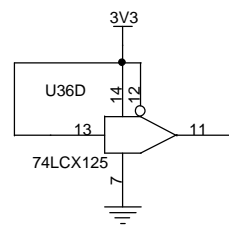
Title: *I/O Card Interface No 1*

Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia

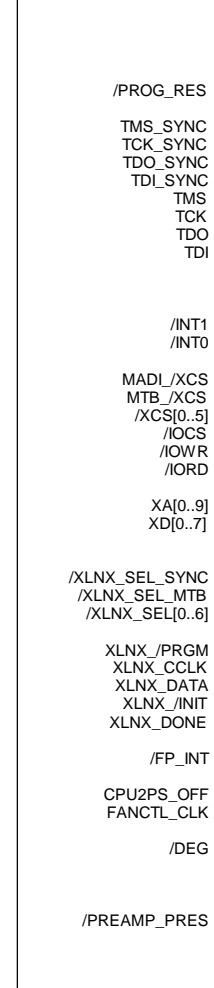


Filename: AudioIOConnectors
 Designer: David le Comte
 Date: Monday, May 15, 2006
 Revision: C
 Sheet: 3 of 17

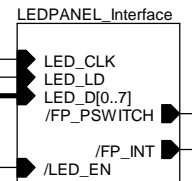
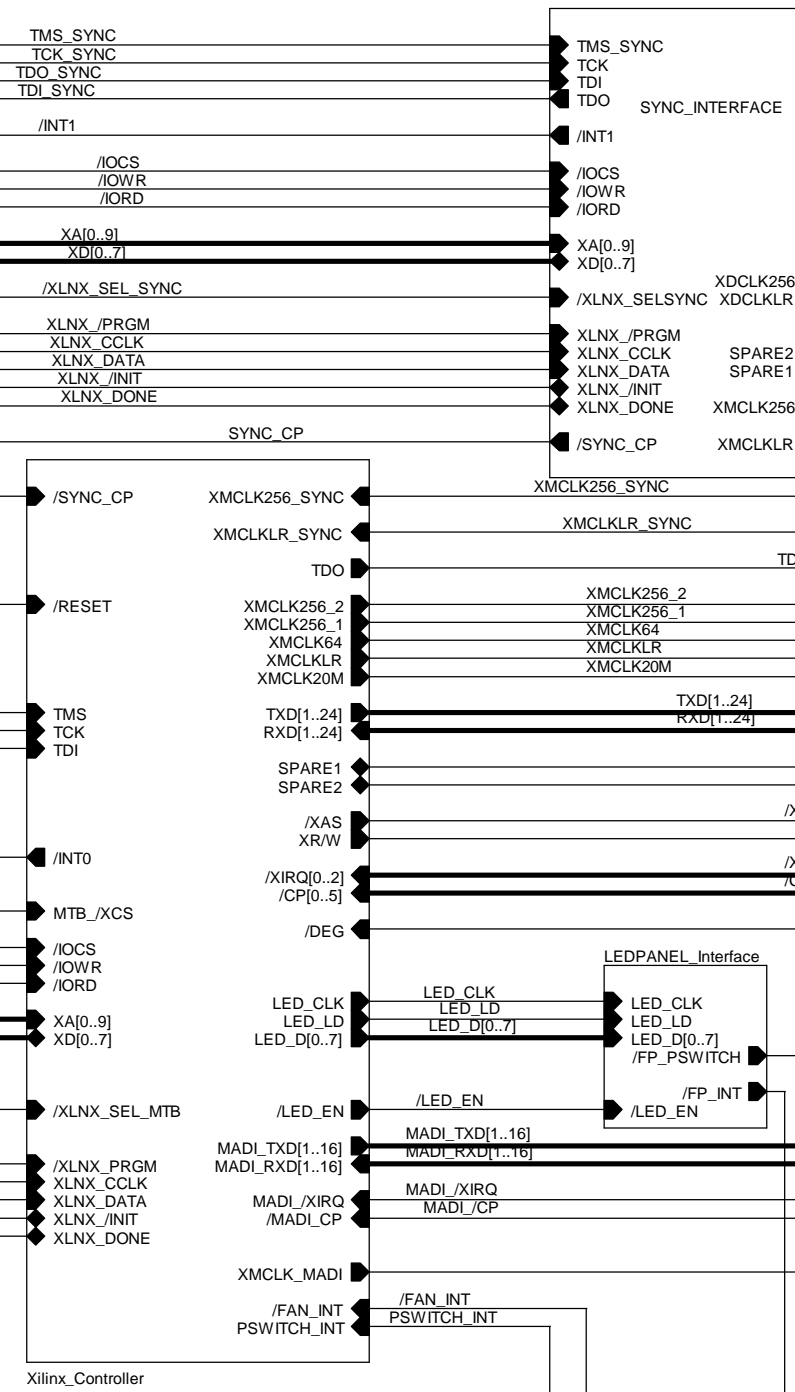
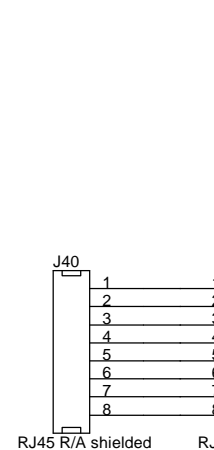




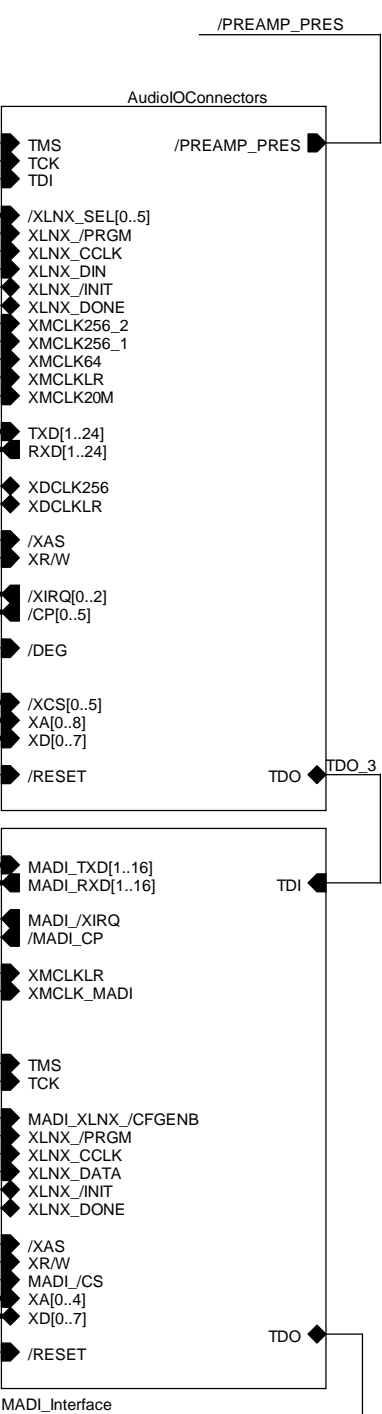
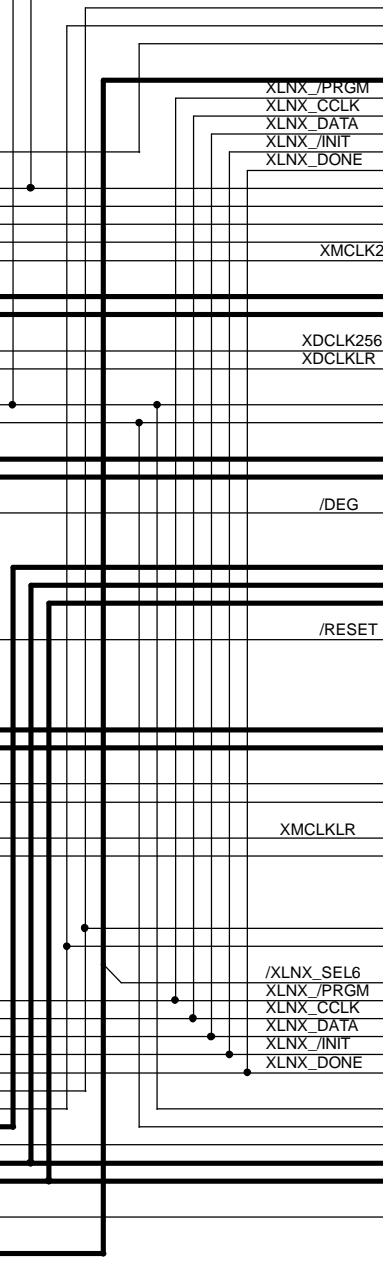
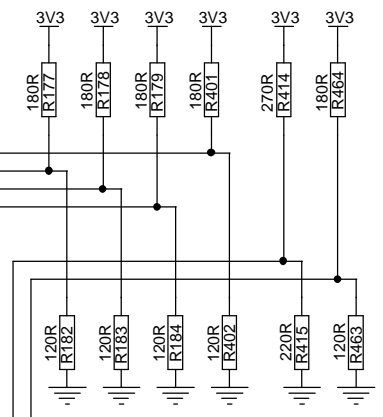
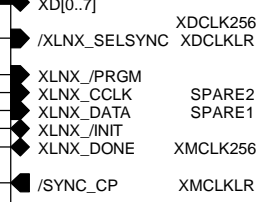
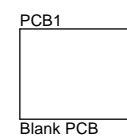
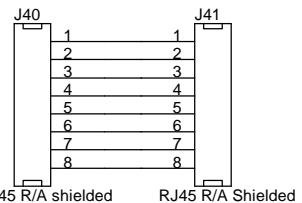
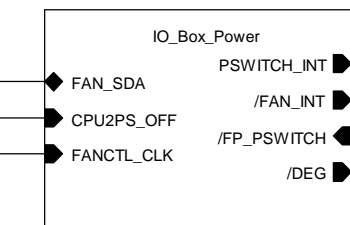
Rabbit Processor Interface

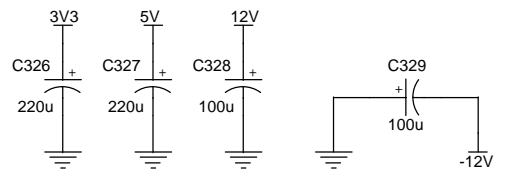
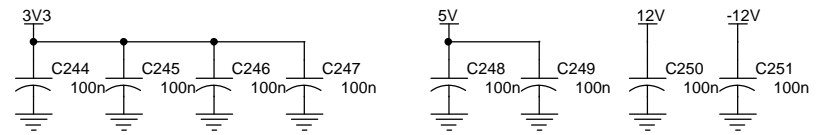
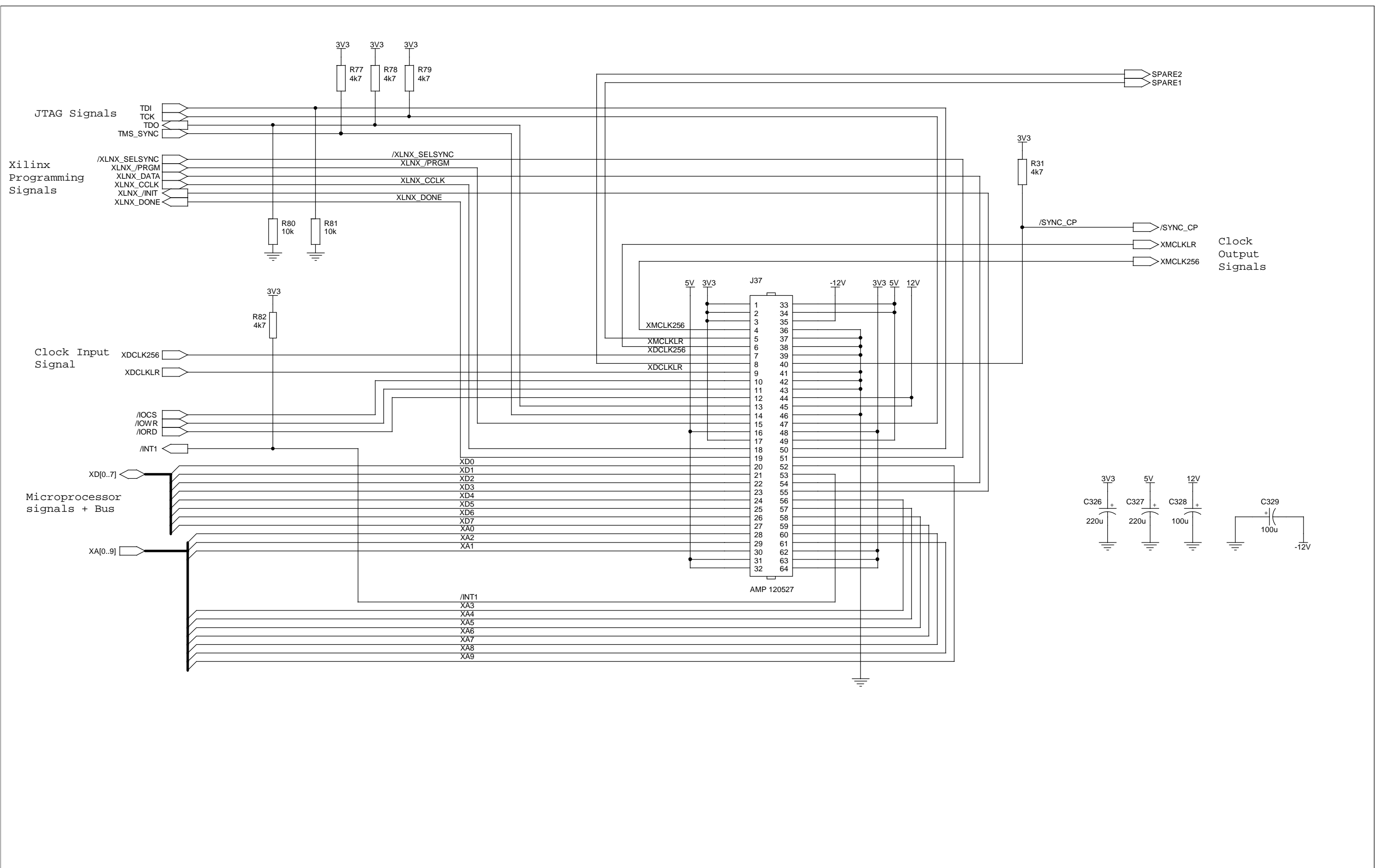


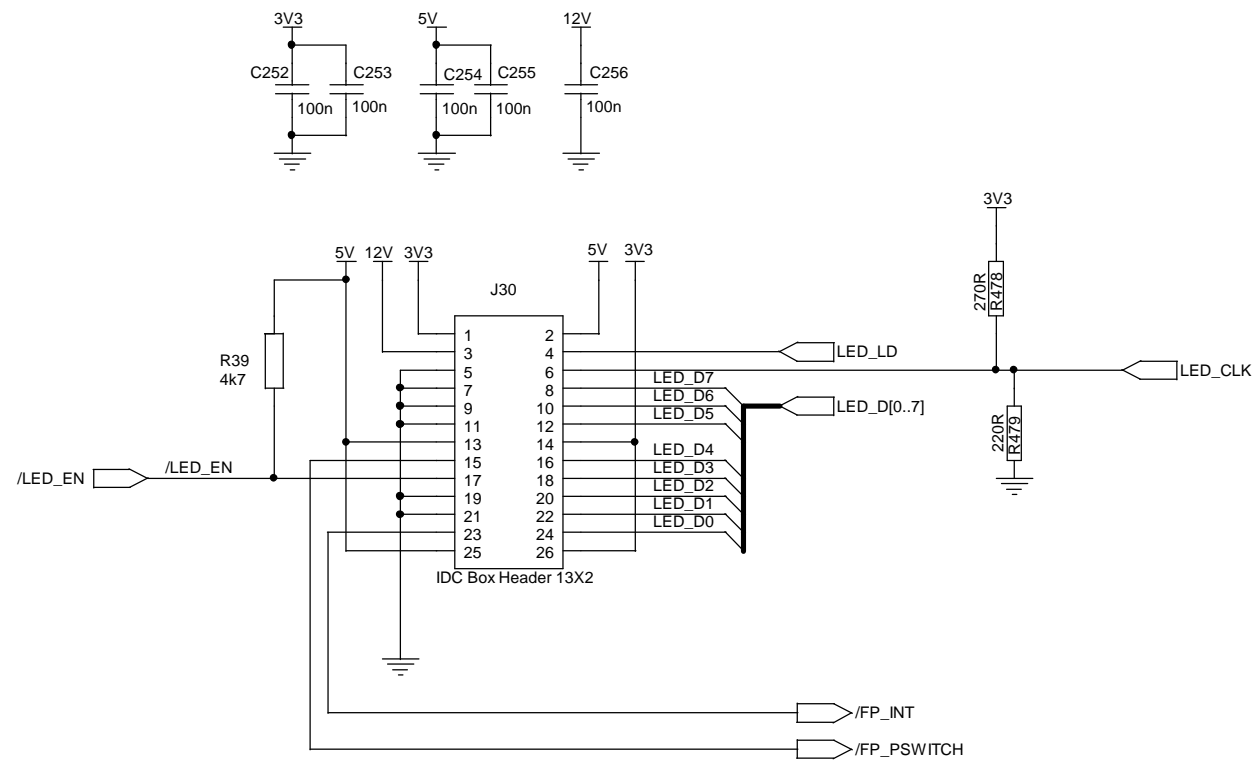
Rabbit Interface



Xilinx_Controller









ENGINEERING CHANGE NOTE:

ECN #: 1034

ASSEMBLY: BIOB101B-A

NEW ASSEMBLY: BIOB101C-A

CURRENT REV: B

NEW REV: C

DESCRIPTION: SX48 Motherboard

DATE: 16/05/2006

Updating PCBs for RoHS compliance. Incorporates ECNs 1026, 1027, and 1028

REASON FOR CHANGE:

- | | | | |
|---|--|--|--------------------------------------|
| <input type="radio"/> Safety Issue | <input type="radio"/> UL Compliance Issue | <input checked="" type="radio"/> New Feature | <input type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix | <input type="radio"/> EMC Compliance Issue | <input checked="" type="radio"/> Quality Issue | |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change | <input type="radio"/> Cosmetic Change | |

PCB silkscreen changed to include RoHS Text, Solder Mask changed to Blue, Plating changed to Au/Ni, and material to FR4-08

Incorporate ECNS 1026, 1027, and 1028

Improve assembly process with larger notches on rear corners

increase pad sizes on U28 (M25P40) to accomodate 8Mbit or larger serial flash devices

COMPATIBILITY MAINTAINED: Yes

DETAILS OF CHANGE:

Update PCB part # from BIOB101B to BIOB101C. Order new top side solder paste stencil

Add new 4k7 resistor R507. Increase Qty of 4k7 0603 resistors (RMD7948) by 1

Add new 100pF capacitor C333. Increase Qty of 100pF 0603 capacitors (CCD1210) by 1

SIGNATURES:

Engineer:

Implementor:

Operations Manager:

Date:



ENGINEERING CHANGE NOTE:

ECN #: 1035

ASSEMBLY: BIOB111B-A**NEW ASSEMBLY:** BIOB111C-A**CURRENT REV:** B**NEW REV:** C**DESCRIPTION:** SX48 LED Board**DATE:** 16/05/2006

Updating PCBs for ROHS compliance.

REASON FOR CHANGE:

- | | | | |
|---|--|--|--------------------------------------|
| <input type="radio"/> Safety Issue | <input type="radio"/> UL Compliance Issue | <input checked="" type="radio"/> New Feature | <input type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix | <input type="radio"/> EMC Compliance Issue | <input checked="" type="radio"/> Quality Issue | |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change | <input type="radio"/> Cosmetic Change | |

PCB silkscreen changed to include RoHS Text, Solder Mask made blue, plating changed to Au/Ni, and material to FR4-08

Move tracks from near front plate mounting screws

COMPATIBILITY MAINTAINED: Yes**DETAILS OF CHANGE:**

Update PCB part# from BIOB111B to BIOB111C.

SIGNATURES:**Engineer:****Implementor:****Operations Manager:****Date:**



ENGINEERING CHANGE NOTE:

ECN #: 1036

ASSEMBLY: BIOB121B-A**NEW ASSEMBLY:** BIOB121C-A**CURRENT REV:** B**NEW REV:** C**DESCRIPTION:** SX48 Sync board**DATE:** 16/05/2006

Updating PCBs for RoHS compliance. Incorporates ECNs 1029, and 1030

REASON FOR CHANGE:

- | | | | |
|---|--|--|--------------------------------------|
| <input type="radio"/> Safety Issue | <input type="radio"/> UL Compliance Issue | <input checked="" type="radio"/> New Feature | <input type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix | <input type="radio"/> EMC Compliance Issue | <input checked="" type="radio"/> Quality Issue | |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change | <input type="radio"/> Cosmetic Change | |

PCB silkscreen changed to include RoHS Text, Solder Mask changed to Blue, Plating changed to Au/Ni, and material to FR4-08

Incorporate ECNS 1029, and 1030

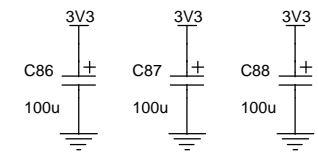
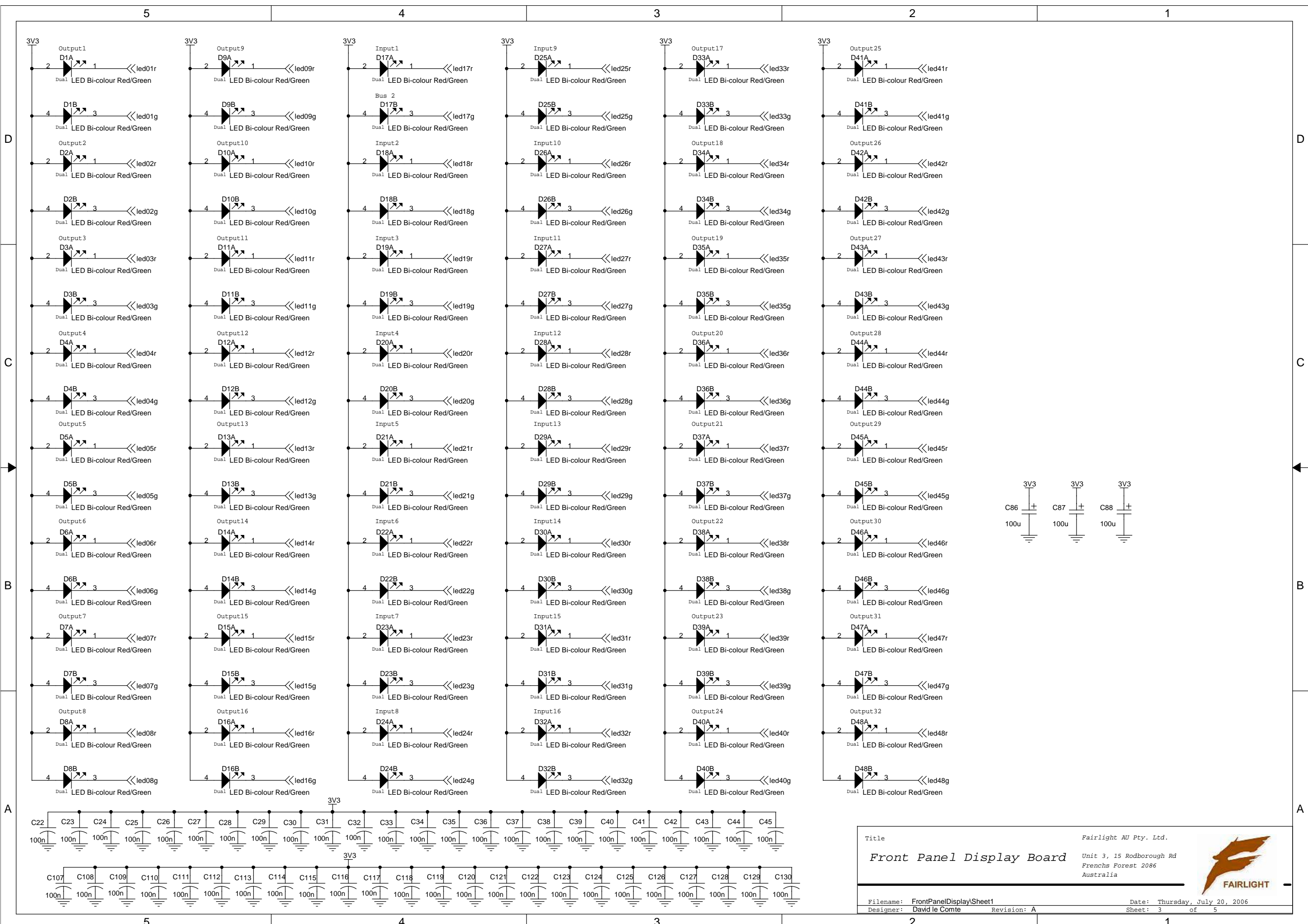
COMPATIBILITY MAINTAINED: Yes**DETAILS OF CHANGE:**

Update PCB part # from BIOB101B to BIOB101C. Order new top side solder paste stencil


Add new part C142, 100pF, 0603 capacitor (CCD1210)

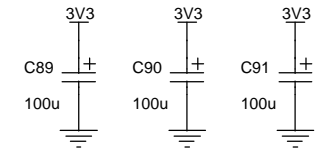
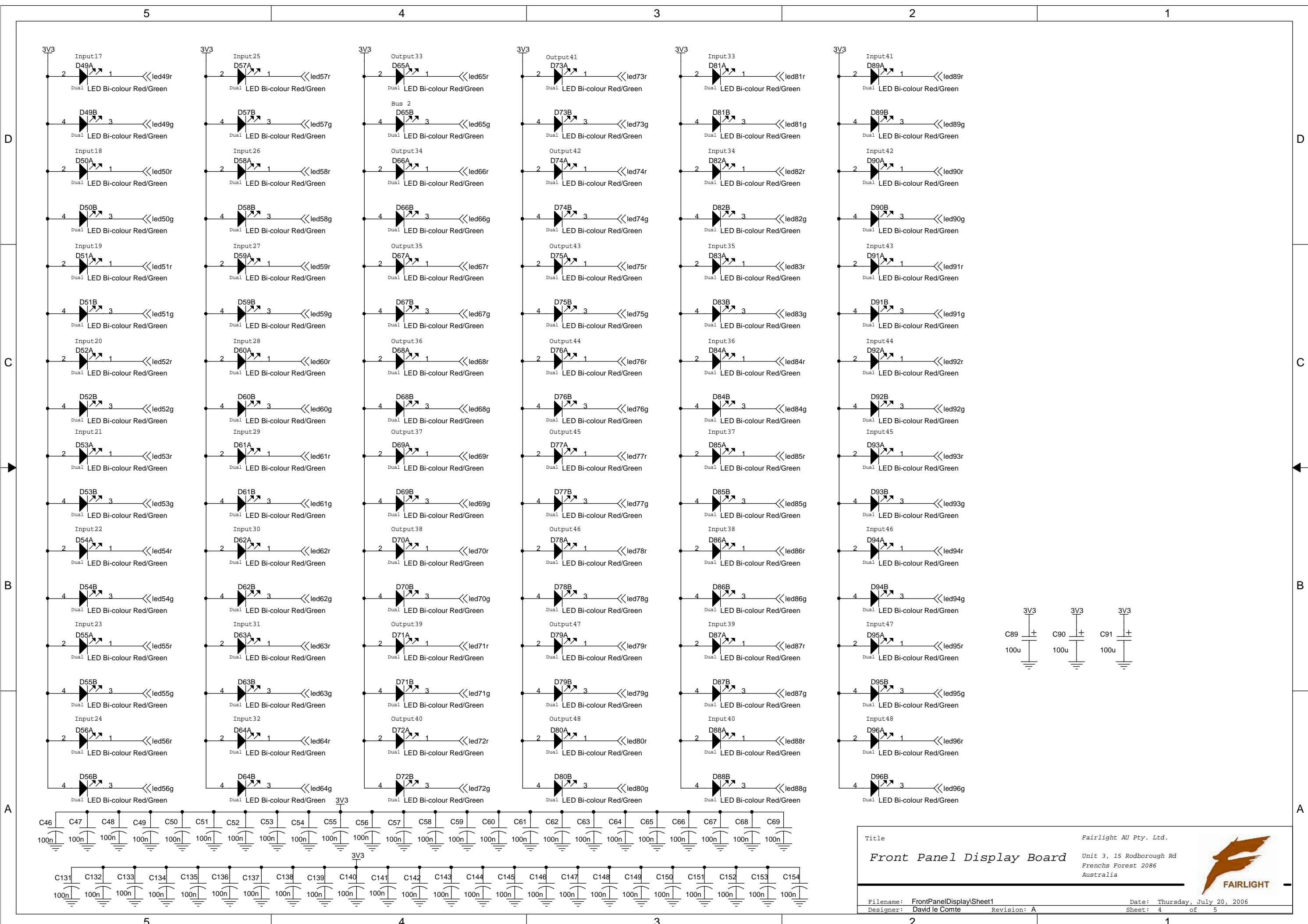
Change C89 from an 0603, 15pF capacitor (CCD7587) to a 1nF, 0603 capacitor (CCD1216)

SIGNATURES:**Engineer:****Implementor:****Operations Manager:****Date:**




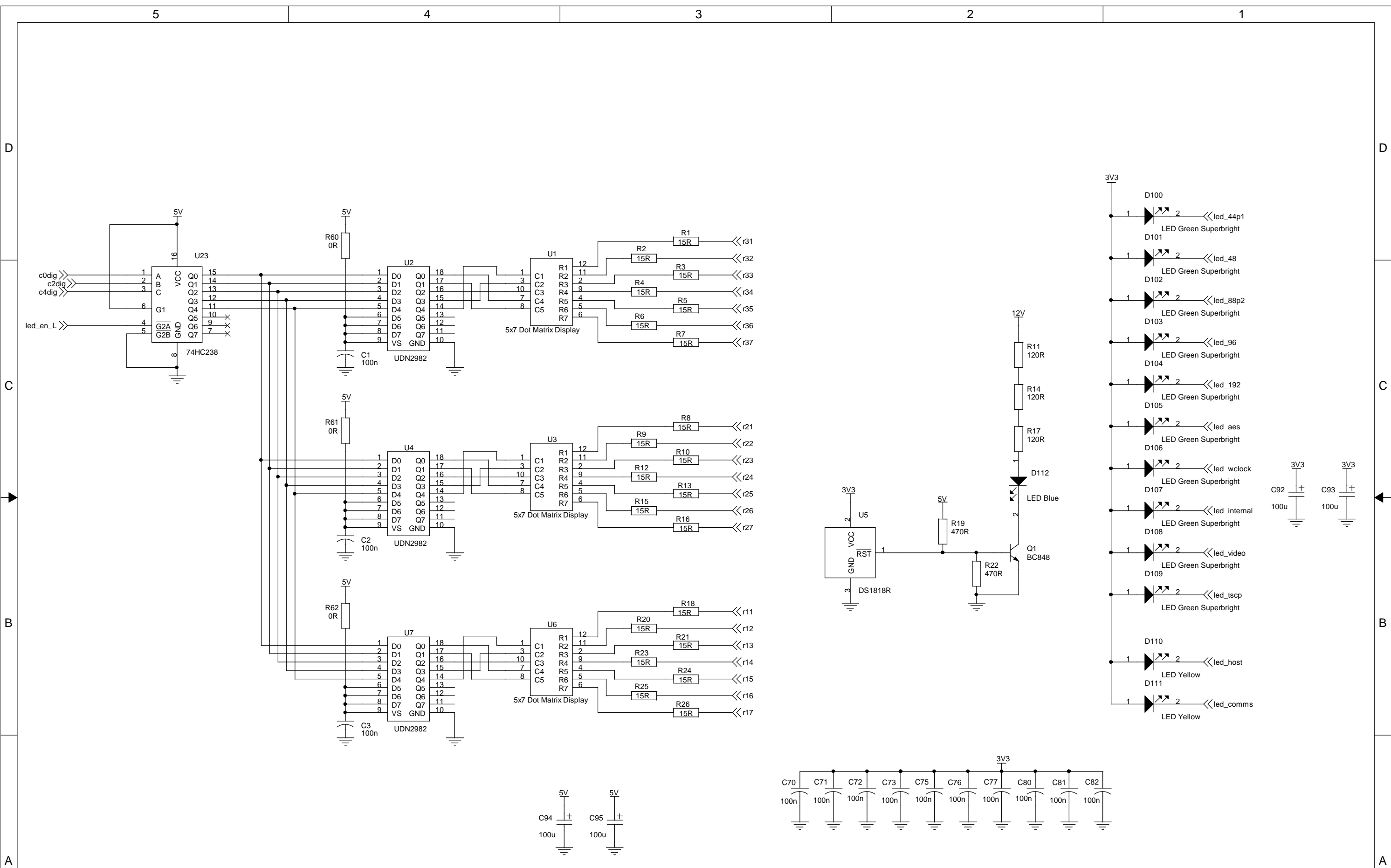
| | |
|-----------------------------------|--|
| Title | Fairlight AU Pty. Ltd. |
| Front Panel Display Board | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia |
| Filename: FrontPanelDisplaySheet1 | Date: Thursday, July 20, 2006 |
| Designer: David le Comte | Revision: A |
| | Sheet: 3 of 5 |

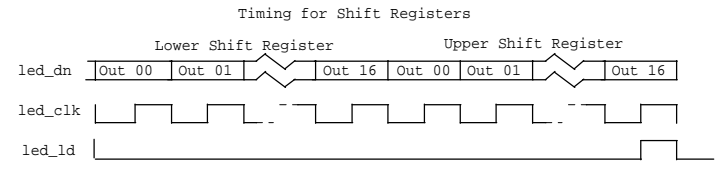
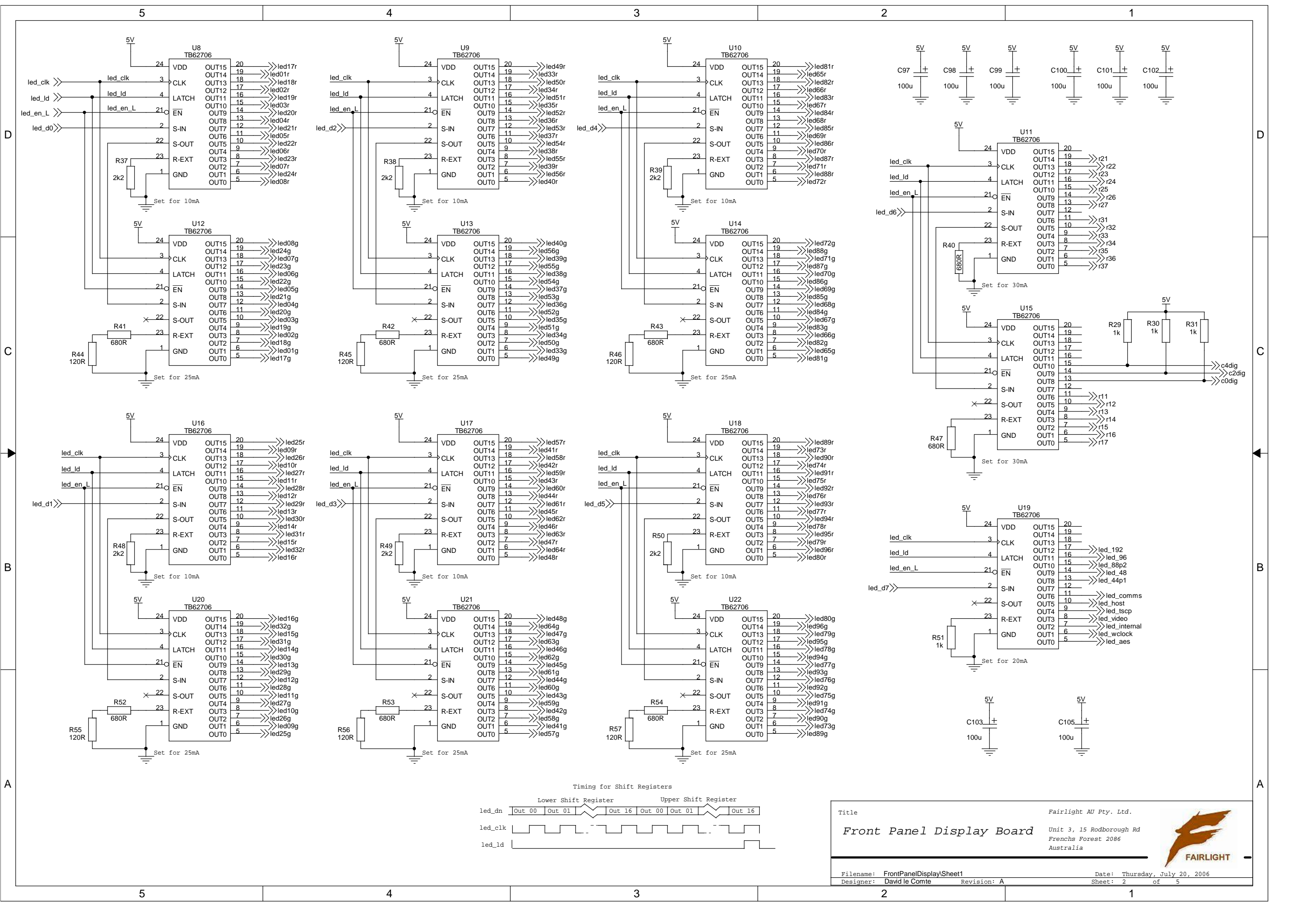





| | |
|-----------------------------------|--|
| Title | Fairlight AU Pty. Ltd. |
| Front Panel Display Board | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia |
| Filename: FrontPanelDisplaySheet1 | Date: Thursday, July 20, 2006 |
| Designer: David le Comte | Revision: A |
| | Sheet: 4 of 5 |

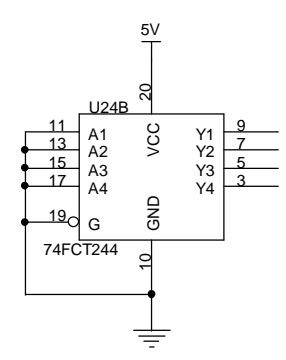
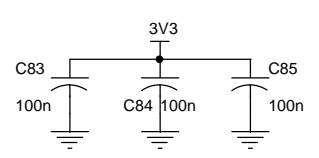
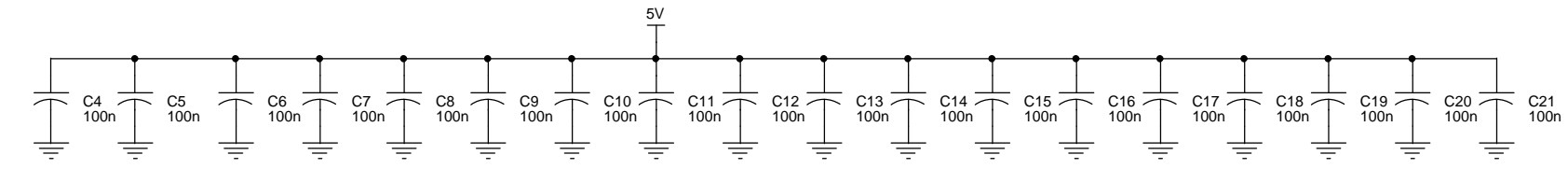
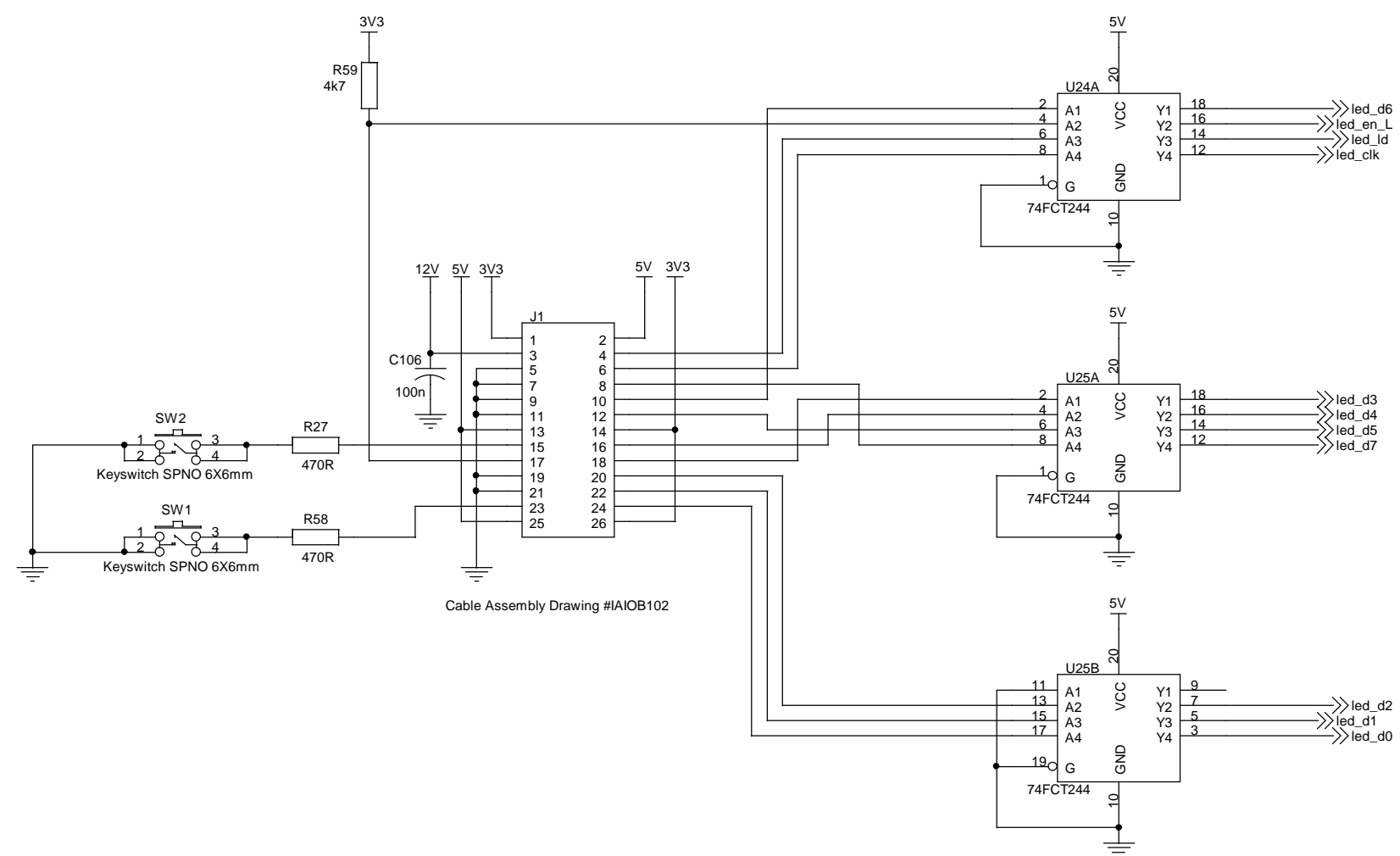


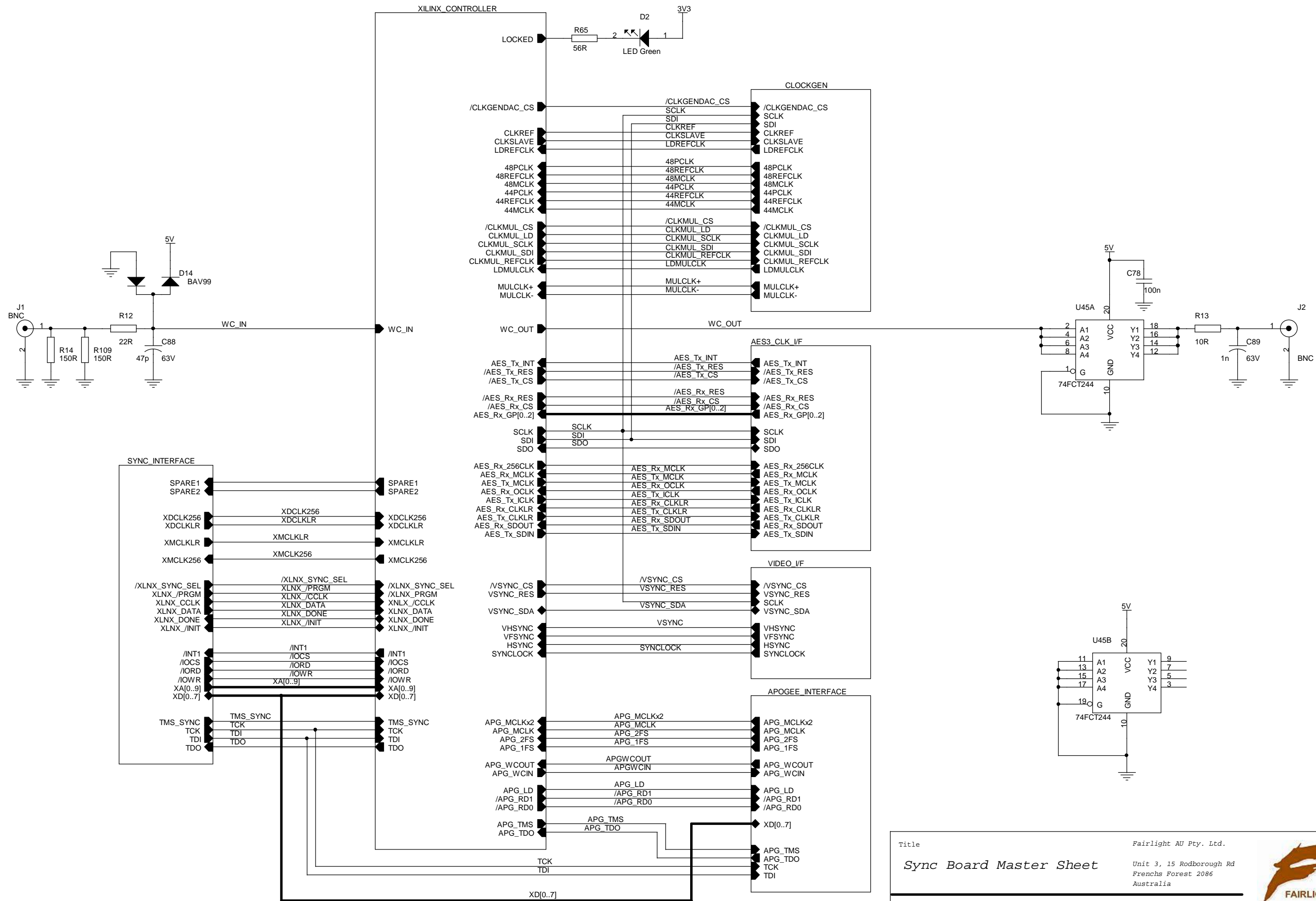


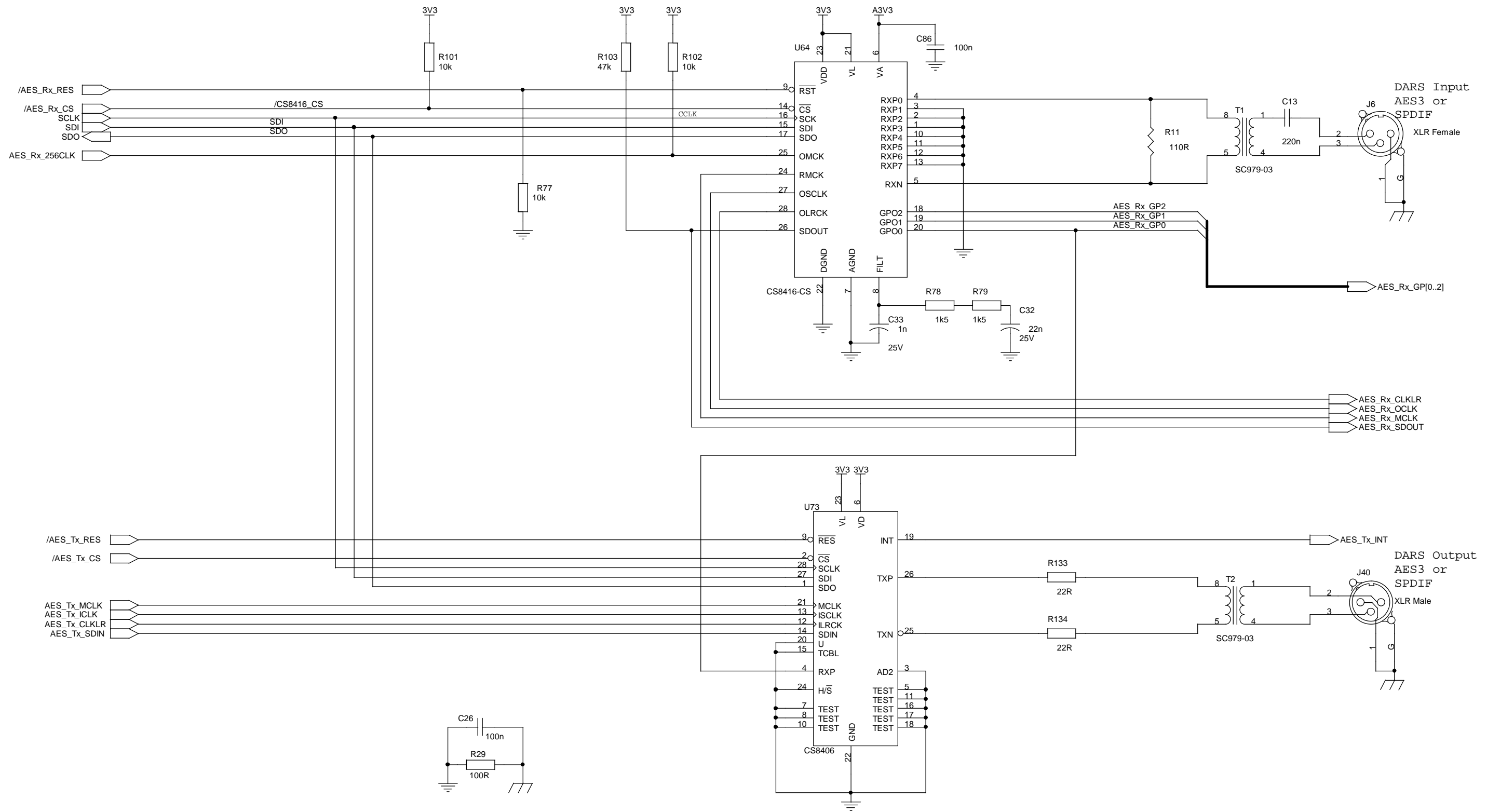


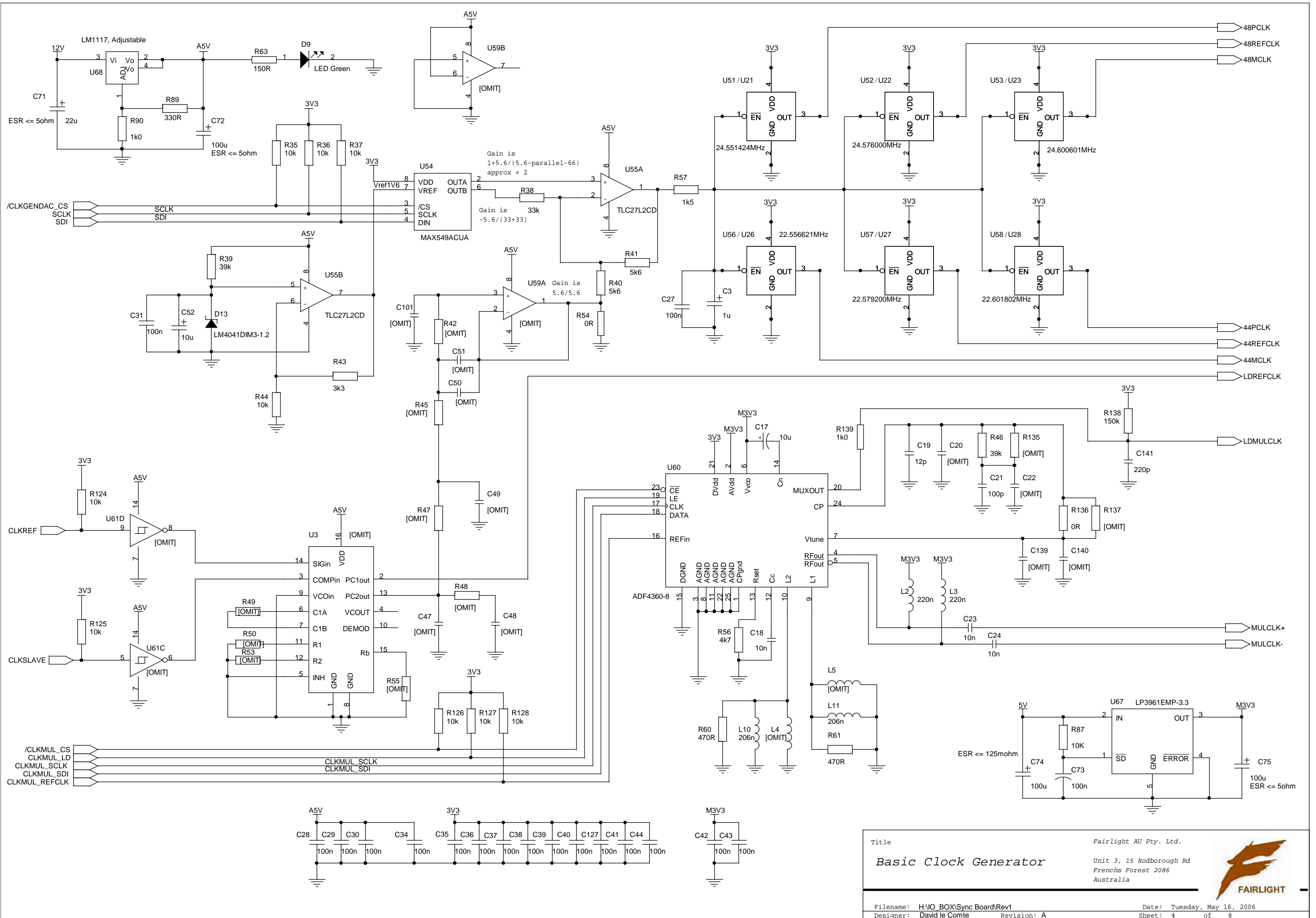
| | |
|-----------------------------------|--|
| Title | Fairlight AU Pty. Ltd. |
| Front Panel Display Board | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia |
| Filename: FrontPanelDisplaySheet1 | Date: Thursday, July 20, 2006 |
| Designer: David le Comte | Revision: A |
| | Sheet: 2 of 5 |

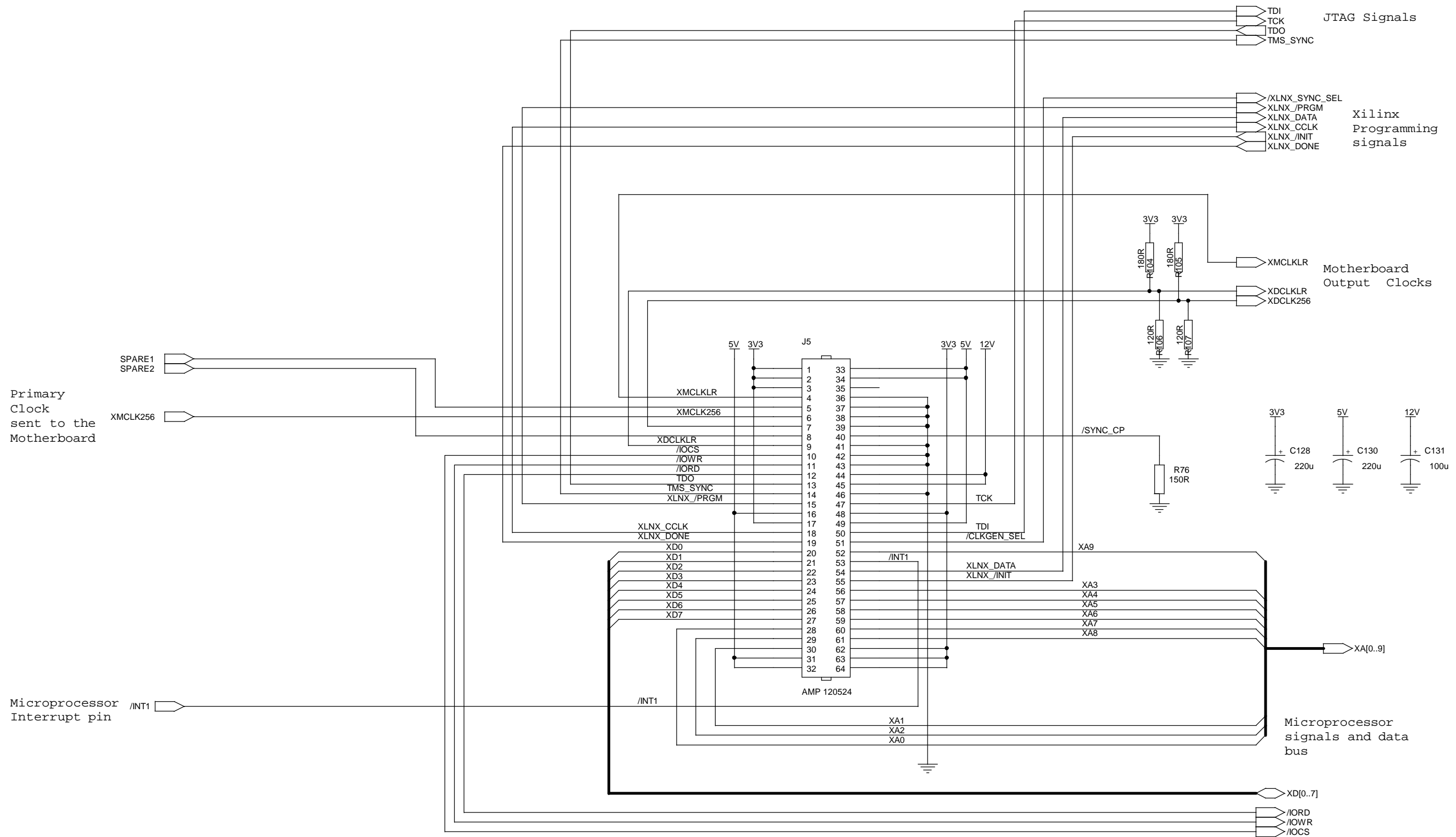












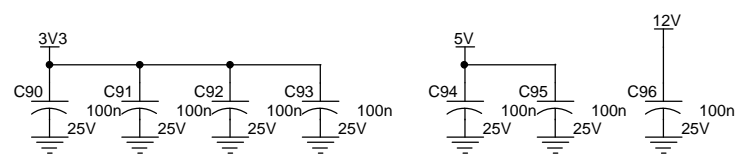
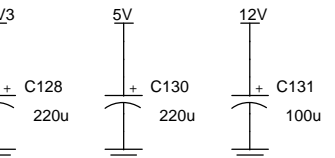
Primary Clock sent to the Motherboard

Microprocessor Interrupt pin


JTAG Signals

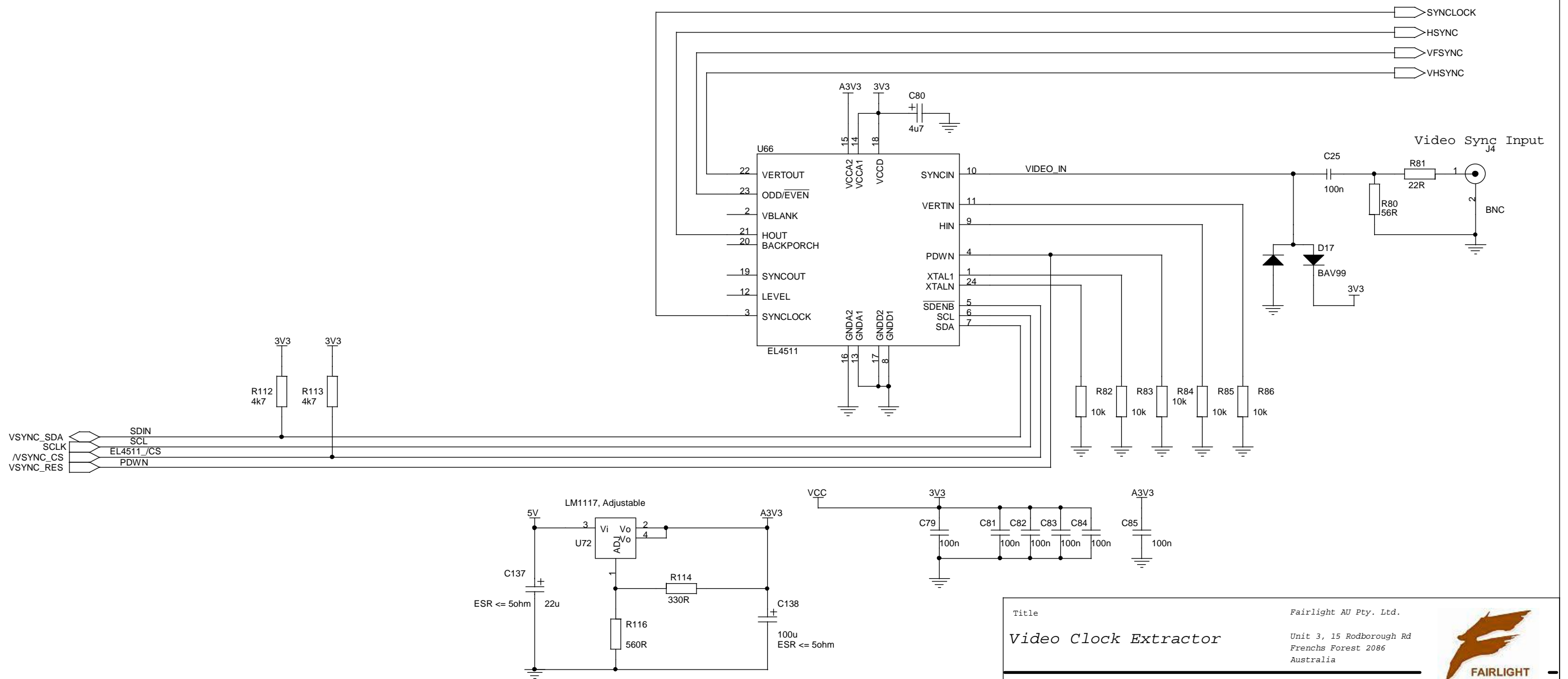
Xilinx Programming signals

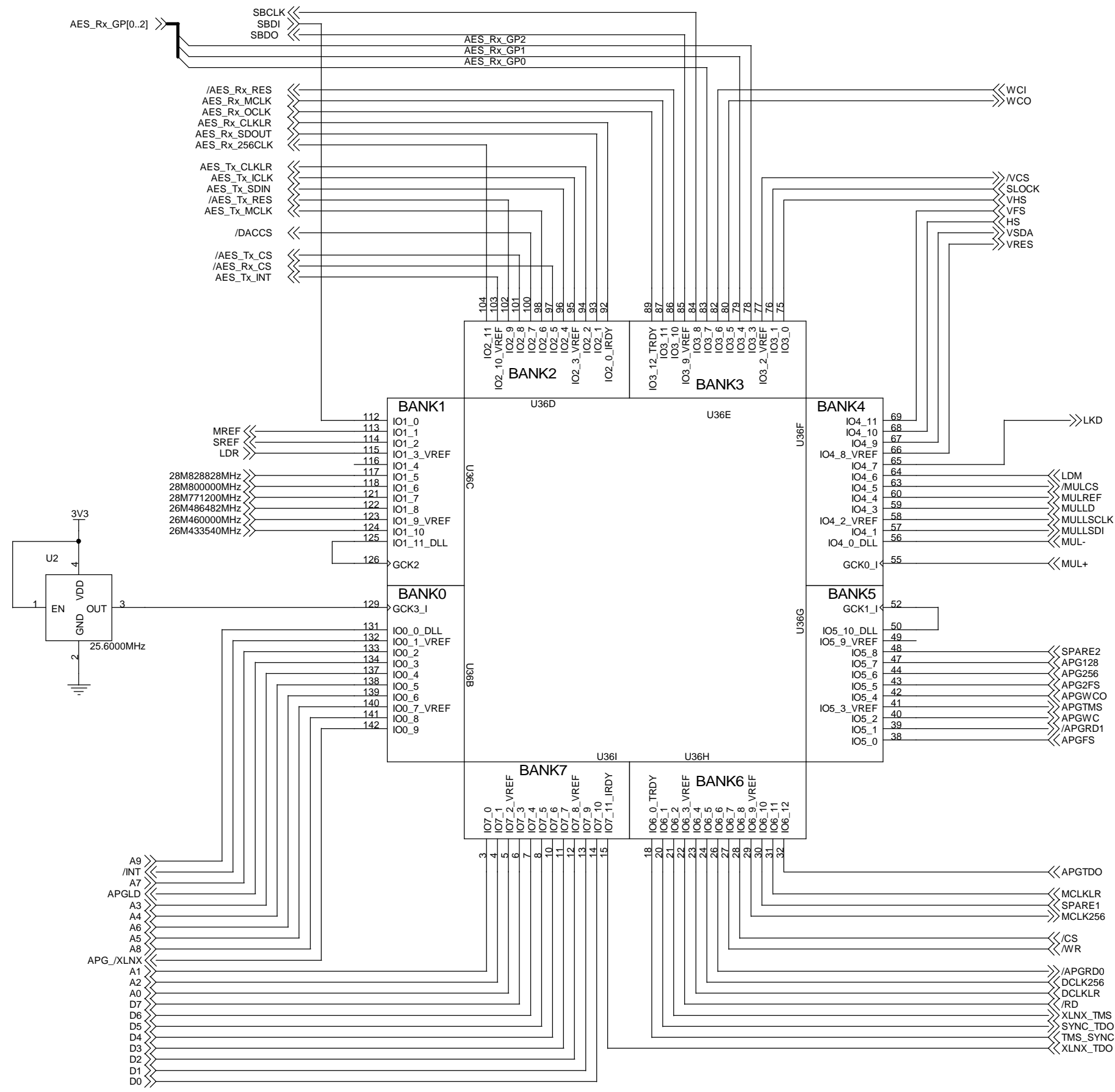
Motherboard Output Clocks



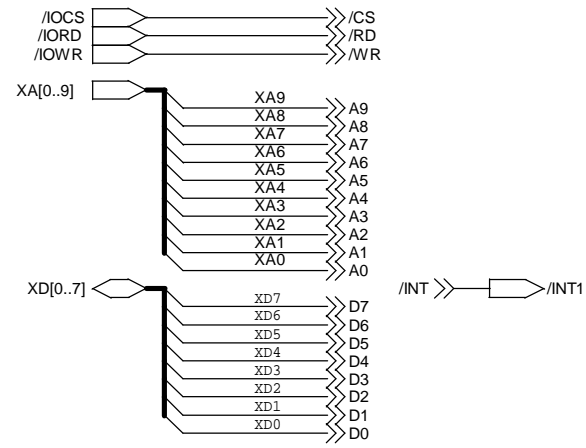
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|-------------------------------------|--|
| Title | Fairlight AU Pty. Ltd. |
| Sync Board Interface | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia |
| Filename: H:\IO_BOX\Sync Board\Rev1 | Date: Tuesday, May 16, 2006 |
| Designer: David le Comte | Revision: A |
| | Sheet: 2 of 8 |



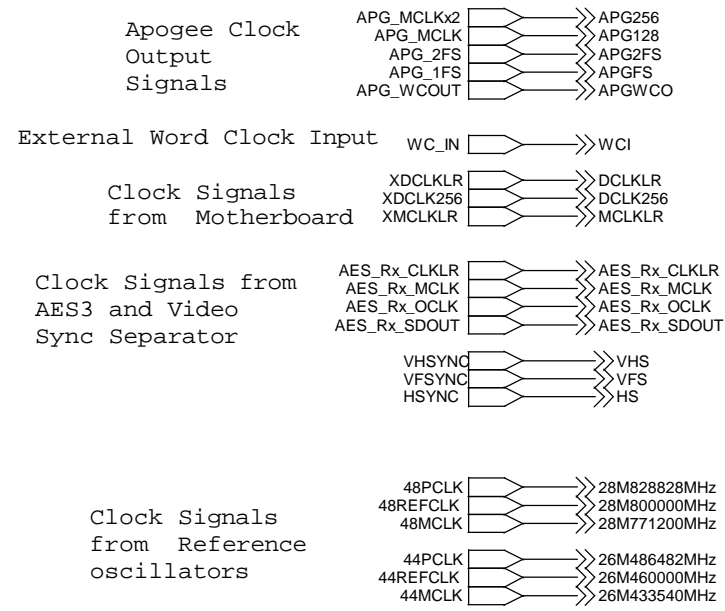




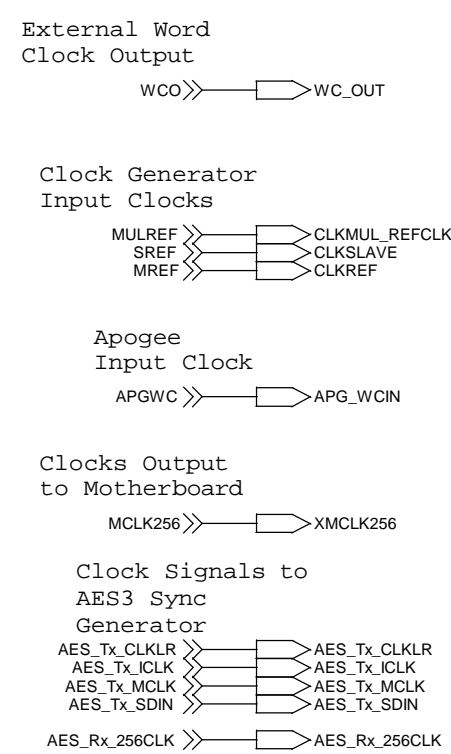
IO Bus Signals



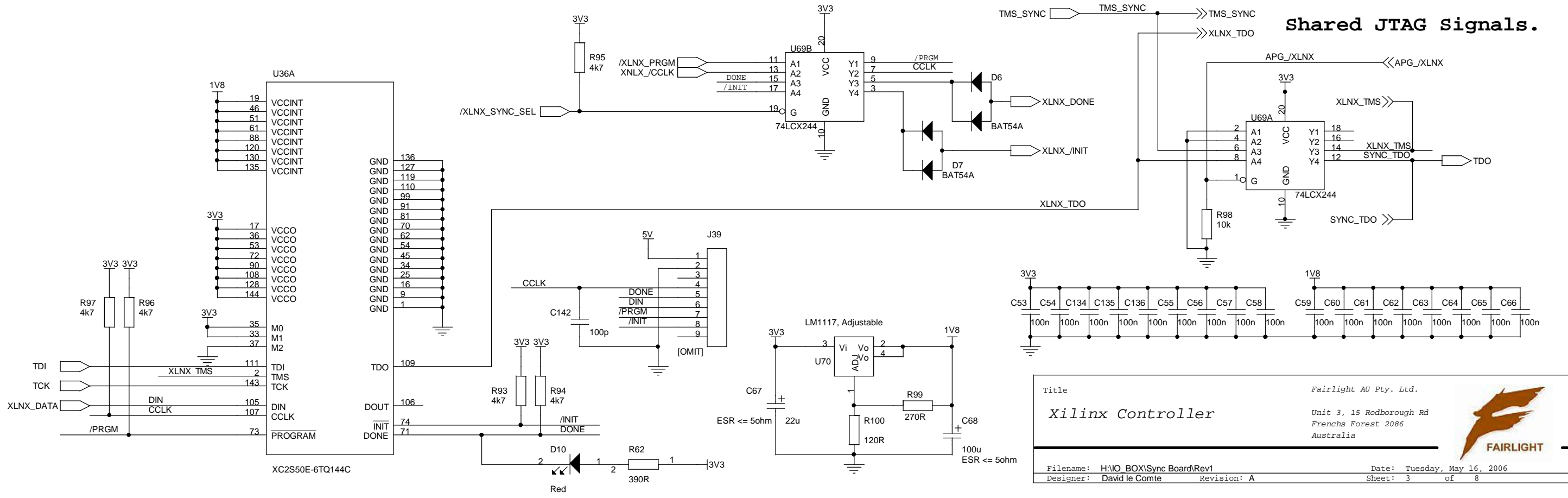
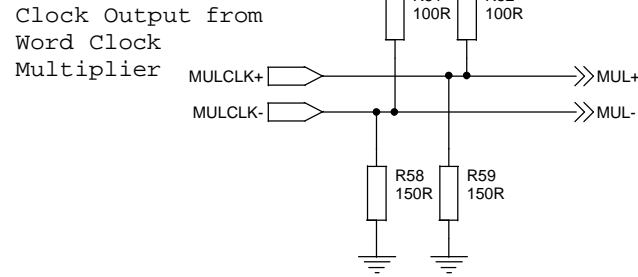
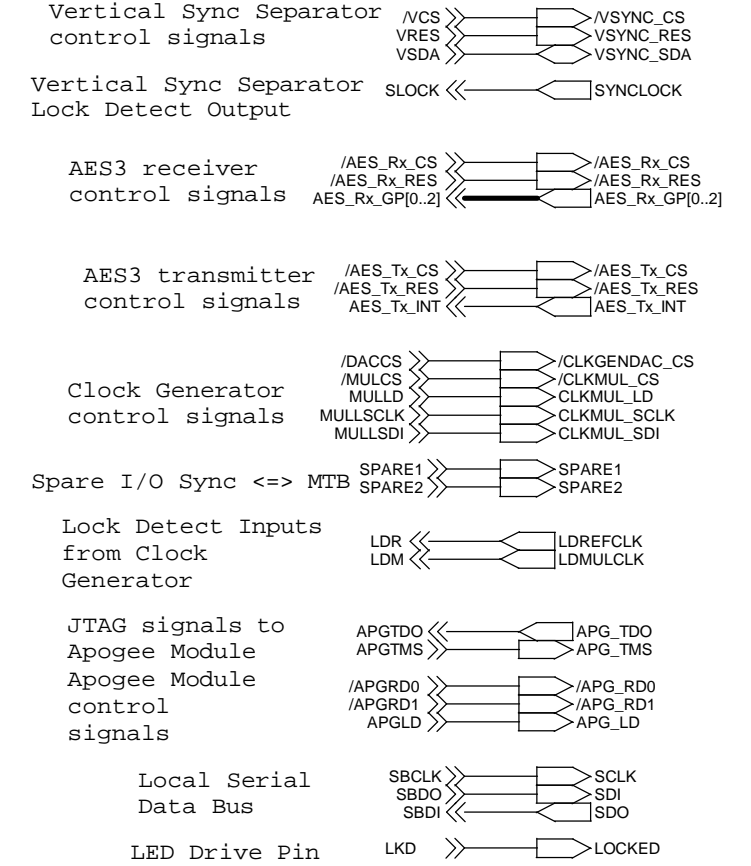
Clock Inputs



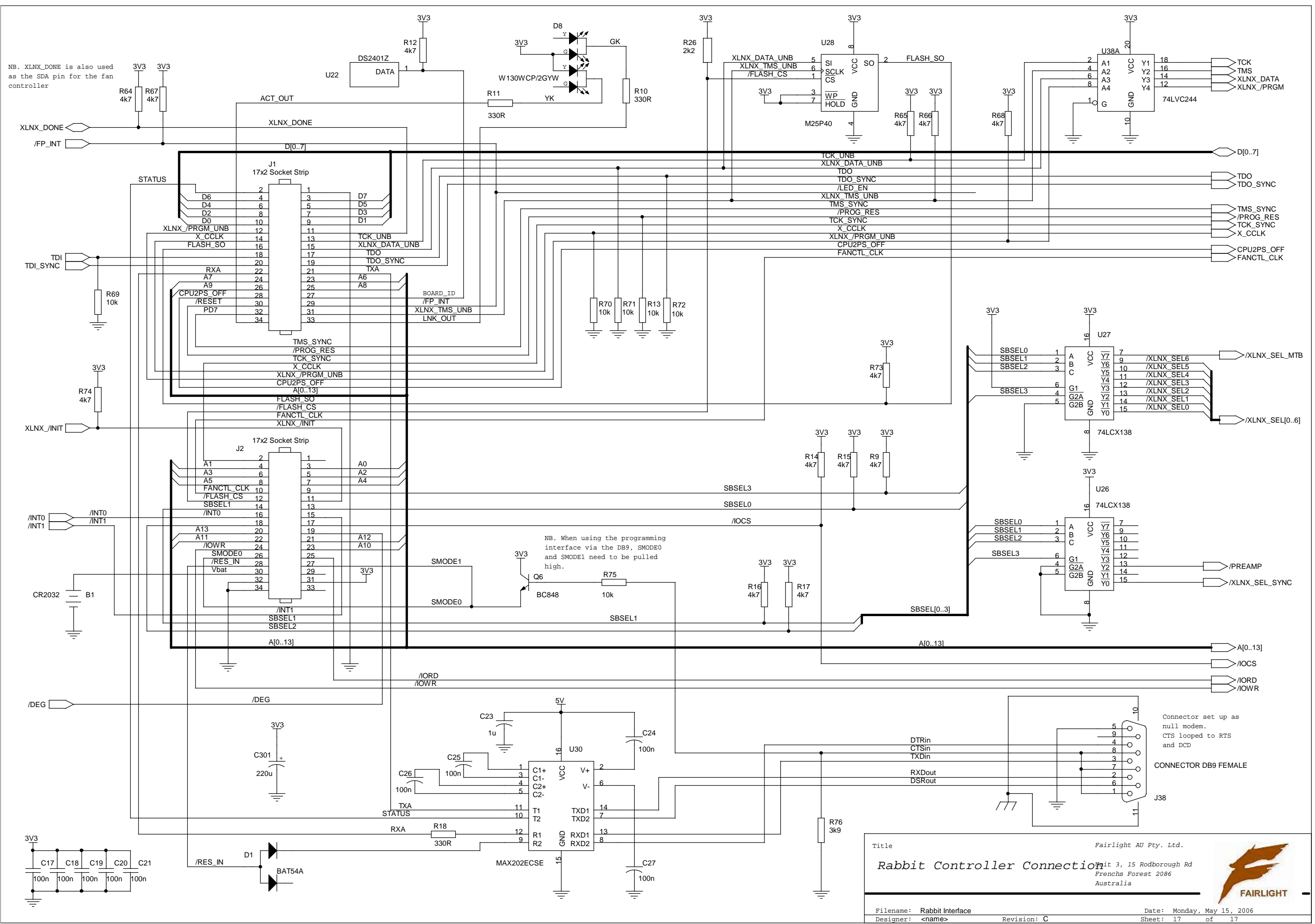
Clock Outputs

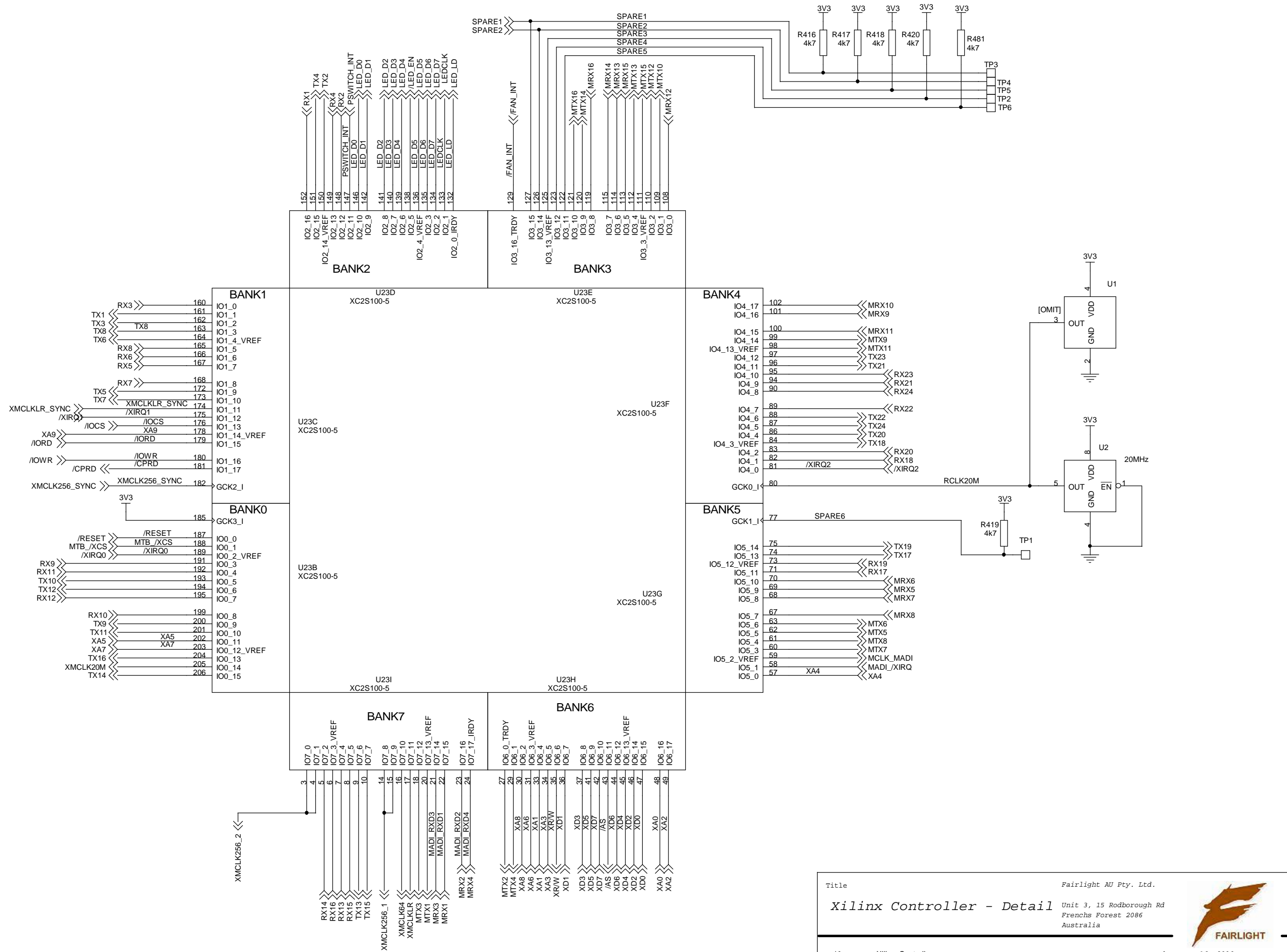


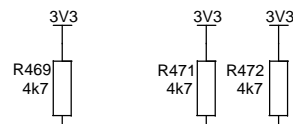
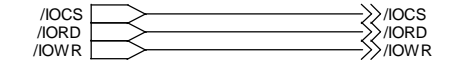
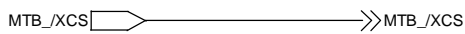
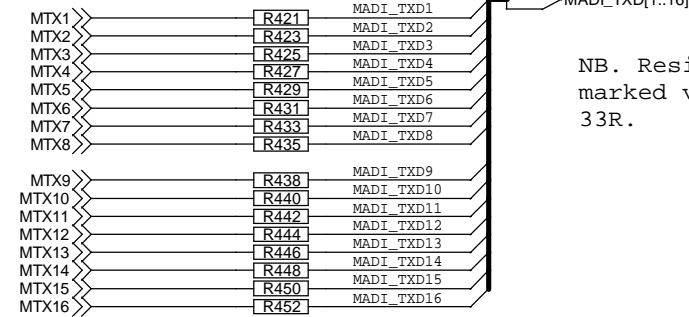
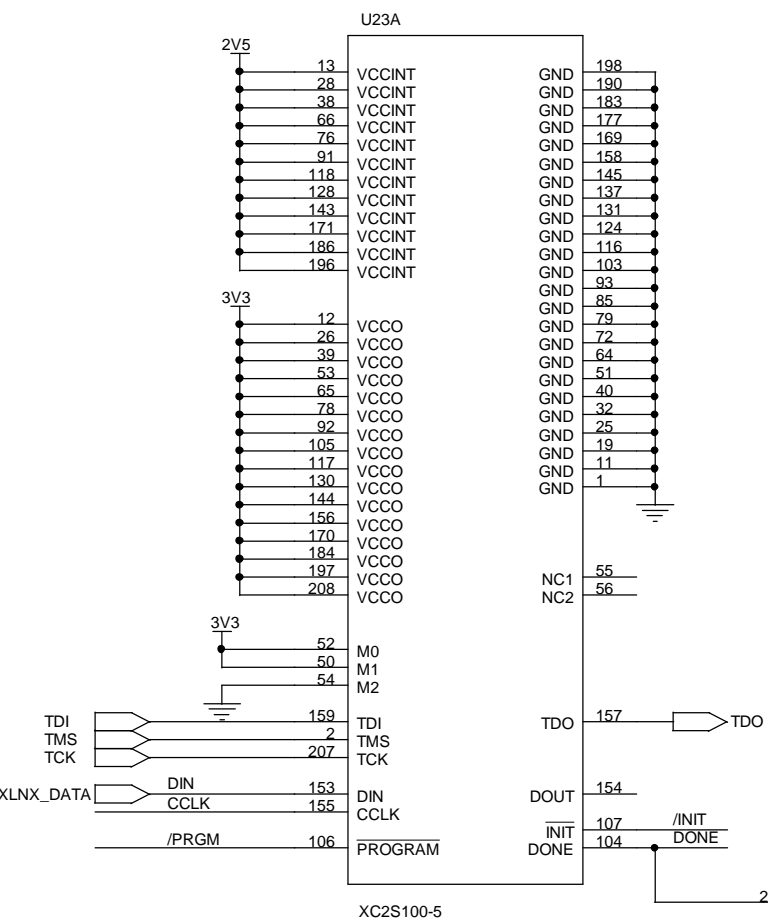
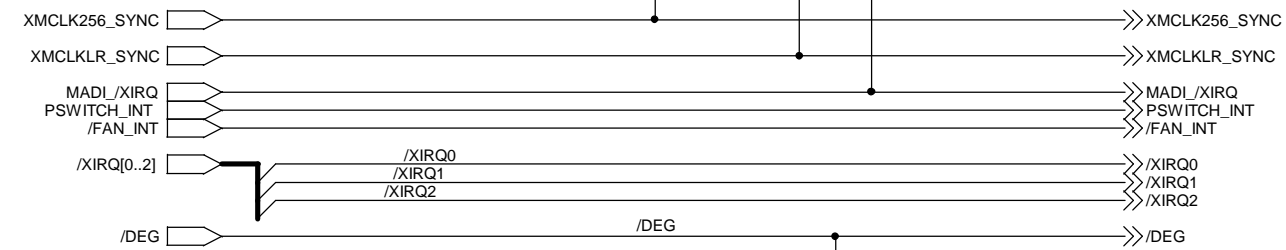
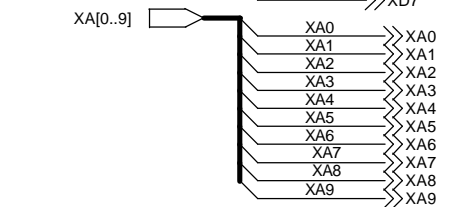
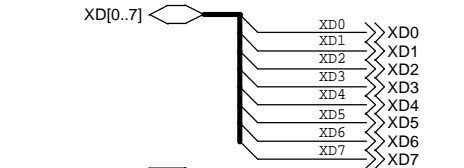
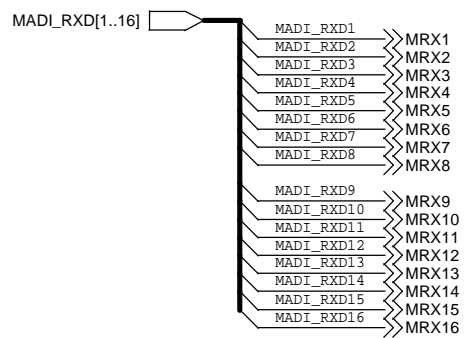
Control Signals



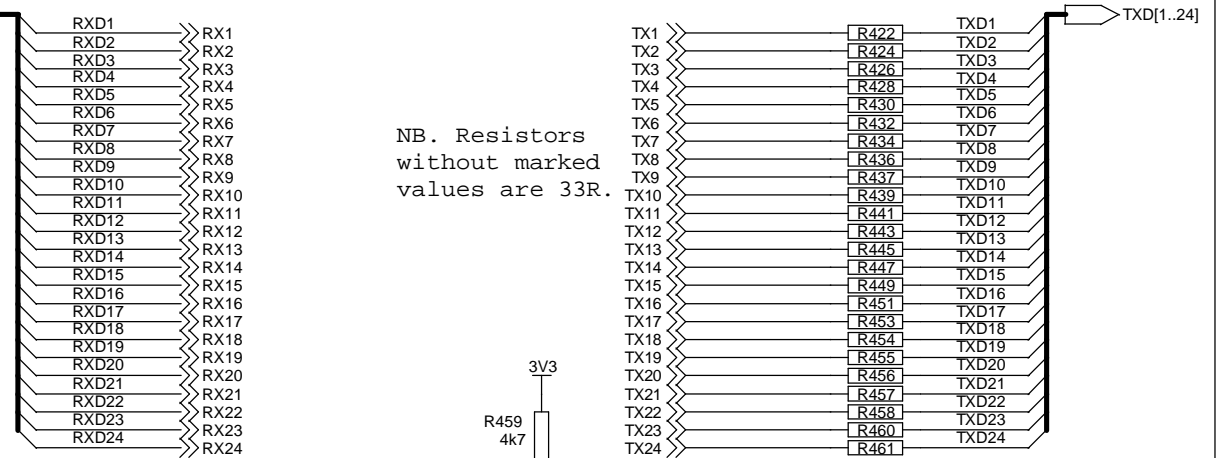
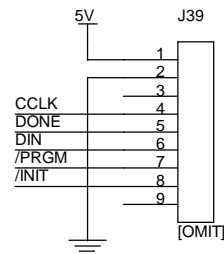
NB. XLNX_DONE is also used as the SDA pin for the fan controller



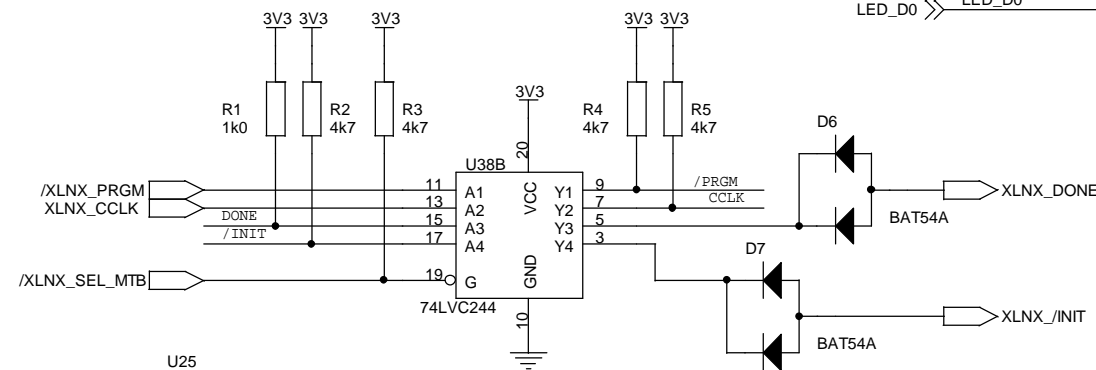
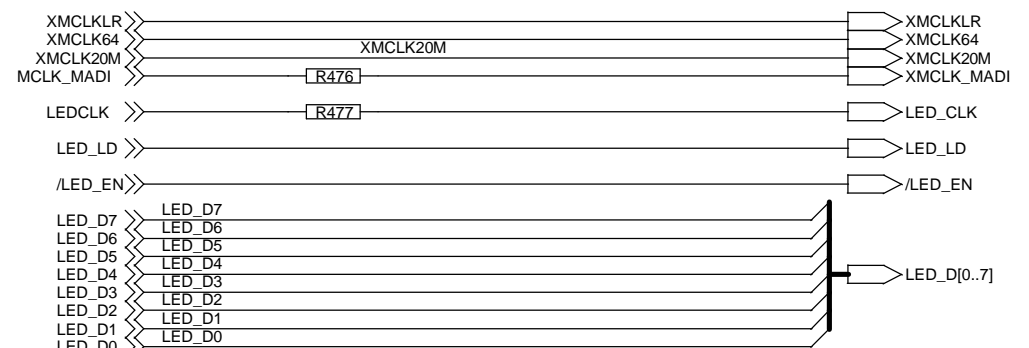
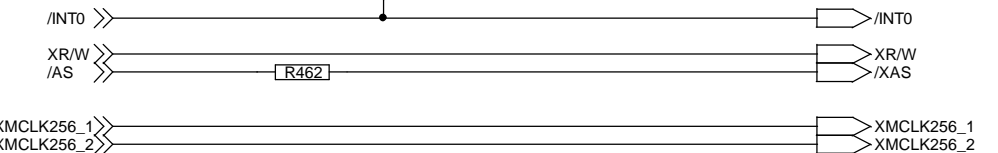




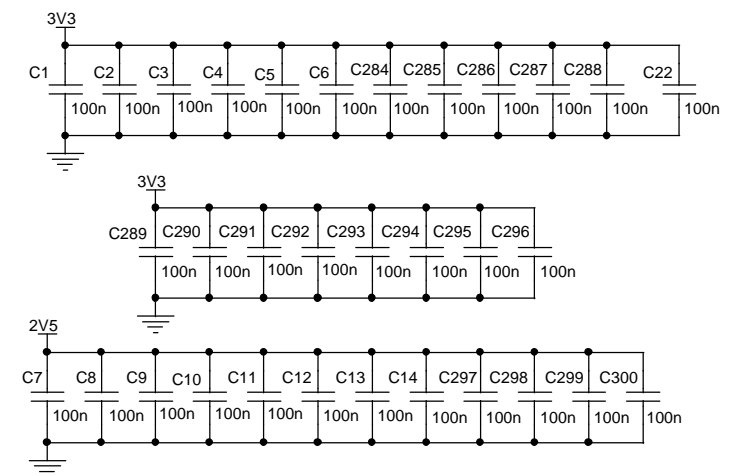
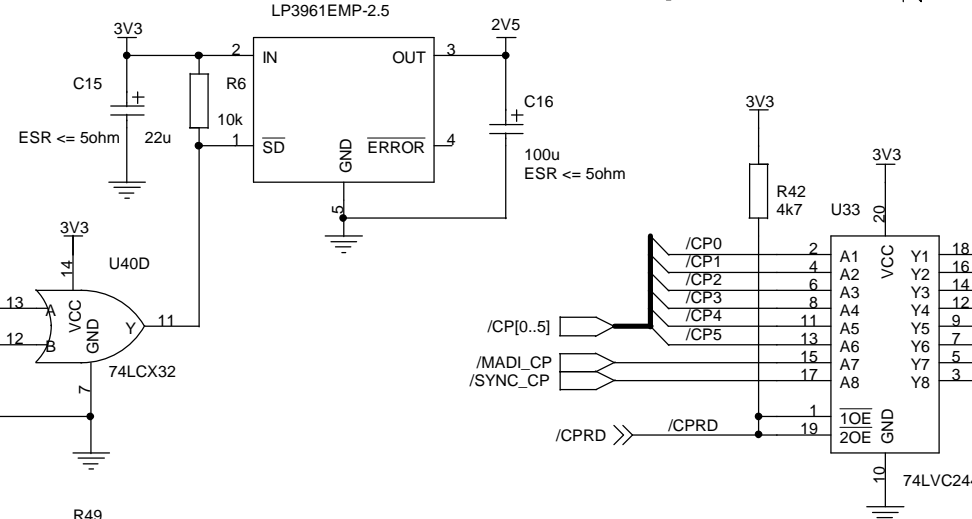
NB. Resistors without marked values are 33R.

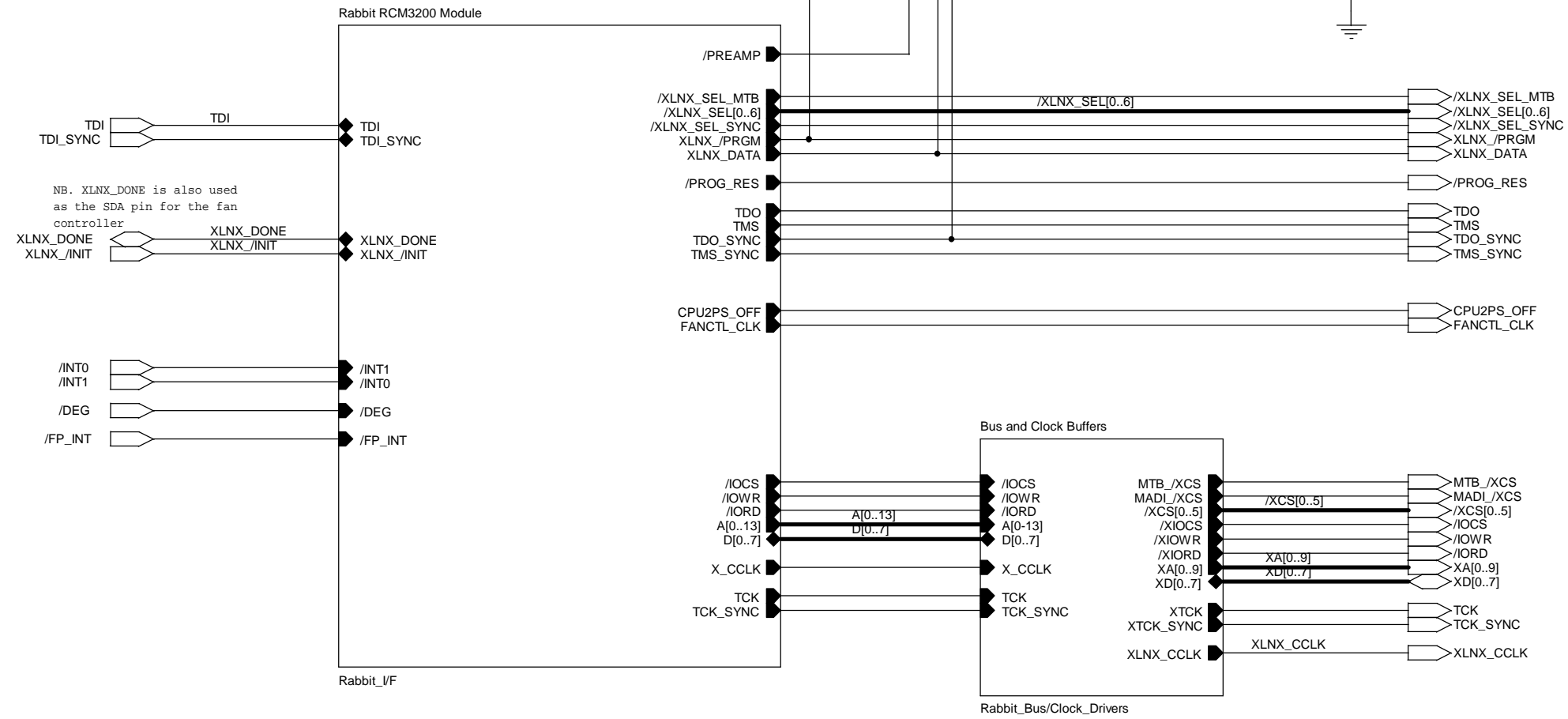
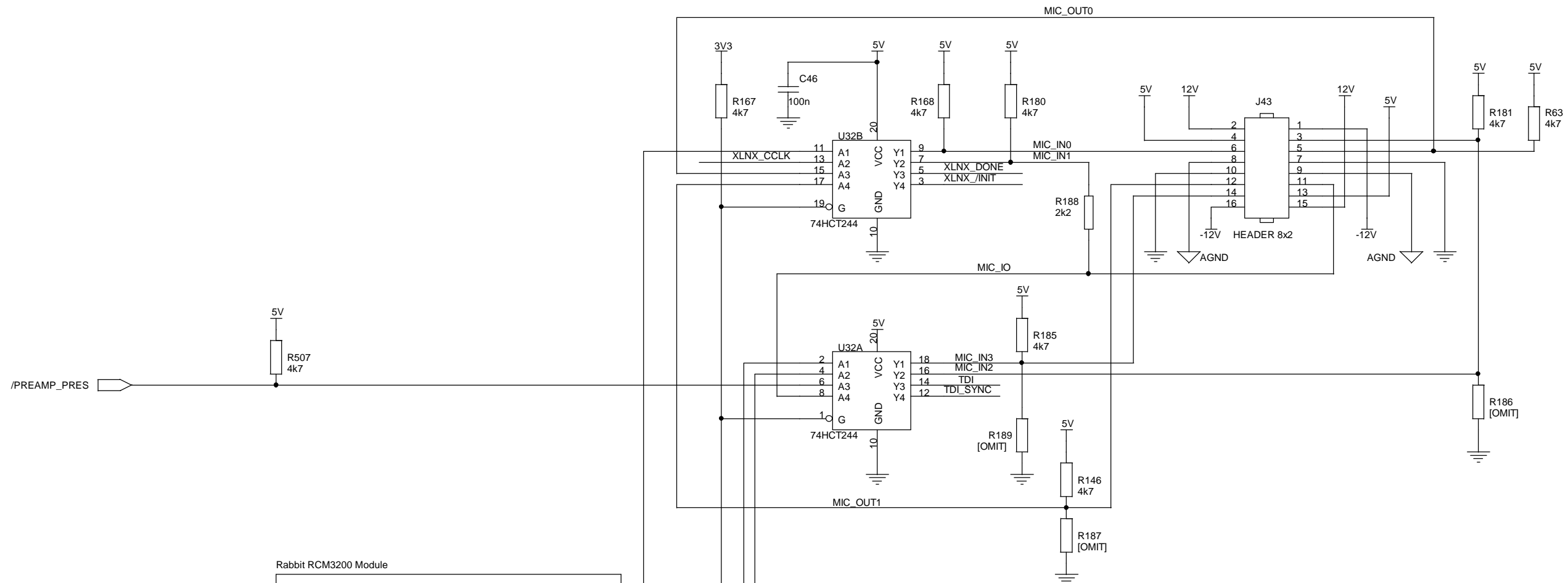


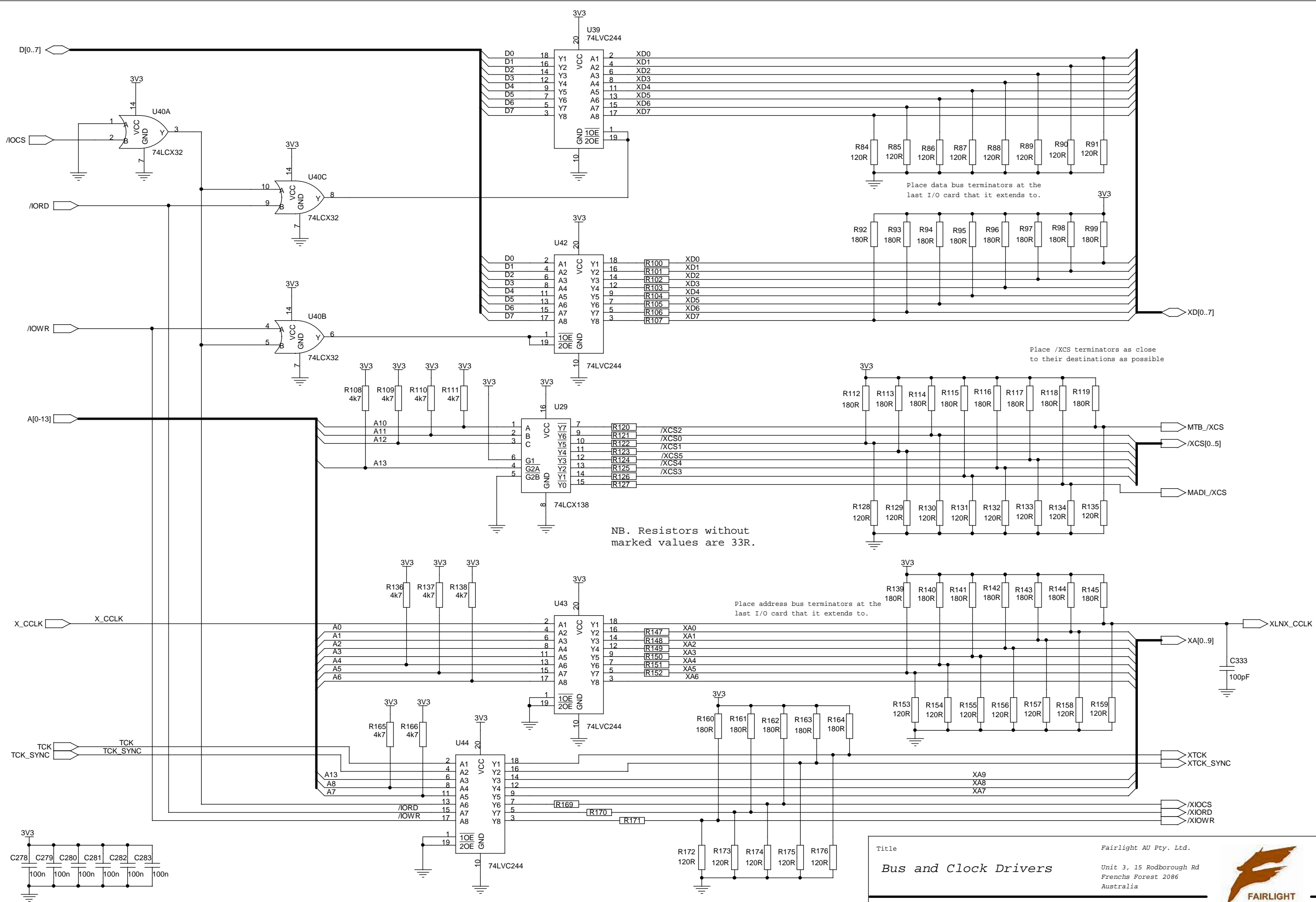
NB. Resistors without marked values are 33R.



NB. Resistors without marked values are 33R.

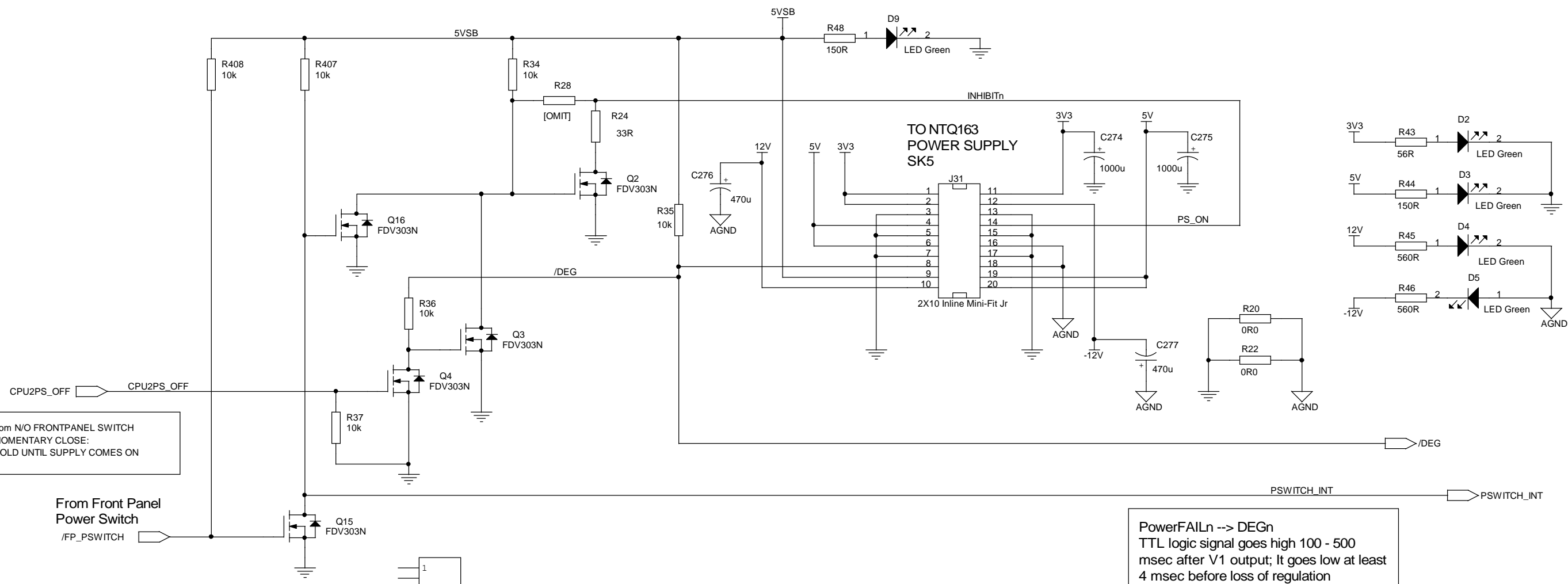
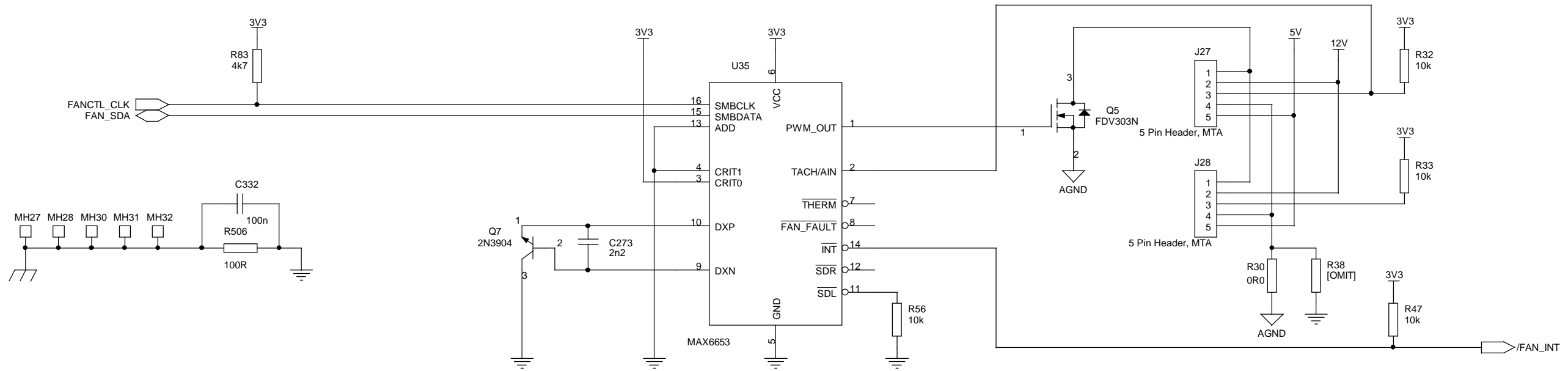






NB. Resistors without marked values are 33R.

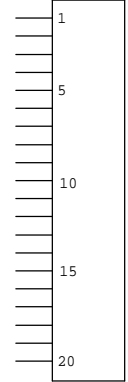


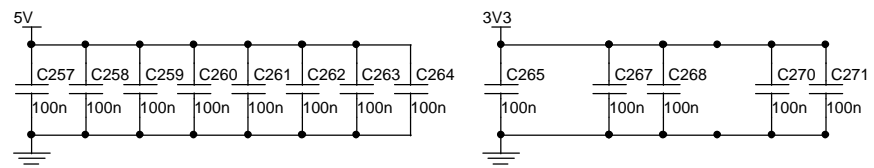
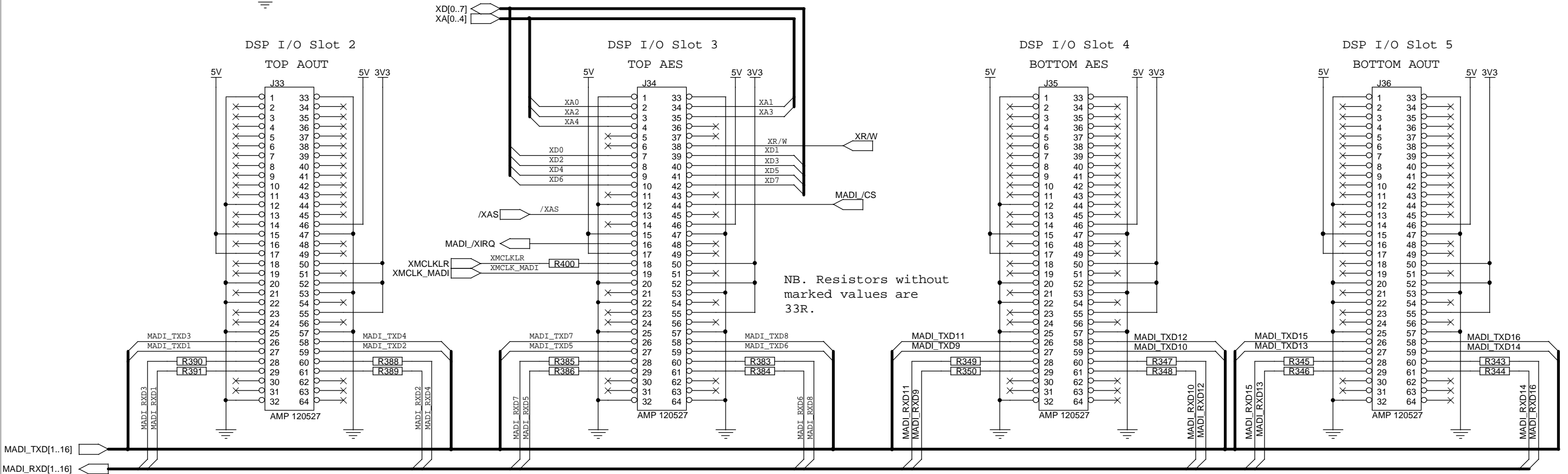
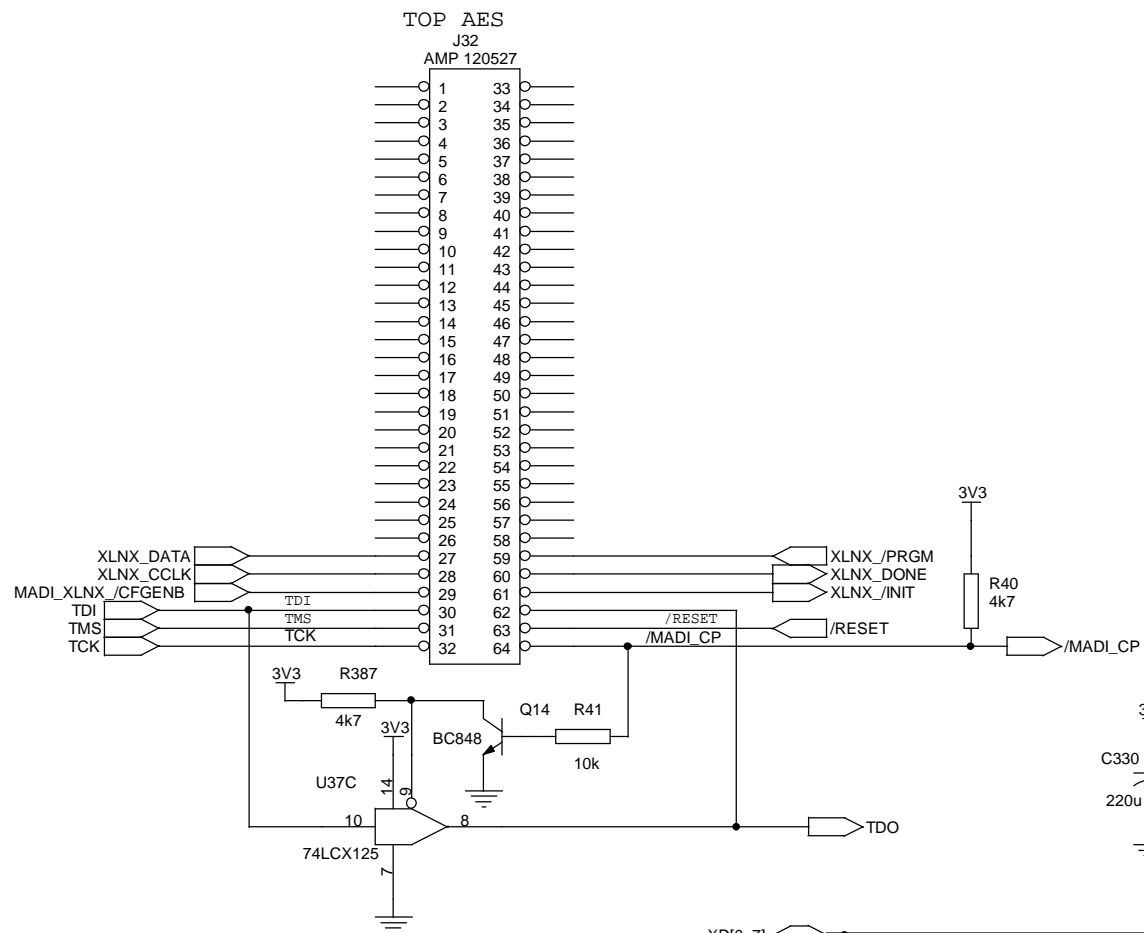


from N/O FRONT PANEL SWITCH
MOMENTARY CLOSE:
HOLD UNTIL SUPPLY COMES ON


PowerFAILn --> DEGn
TTL logic signal goes high 100 - 500
msec after V1 output; it goes low at least
4 msec before loss of regulation

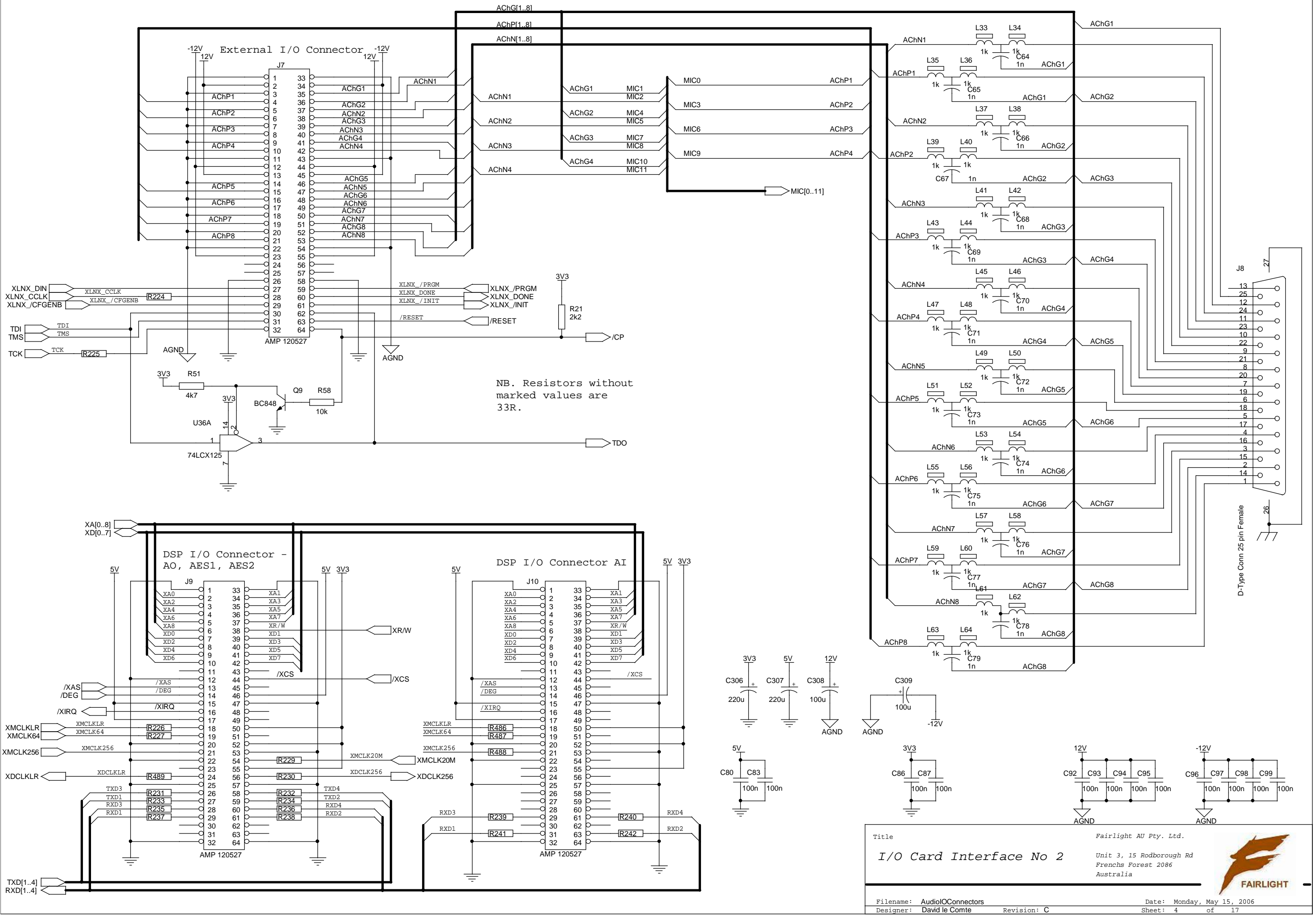
Molex 39-01-2200 Housing
Molex 39-00-0039 Crimp Pins





| | |
|--------------------------|--|
| Title | Fairlight AU Pty. Ltd. |
| MADI Interface | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia |
| Filename: MADI Interface | Date: Monday, May 15, 2006 |
| Designer: David le Comte | Revision: C |
| | Sheet: 10 of 17 |






NB. Resistors without marked values are 33R.

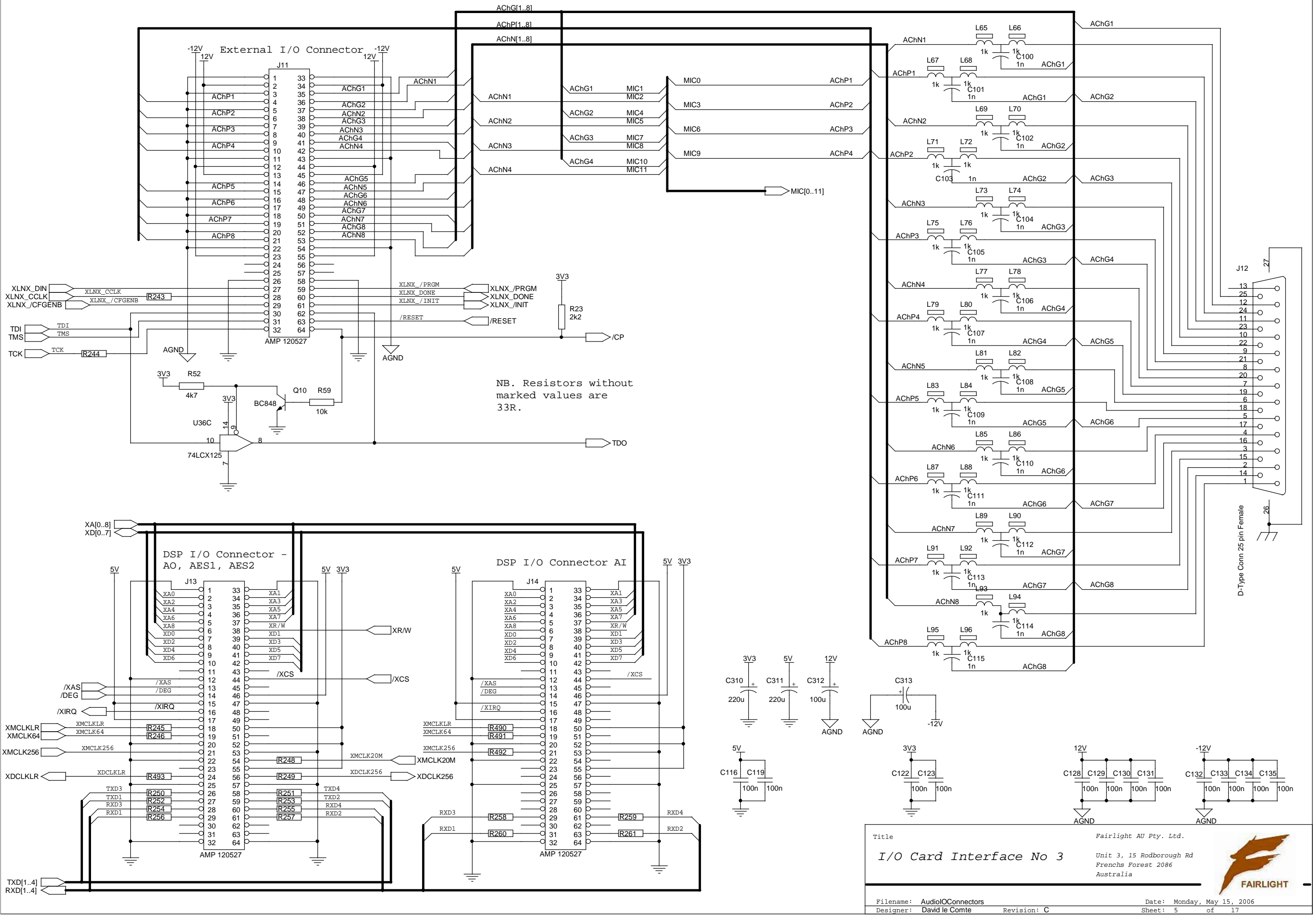
Title: I/O Card Interface No 2

Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia

Filename: AudioIOConnectors
 Designer: David le Comte

Date: Monday, May 15, 2006
 Revision: C
 Sheet: 4 of 17






NB. Resistors without marked values are 33R.

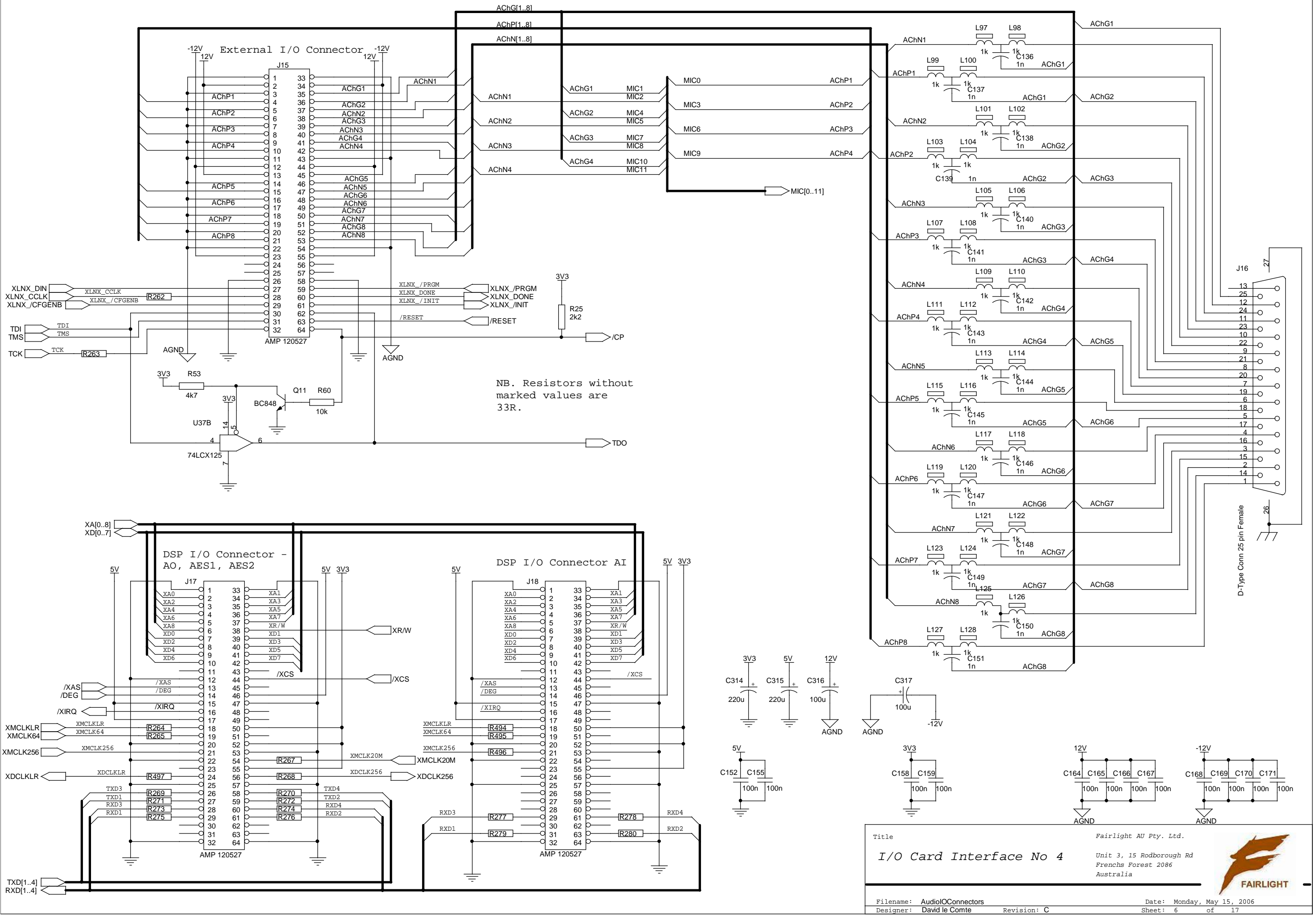
Title: I/O Card Interface No 3

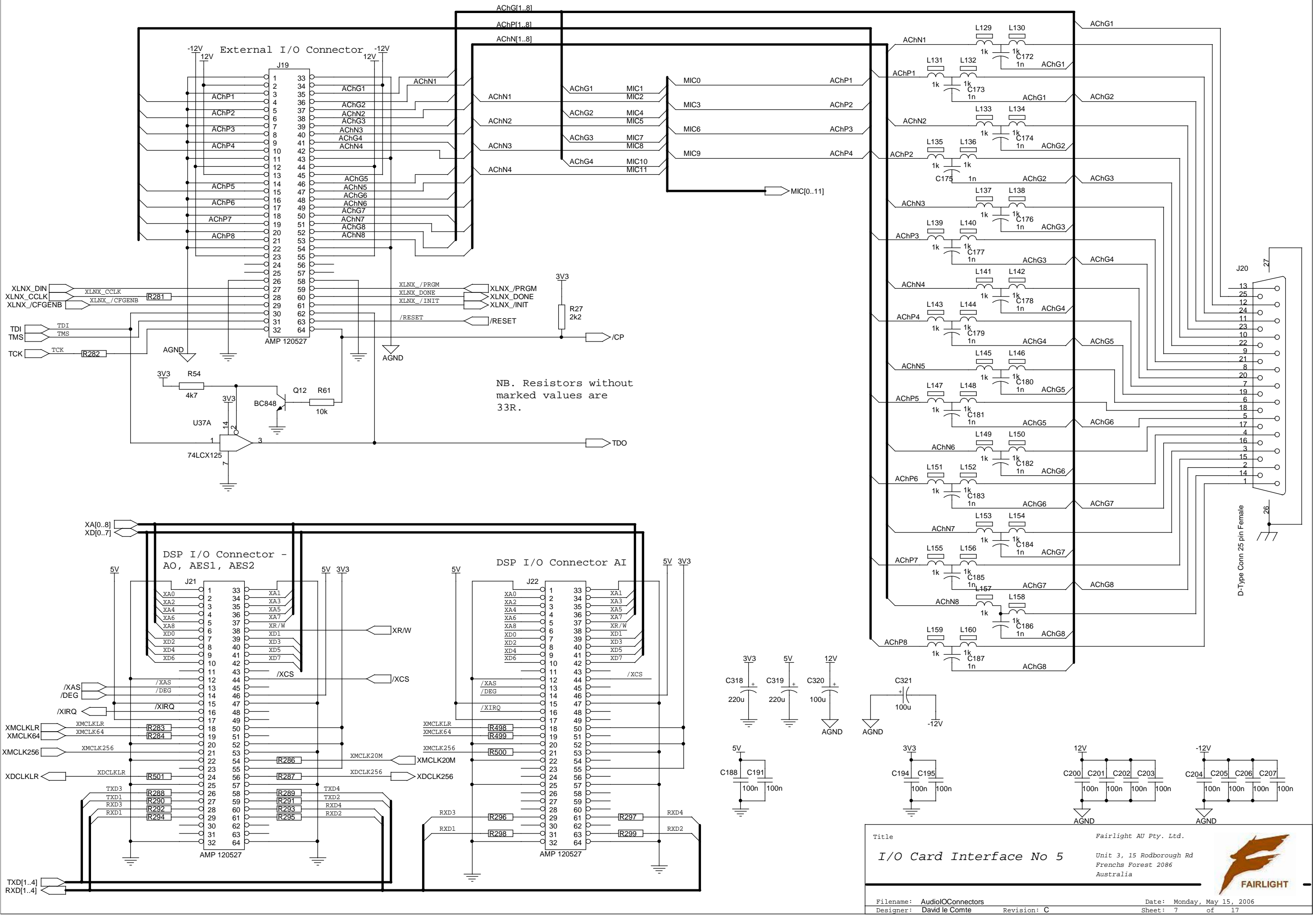
Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia

Filename: AudioIOConnectors
 Designer: David le Comte

Date: Monday, May 15, 2006
 Revision: C
 Sheet: 5 of 17






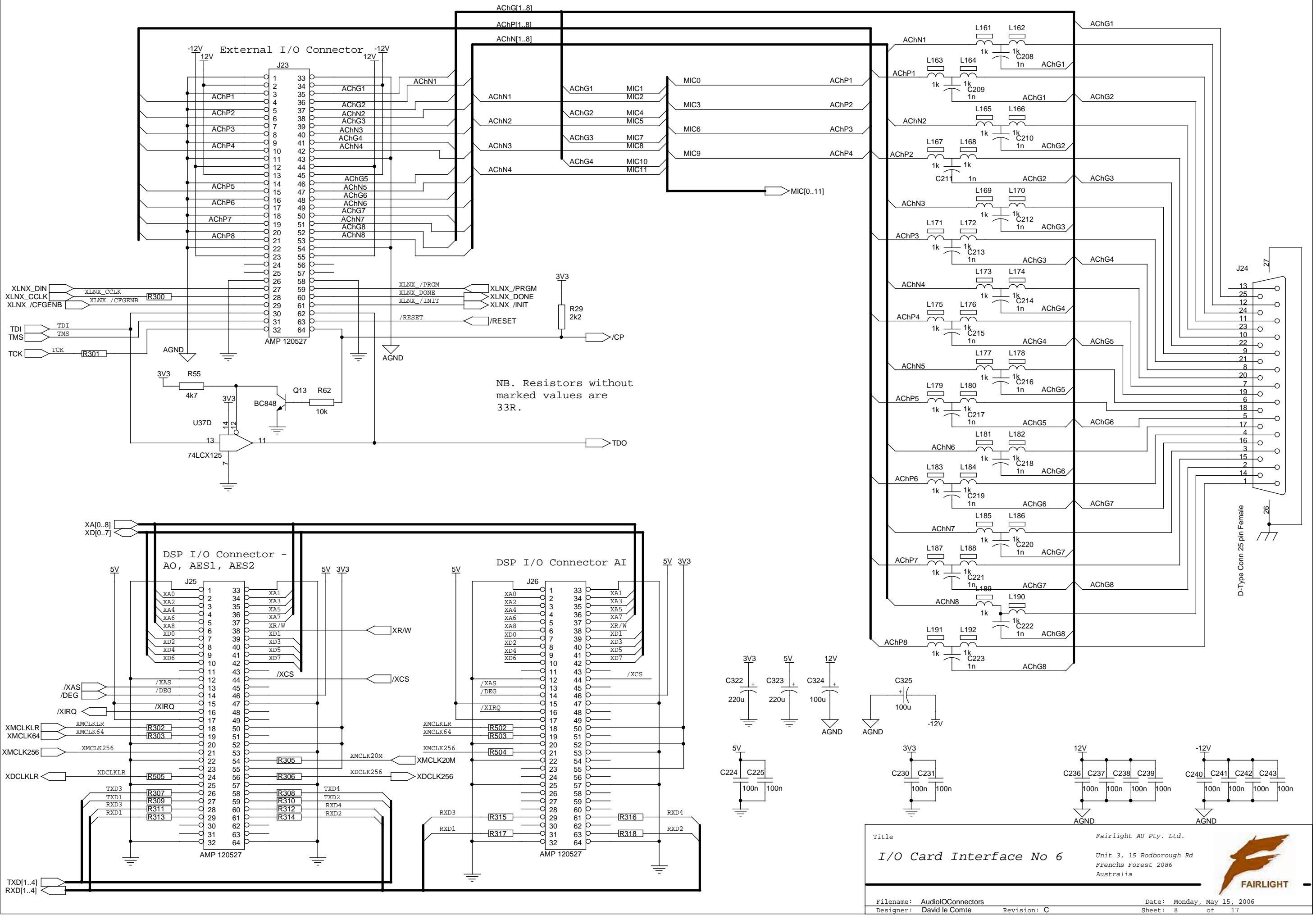


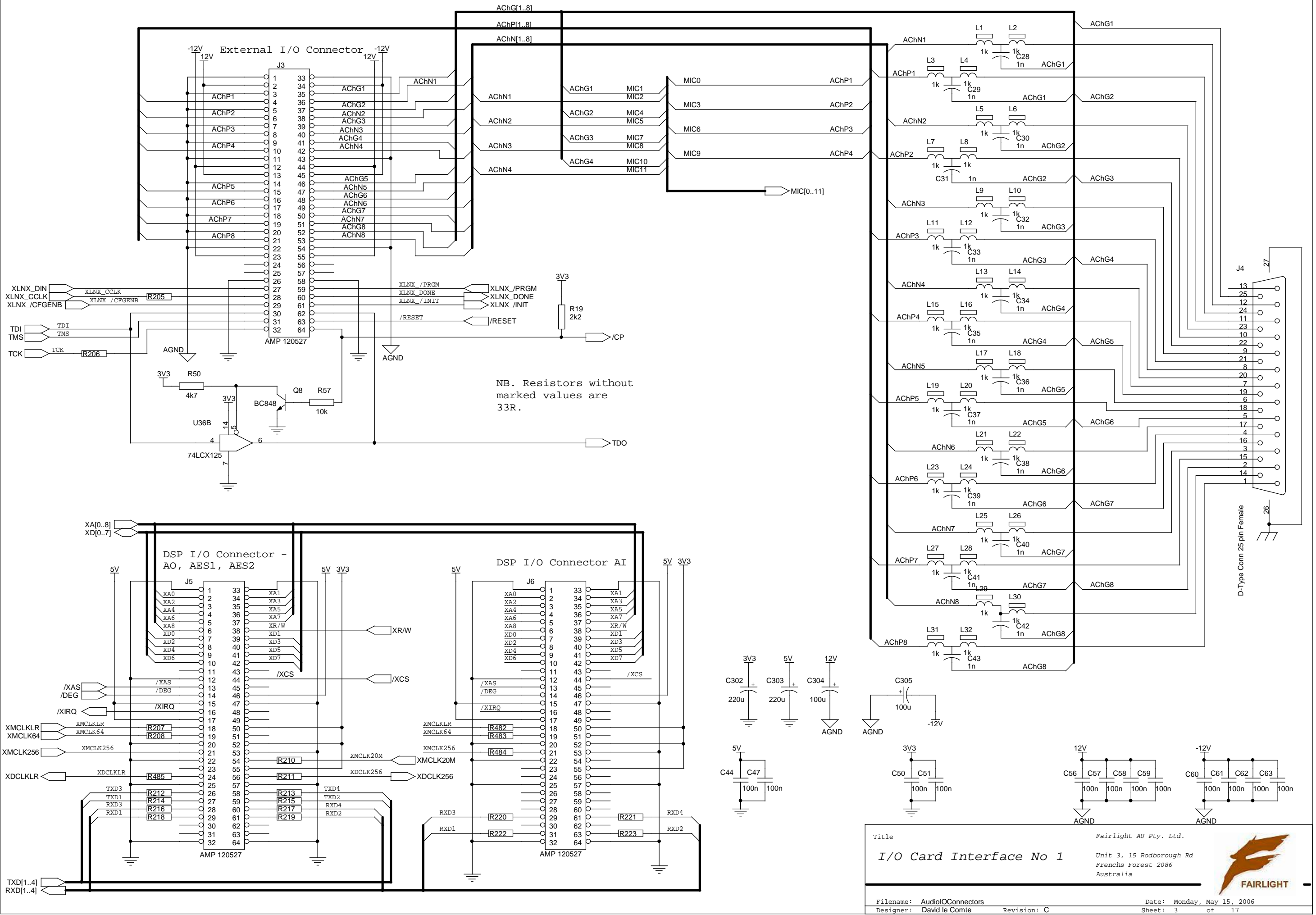
Title: I/O Card Interface No 5

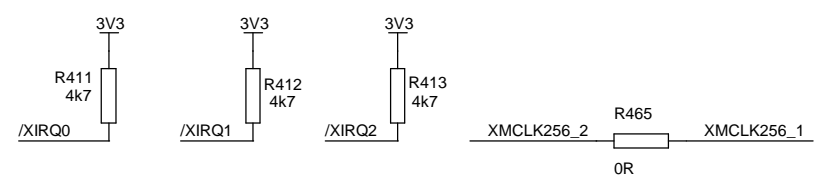
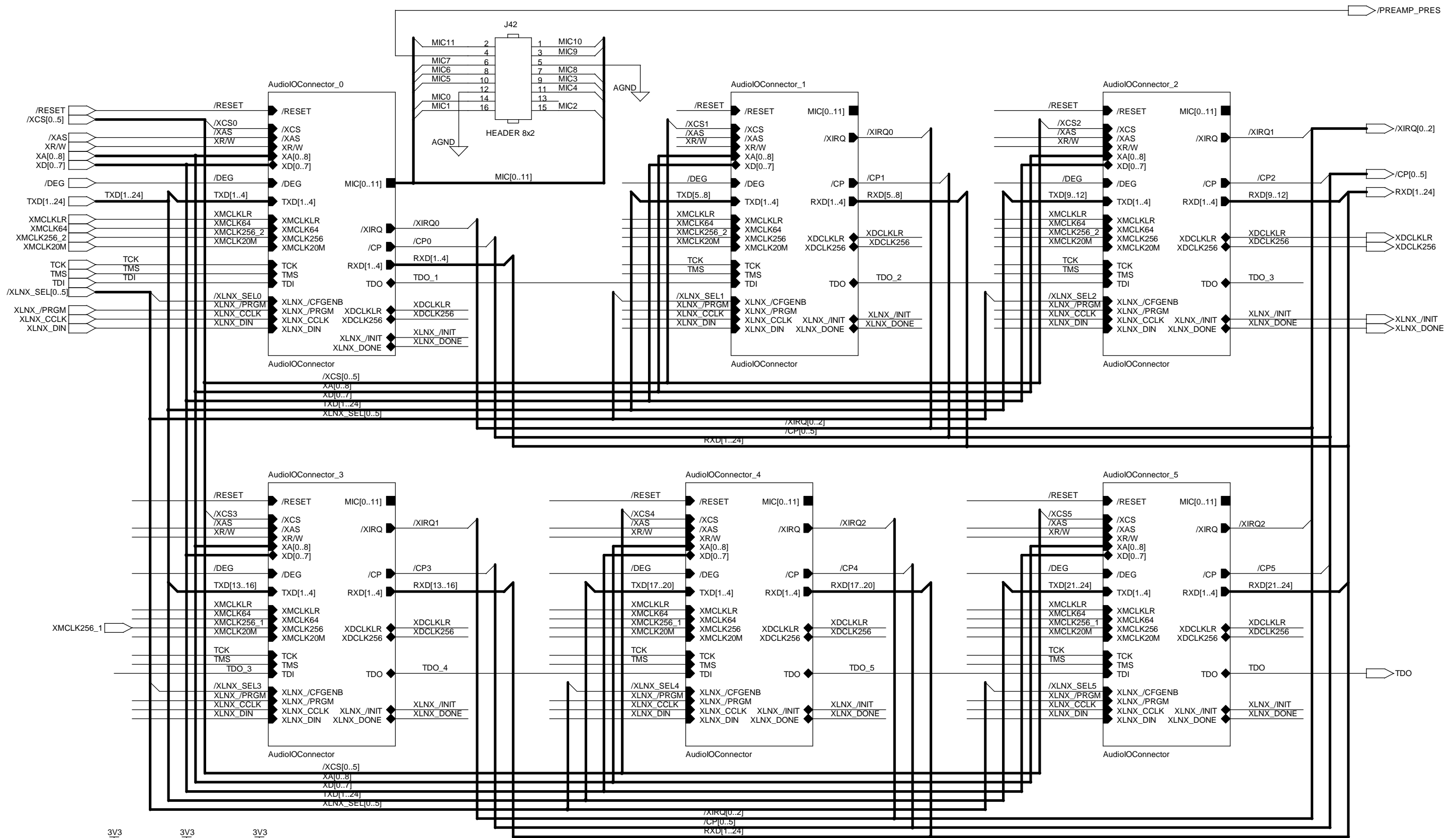
Fairlight AU Pty. Ltd.
 Unit 3, 15 Rodborough Rd
 Frenchs Forest 2086
 Australia

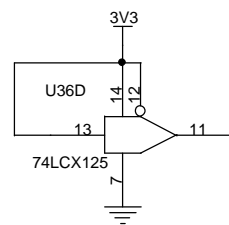


Filename: AudioIOConnectors
 Designer: David le Comte
 Date: Monday, May 15, 2006
 Revision: C
 Sheet: 7 of 17

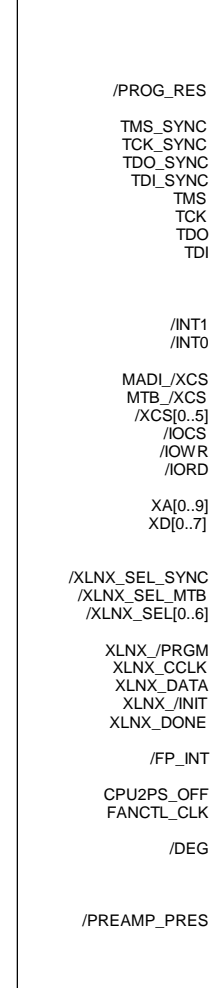




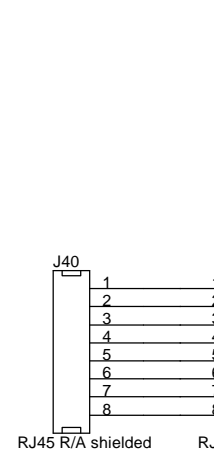




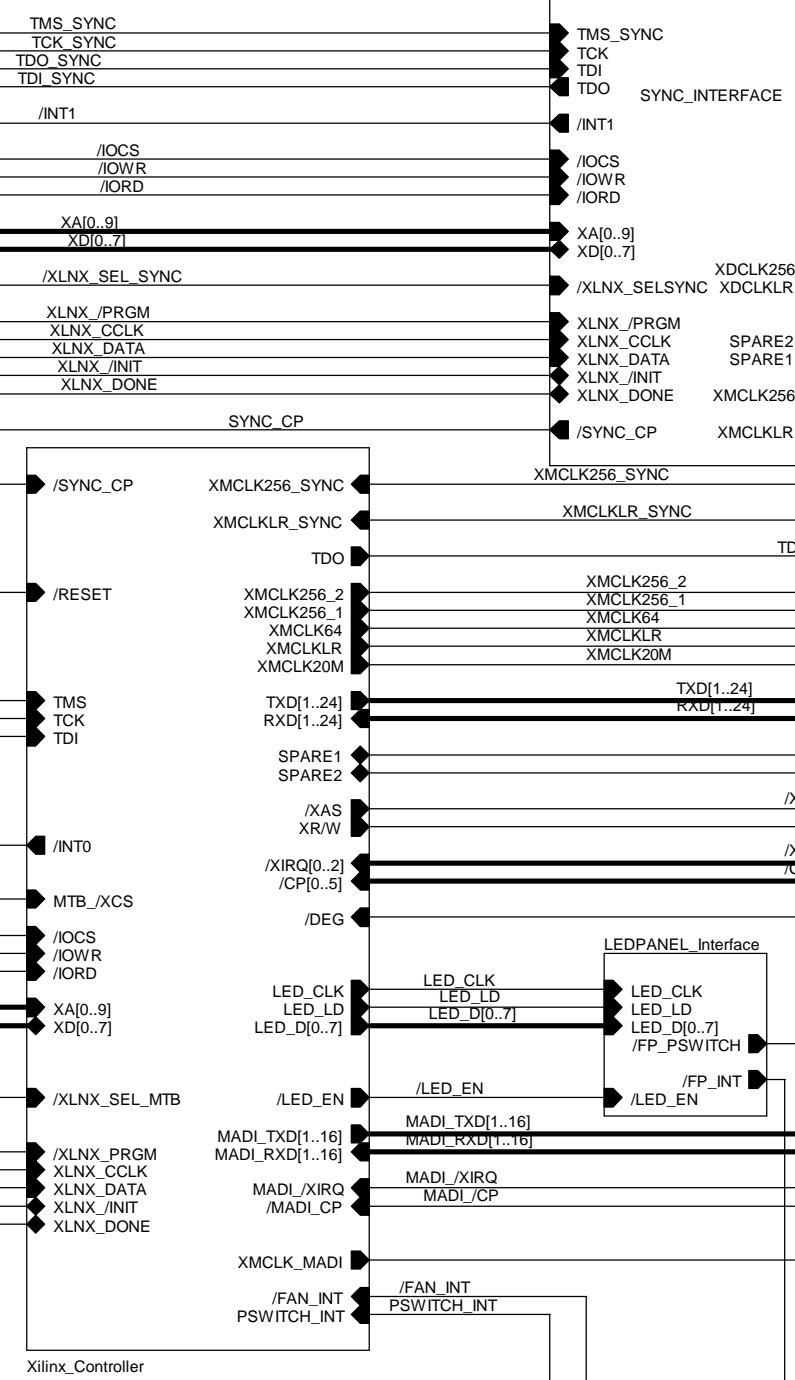
Rabbit Processor Interface



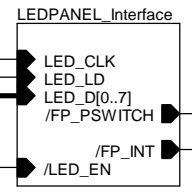
Rabbit Interface



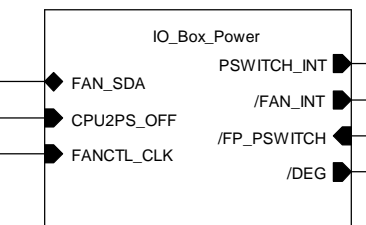
RJ45 R/A shielded RJ45 R/A Shielded



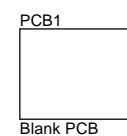
Xilinx_Controller



LEDPANEL_Interface

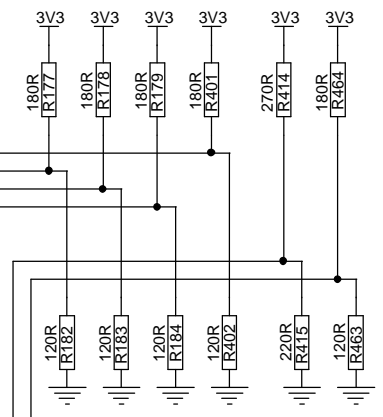


IO_Box_Power

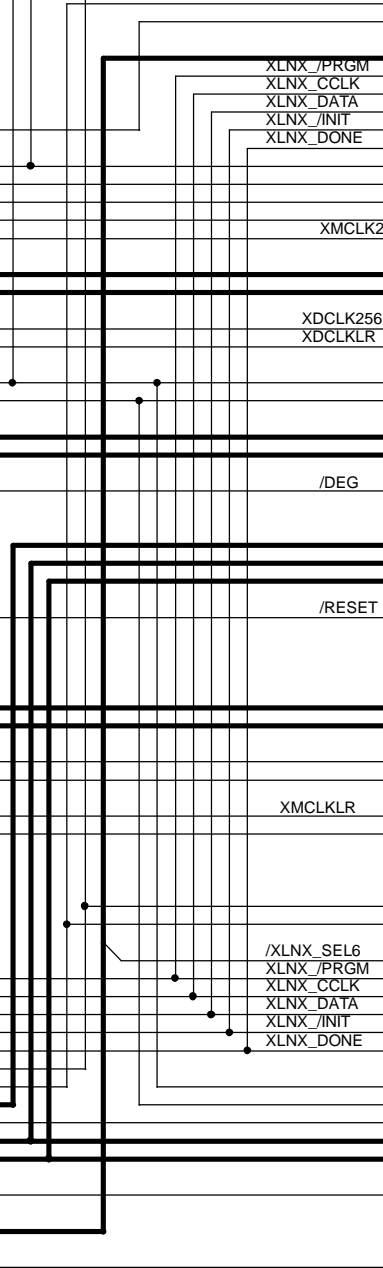


PCB1

Blank PCB



AudioIOConnectors



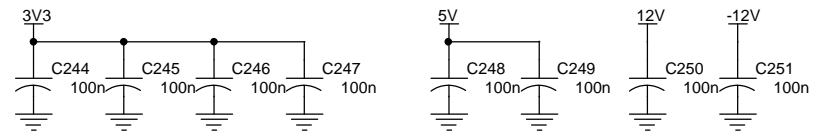
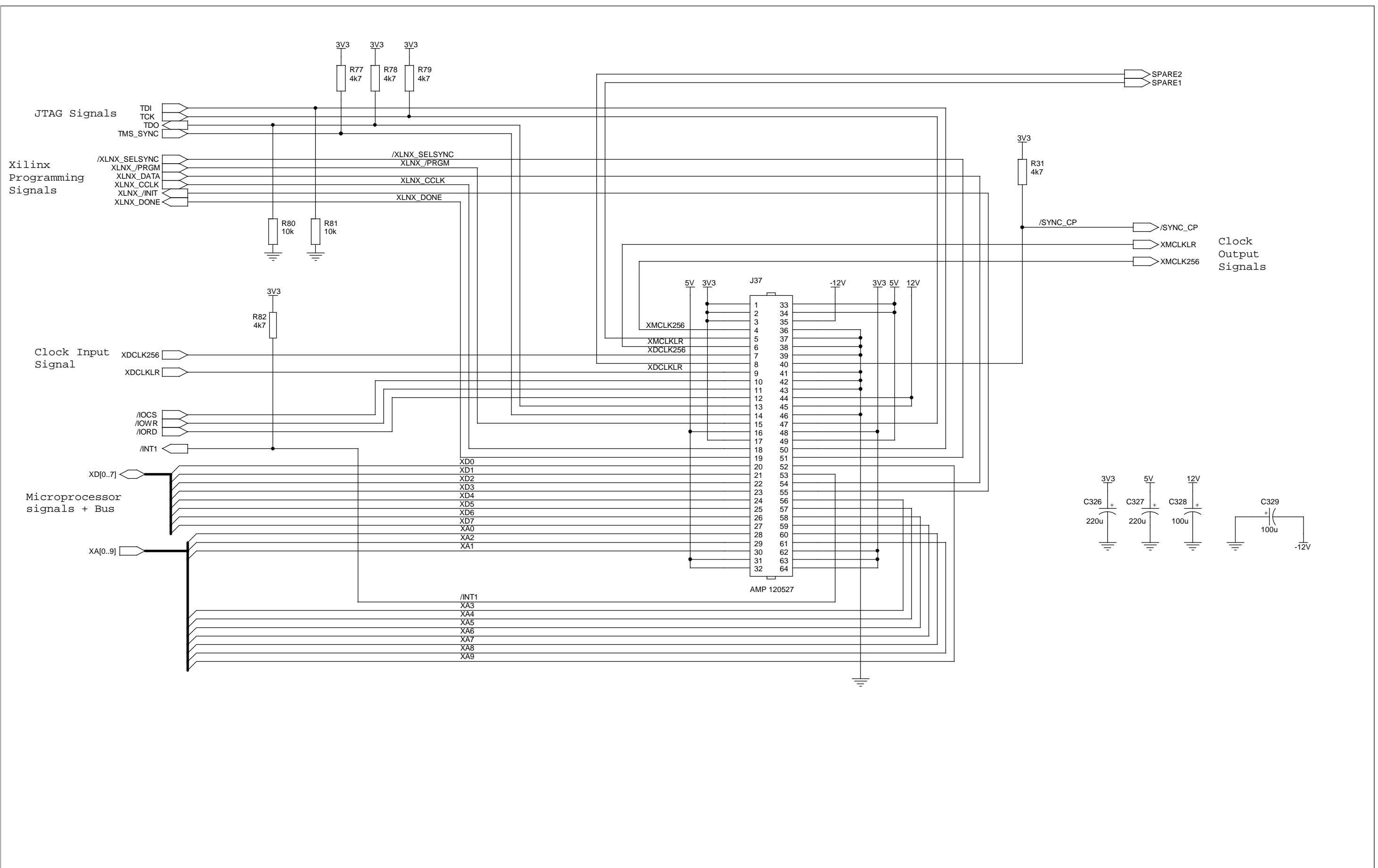
MADI_Interface

Title: *IO Motherboard - Main Sheet*


Fairlight AU Pty. Ltd.
Unit 3, 15 Rodborough Rd
Frenchs Forest 2086
Australia

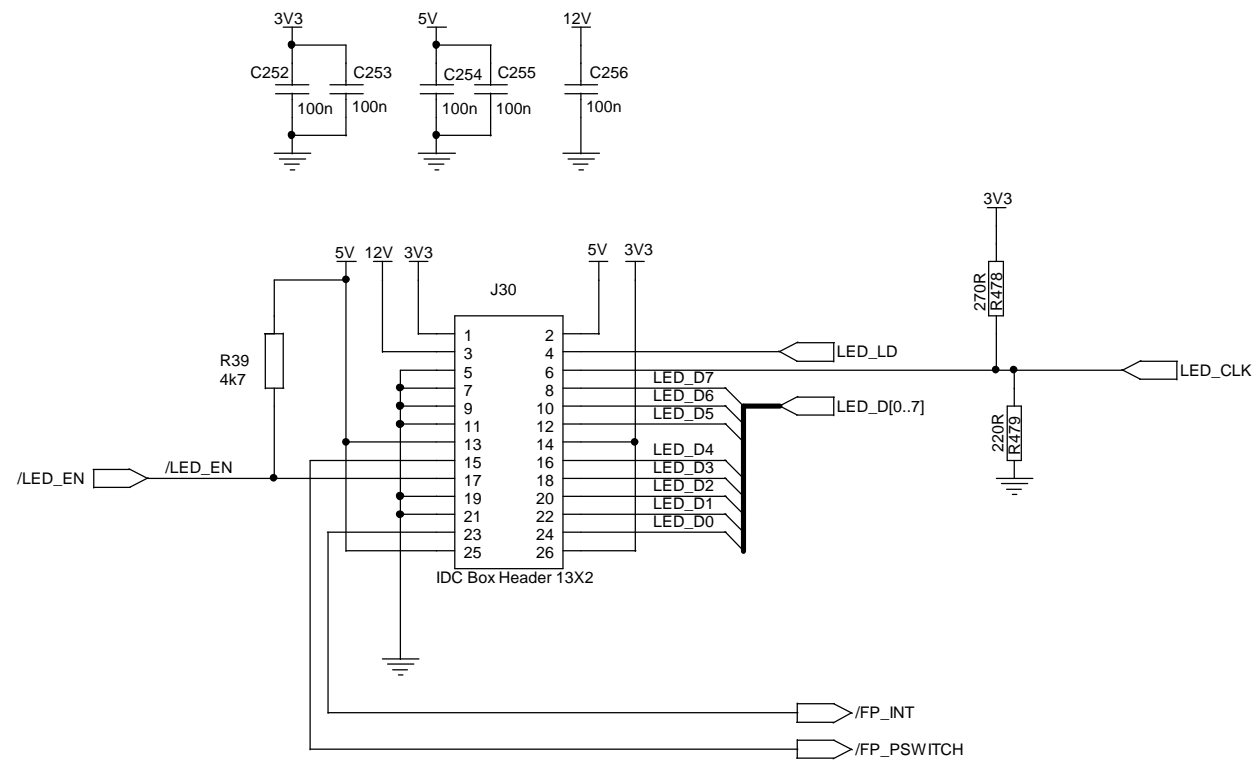
Filename: Motherboard
Designer: David le Comte

Date: Tuesday, May 16, 2006
Revision: C
Sheet: 1 of 17



| | | | |
|---------------------------------|--|--|--|
| Title | | Fairlight AU Pty. Ltd. | |
| Sync Board Interface | | Unit 3, 15 Rodborough Rd Frenchs Forest 2086 Australia | |
| Filename: <schematic file name> | | Date: Tuesday, May 16, 2006 | |
| Designer: David le Comte | | Revision: C | |
| | | Sheet: 11 of 17 | |







ENGINEERING CHANGE NOTE:

ECN #: 1034

ASSEMBLY: BIOB101B-A

NEW ASSEMBLY: BIOB101C-A

CURRENT REV: B

NEW REV: C

DESCRIPTION: SX48 Motherboard

DATE: 16/05/2006

Updating PCBs for RoHS compliance. Incorporates ECNs 1026, 1027, and 1028

REASON FOR CHANGE:

- | | | | |
|---|--|--|--------------------------------------|
| <input type="radio"/> Safety Issue | <input type="radio"/> UL Compliance Issue | <input checked="" type="radio"/> New Feature | <input type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix | <input type="radio"/> EMC Compliance Issue | <input checked="" type="radio"/> Quality Issue | |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change | <input type="radio"/> Cosmetic Change | |

PCB silkscreen changed to include RoHS Text, Solder Mask changed to Blue, Plating changed to Au/Ni, and material to FR4-08

Incorporate ECNS 1026, 1027, and 1028

Improve assembly process with larger notches on rear corners

increase pad sizes on U28 (M25P40) to accomodate 8Mbit or larger serial flash devices

COMPATIBILITY MAINTAINED: Yes

DETAILS OF CHANGE:

Update PCB part # from BIOB101B to BIOB101C. Order new top side solder paste stencil

Add new 4k7 resistor R507. Increase Qty of 4k7 0603 resistors (RMD7948) by 1

Add new 100pF capacitor C333. Increase Qty of 100pF 0603 capacitors (CCD1210) by 1

SIGNATURES:

Engineer:

Implementor:

Operations Manager:

Date:



ENGINEERING CHANGE NOTE:

ECN #: 1035

ASSEMBLY: BIOB111B-A**NEW ASSEMBLY:** BIOB111C-A**CURRENT REV:** B**NEW REV:** C**DESCRIPTION:** SX48 LED Board**DATE:** 16/05/2006

Updating PCBs for ROHS compliance.

REASON FOR CHANGE:

- | | | | |
|---|--|--|--------------------------------------|
| <input type="radio"/> Safety Issue | <input type="radio"/> UL Compliance Issue | <input checked="" type="radio"/> New Feature | <input type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix | <input type="radio"/> EMC Compliance Issue | <input checked="" type="radio"/> Quality Issue | |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change | <input type="radio"/> Cosmetic Change | |

PCB silkscreen changed to include RoHS Text, Solder Mask made blue, plating changed to Au/Ni, and material to FR4-08

Move tracks from near front plate mounting screws

COMPATIBILITY MAINTAINED: Yes**DETAILS OF CHANGE:**

Update PCB part# from BIOB111B to BIOB111C.

SIGNATURES:**Engineer:****Implementor:****Operations Manager:****Date:**



ENGINEERING CHANGE NOTE:

ECN #: 1036

ASSEMBLY: BIOB121B-A**NEW ASSEMBLY:** BIOB121C-A**CURRENT REV:** B**NEW REV:** C**DESCRIPTION:** SX48 Sync board**DATE:** 16/05/2006

Updating PCBs for RoHS compliance. Incorporates ECNs 1029, and 1030

REASON FOR CHANGE:

- | | | | |
|---|--|--|--------------------------------------|
| <input type="radio"/> Safety Issue | <input type="radio"/> UL Compliance Issue | <input checked="" type="radio"/> New Feature | <input type="radio"/> Cost Reduction |
| <input type="radio"/> Bug Fix | <input type="radio"/> EMC Compliance Issue | <input checked="" type="radio"/> Quality Issue | |
| <input type="radio"/> Reliability Reasons | <input type="radio"/> Software Change | <input type="radio"/> Cosmetic Change | |

PCB silkscreen changed to include RoHS Text, Solder Mask changed to Blue, Plating changed to Au/Ni, and material to FR4-08

Incorporate ECNS 1029, and 1030

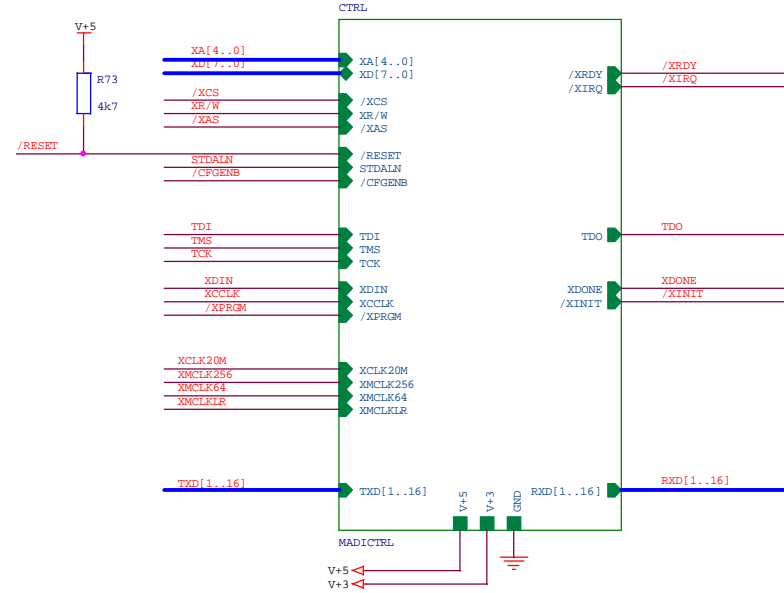
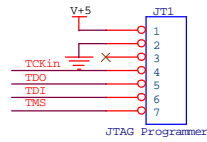
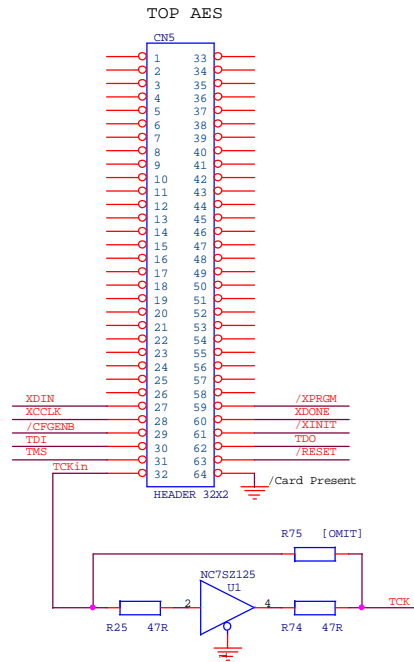
COMPATIBILITY MAINTAINED: Yes**DETAILS OF CHANGE:**

Update PCB part # from BIOB101B to BIOB101C. Order new top side solder paste stencil

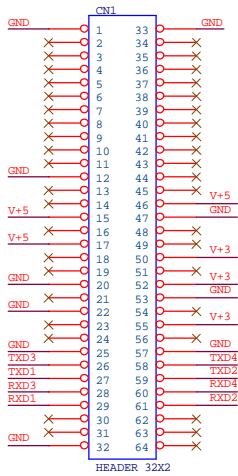
Add new part C142, 100pF, 0603 capacitor (CCD1210)

Change C89 from an 0603, 15pF capacitor (CCD7587) to a 1nF, 0603 capacitor (CCD1216)

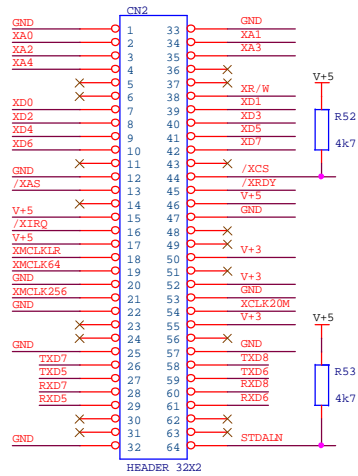
SIGNATURES:**Engineer:****Implementor:****Date:****Operations Manager:**



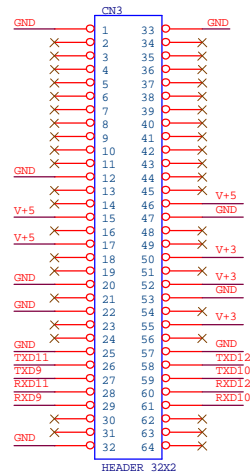
DSP I/O Slot 2
TOP AOUT



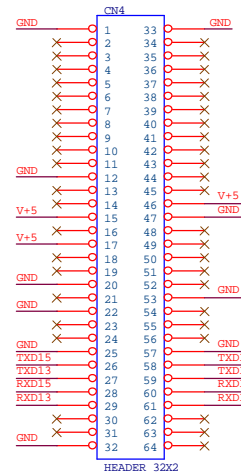
DSP I/O Slot 3
TOP AES



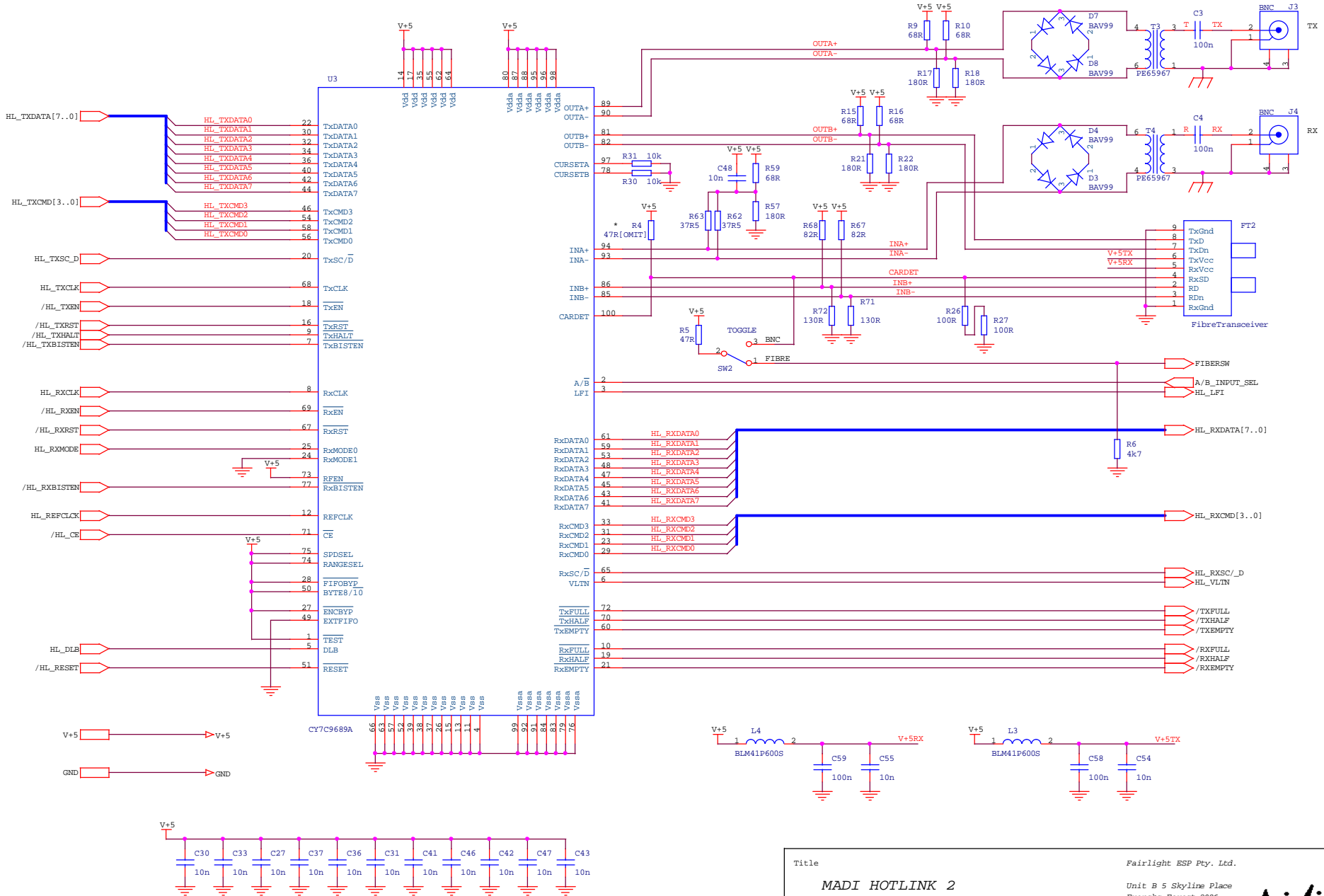
DSP I/O Slot 4
BOTTOM AES



DSP I/O Slot 5
BOTTOM AOUT

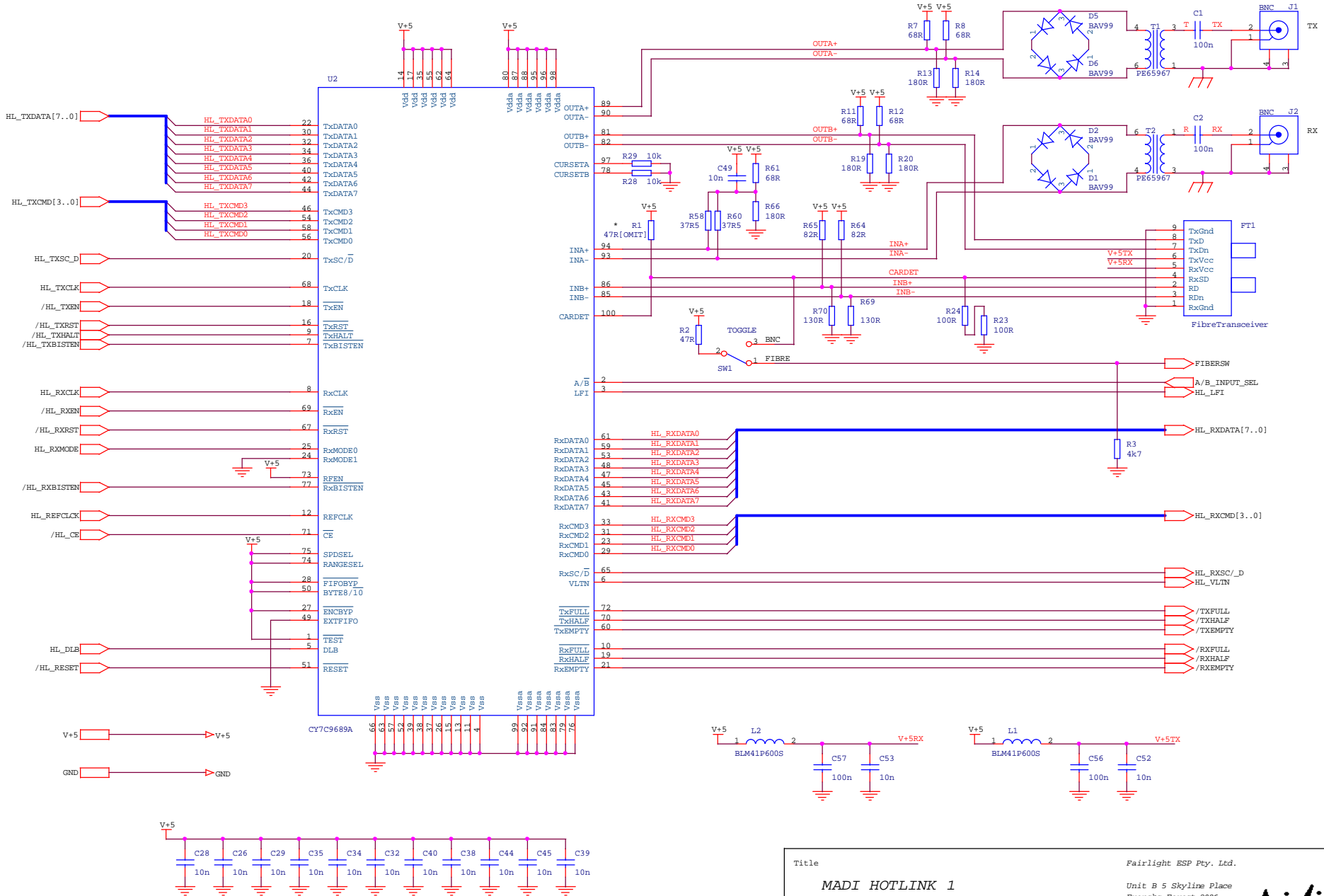


* Only Fit if switch not loaded

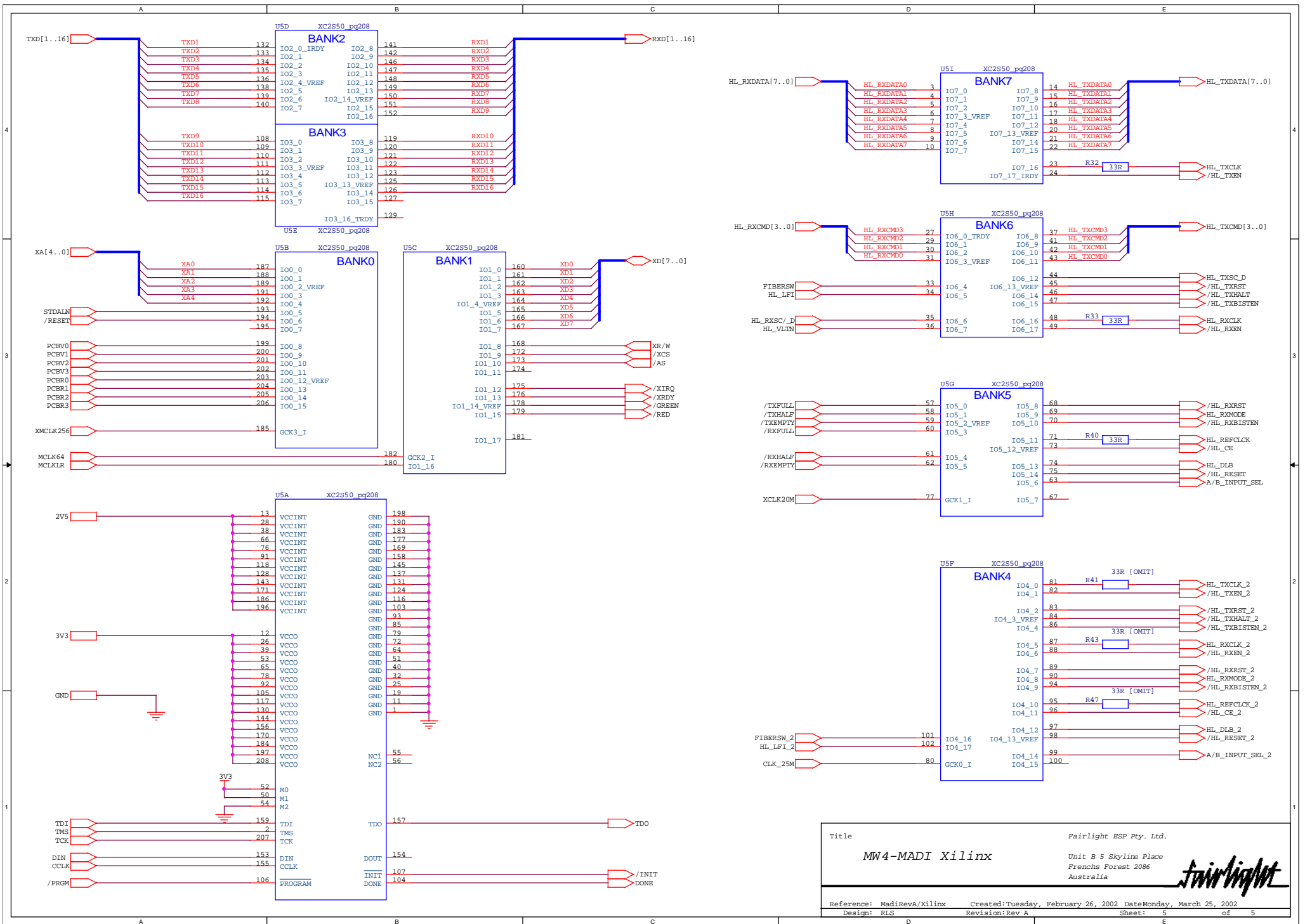


| | |
|------------------------------|--|
| Title | Fairlight ESP Pty. Ltd. |
| MADI HOTLINK 2 | Unit B 5 Skyline Place Frenchs Forest 2086 Australia |
| Reference: MadiRevA/HOTLINK2 | Created: Wednesday, February 18, 1998 |
| Design: RLS | Date: Thursday, March 21, 2002 |
| Revision: Rev A | Sheet: 4 of 5 |

* Only Fit if switch not loaded



| | |
|------------------------------|--|
| Title | Fairlight ESP Pty. Ltd. |
| MADI HOTLINK 1 | Unit B 5 Skyline Place Frenchs Forest 2086 Australia |
| Reference: MadiRevA/HOTLINK1 | Created: Wednesday, February 18, 1998 |
| Design: RLS | Date: Thursday, March 21, 2002 |
| Revision: Rev A | Sheet: 3 of 5 |



| | |
|------------------|--|
| Title | Fairlight ESP Pty. Ltd. |
| MW4-MADI Xilinx | Unit B 5 Skyline Place Frenchs Forest 2086 Australia |
| <i>Fairlight</i> | |

